



# Super Six

The World's Most Powerful  
High Speed 8-bit Single Board Computer

**Advanced Digital's** high speed (6 MHz), single board S-100 based Super-Six computer is the most powerful 8-bit computer system available today. It increases the speed and performance by up to 1½ times faster than other systems currently available. A complete computer on a single board, the Super-Six enhances system expansion by making available at least one extra slot on the motherboard.

With 128KBytes of bank selectable RAM with parity as standard, the Super-Six is the only single board, S-100 computer that will run CP/M 3.0 (plus) in the banked mode and TurboDOS.

#### Features

- IEEE 696, S-100 Compatible
- 6MHz, Z-80B CPU
- 6MHz, Z-80B PIO, SIO  
(two RS-232 Serial, two Parallel)
- 128KBytes bank selectable RAM with parity
- 2K/4K monitor EPROM
- Floppy disk controller with simultaneous 8" and 5¼" drive operation
- Z-80 DMA controller
- TurboDOS™, CP/M® 2.2, CP/M3.0 (plus), OASIS and NETWORK-OS operating systems.

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CORPORATION**



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## The Most Powerful High Speed 8-bit Single Board Computer Available Today

### **Z-80B CPU:**

Runs at 6MHz with option to run at 4MHz

### **Floppy Controller:**

Uses WD2793 floppy disk controller and supports combination of 8" and 5¼" floppy drive in single or double density (sided) formats. Because of this feature, the problem of incompatible 5¼" format no longer exists. It's fully interrupt driven.

### **128K RAM:**

Uses 4164 dynamic RAM chips. It's designed with 16K banks to be selected or deselected. Control port #0 controls the first 64K bytes and control port #1 controls the second 64K bytes.

### **Z-80B, SIO, PIO:**

Uses the Z-80B PIO and Z-80B Dart (SIO). Total of 2 - RS232C serial I/O with software select baud rates (and hardware switch) to set an initial default value and 2 - Parallel ports (centronics). Since TTL level logic is provided on the board, options of RS422, RS232, RS499 could be supported by an adapter card.

One serial adapter is provided with every board for asynchronous operation.

### **2K/4K Monitor:**

A 2716 EPROM monitor is supplied with every SUPER SIX. The monitor features a single cold boot routine, memory fill and DUMP, print, Move, I/O Read/Write, Execute Address. The Monitor EPROM occupies address F000-FFFFH, the EPROM is switched on automatically during reset or power on, it contains the SIO and FDC initialization code. After the operating system is loaded, the EPROM is turned off.

### **Z-80B CTC:**

A Z-80B CTC is used for interrupts and real time clock at 6MHz.

### **Operating System:**

CP/M® 2.2, CP/M® 3.0, and TurboDOS™ are directly supported. With 128K of RAM on board, CP/M® 3.0 becomes more advantageous. A free copy of CP/M® 2.2 bios will be supplied.

### **Z-80 DMA:**

Uses Z-80 DMA controller. The DMA controller will transfer between two ports (sources and destination), i.e., memory to I/O, I/O to memory, I/O to I/O, and memory to memory. It also will do bit maskable byte searches along with data transfers or by itself, and it supports the daisy chaining of an external DMA controller.



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