

TECHNICAL ORDER
NSP SYSTEM OPERATION AND MAINTENANCE MANUAL

VOLUME 2 OF 3
NSP TEKTRONIX 4010 DATA DISPLAY TERMINAL USER'S MANUAL
Volume 2 Part Number: 901181-116NC

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SAFETY SUMMARY

The following are general safety precautions that are not related to any specific procedures and therefore do not appear elsewhere in this publication. These are recommended precautions that personnel must understand and apply during many phases of operation and maintenance.

KEEP AWAY FROM LIVE CIRCUITS

Operating personnel must at all times observe all safety regulations. Do not replace components or make adjustments inside the equipment with the high voltage supply turned on. Under certain conditions, dangerous potentials may exist when the power control is in the off position, due to charges retained by capacitors. To avoid casualties, always remove power and discharge and ground a circuit before touching it.

DO NOT SERVICE OR ADJUST ALONE

Under no circumstances should any person reach into the enclosure for the purpose of servicing or adjusting the equipment except in the presence of someone who is capable of rendering aid.

RESUSCITATION

Personnel working with or near high voltages should be familiar with methods of cardio pulmonary resuscitation (CPR).

WARNING

High voltages capable of causing death are used in this equipment. Use extreme caution when servicing either the power supplies or their load components.

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SECTION 1

INTRODUCTION

1.1 MANUAL SCOPE

This manual (to be used in conjunction with the NSP Operation and Maintenance Manual P/N 901181-100) describes the optional Tektronix 4010 computer display terminal. It is divided into seven sections, with each section containing the particular information described below.

- Section 1 INTRODUCTION--This section contains a brief description of the data display terminal and its application to the NSP system.
- Section 2 INSTALLATION--This section describes the installation procedures.
- Section 3 OPERATION--This section describes the NSP system dependent operation of the data display terminal.
- Section 4 THEORY OF OPERATION--This section contains the theory of operation of the data interface circuit card.
- Section 5 MAINTENANCE--This section contains the performance check procedure used to evaluate the overall operation of the data display terminal with the NSP system and a basic troubleshooting guide.
- Section 6 PARTS LIST--This section contains a parts list of the E&S items used with the data display terminal.

Section 7 DRAWINGS--This section contains the engineering drawings of the E&S items used with the data display terminal.

1.2 BASIC DESCRIPTION

The Tektronix 4010 data display terminal consists of a pedestal mounted unit that contains a standard ASCII keyboard and a display storage CRT and related circuits. The pedestal contains the power supply, control and optional circuits. The data interface card (P/N 200109-100) is located in the pedestal.

The data display terminal interfaces between man and the TI 980B computer by presenting inputs through the standard ASCII keyboard, and providing a display (alphanumeric or graphic) of computer output data. The data interface card performs the buffer/interface functions required between the data display terminal and the data module in the computer.

Operationally the data display terminal is used primarily as a display device for light and surface module data bases during modeling (NSPBLD). It presents an X and Y axis display (no vertical information) of the module data base and facilitates building, editing and changing. Its display is controlled by commands within the NSPBLD program software.

SECTION 2

INSTALLATION

2.1 UNPACKING

Carefully unpack all of the equipment and, before discarding the packing material, determine that the data display terminal is complete. Remove all materials used to secure parts during shipment. Although the equipment has been carefully packaged at the factory prior to shipment to avoid possible damage, all parts should be thoroughly inspected upon receipt for scratches, dents, broken connectors and damaged cables.

If damage to the equipment is discovered, notify the carrier of the damage and arrange to have the shipment inspected by the carrier's agent. Inform the carrier of your intent to file a claim, and then immediately notify your Evans & Sutherland representative.

2.2 INSTALLATION

Basic installation requirements for the data display terminal are covered in the appropriate section of the maintenance manual for the unit. This information includes desk-top operation of the keyboard/display section.

2.3 CONNECTIONS

Figure 2-1 shows the location of the data interface card P/N 200109-100 and identifies the cables used to connect the data display terminal to input AC power, the TI 980B computer data module (DM), and an additional optional hard copy unit.

2.3.1 AC INPUT POWER

The AC input power cable of the data display terminal connects to the DISPLAY TERMINAL connector on the rear panel of the primary power control in cabinet 1 of the NSP system.

2.3.2 DATA CABLE

The data cable P/N 101373-030 shown on figure 2-2 connects the data interface card in the data display terminal pedestal to the DM in the TI 980B computer. Figure 2-3 is a wiring diagram for the data cable. Figure 2-4 shows the normal location of the DM in the TI 980B computer.

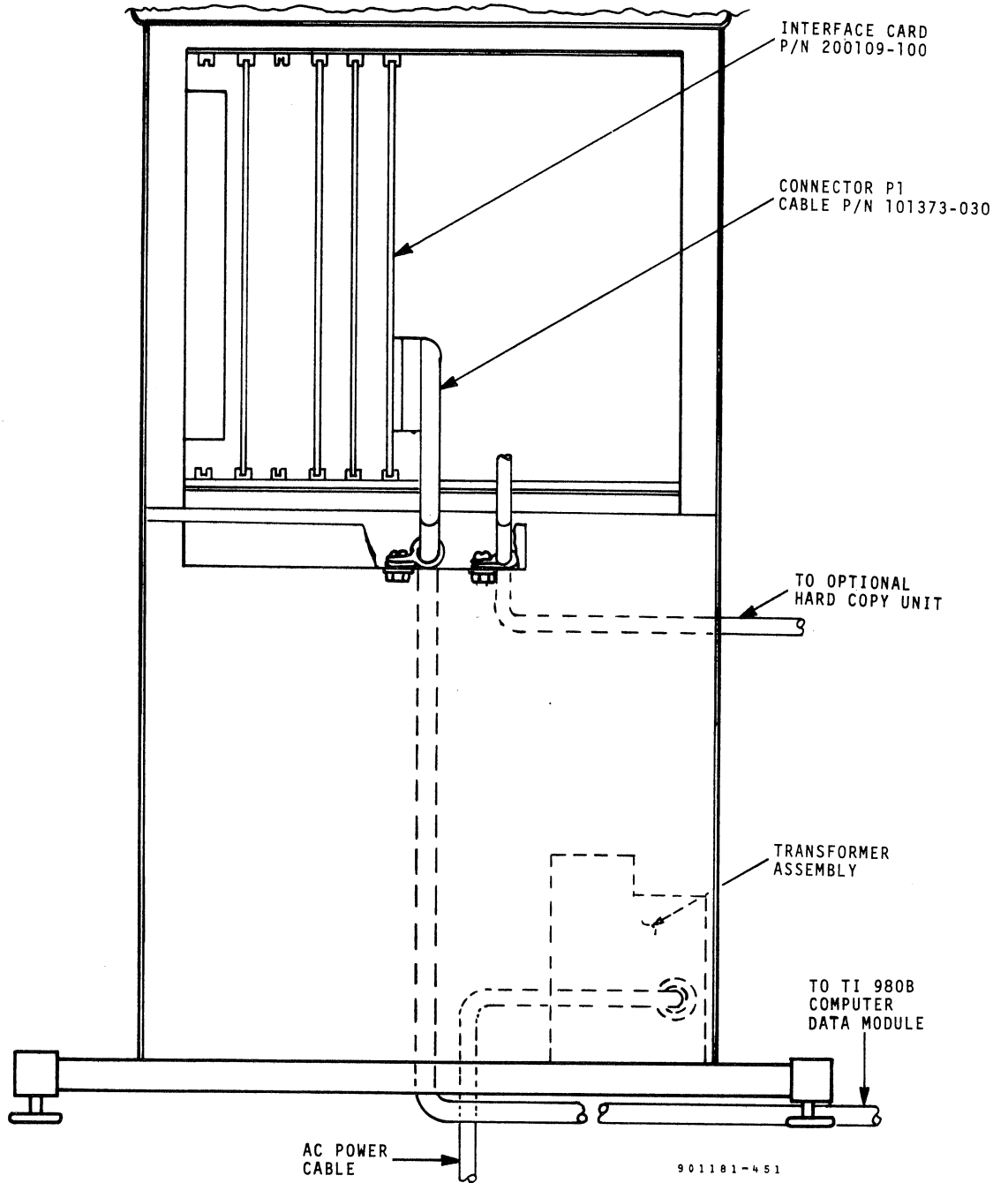
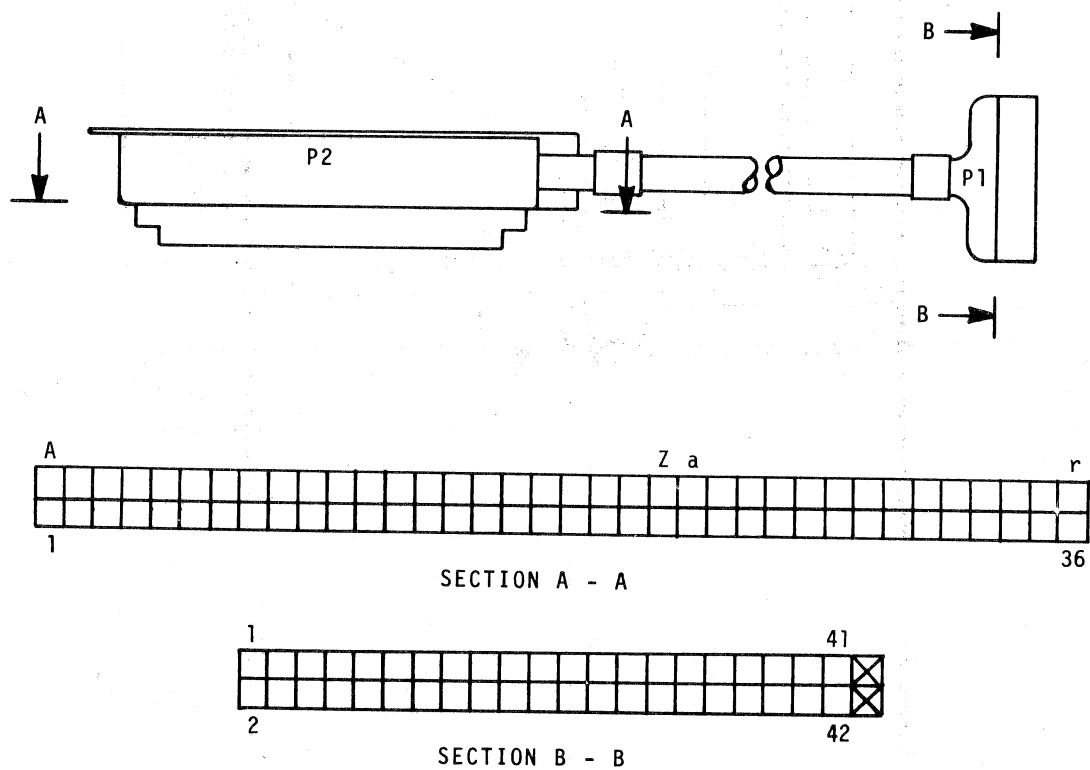
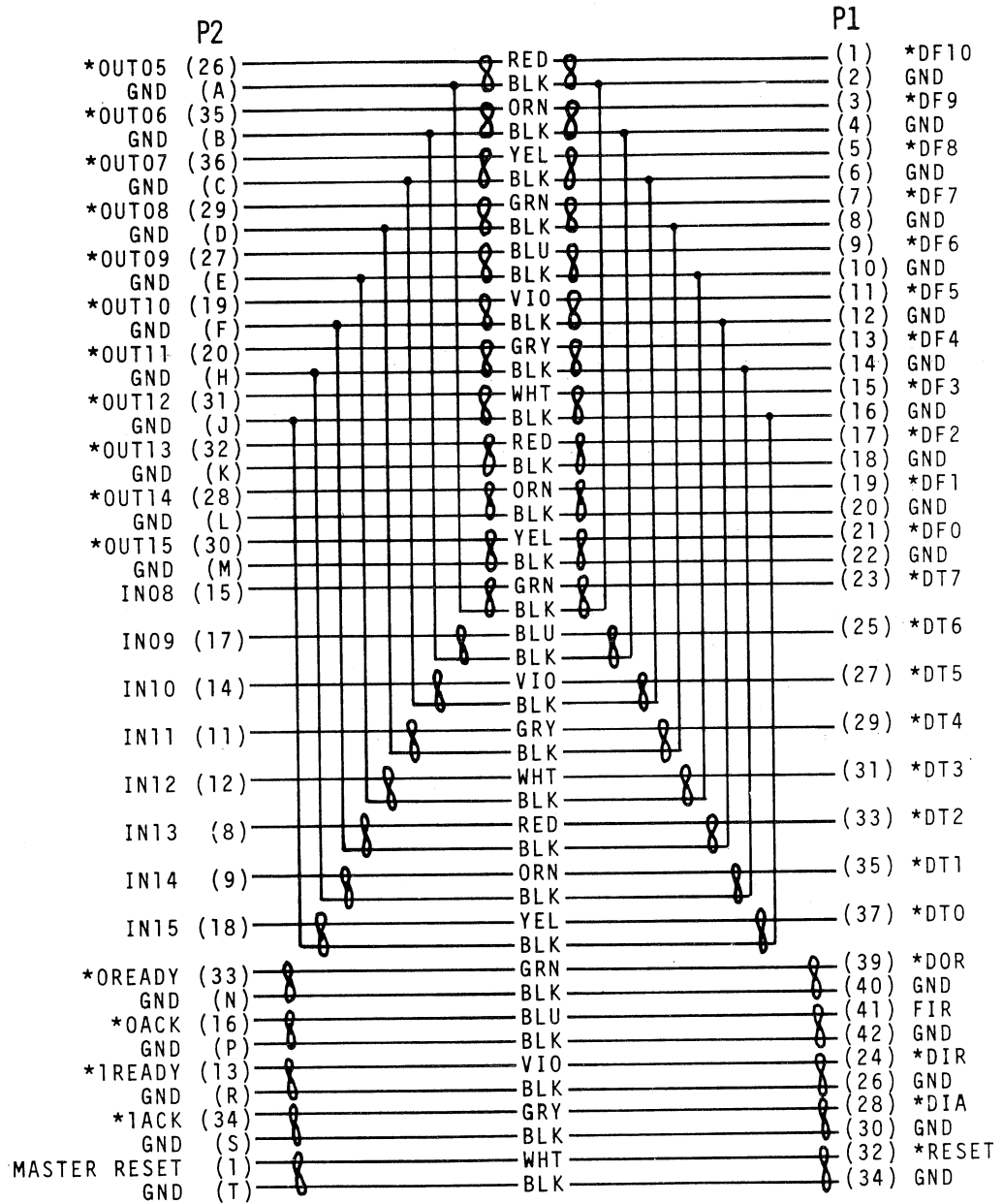


FIGURE 2-1
DATA DISPLAY TERMINAL PEDESTAL



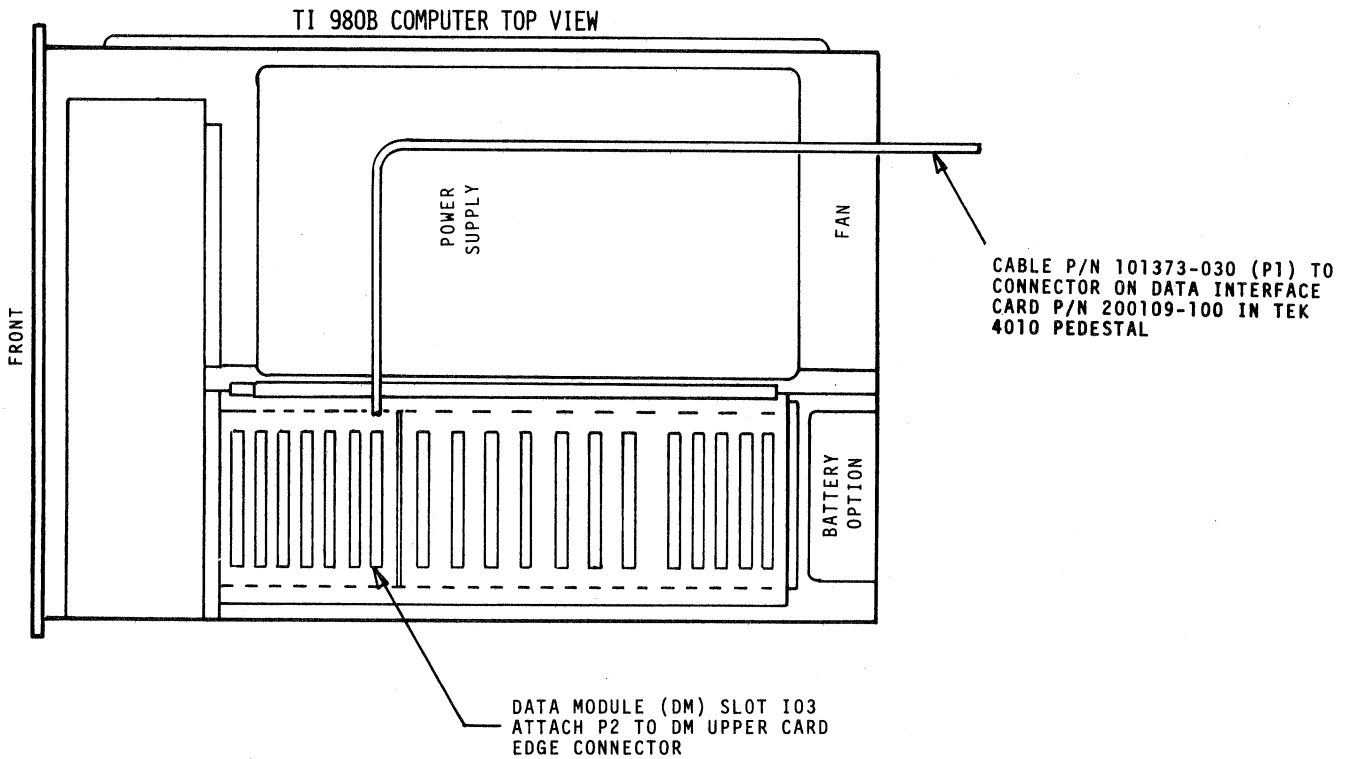
901181-452

FIGURE 2-2
DATA CABLE P/N 101373-030



901181-453

FIGURE 2-3
DATA CABLE P/N 101373-030
WIRING DIAGRAM



901181-454

FIGURE 2-4
DATA CABLE ROUTING (TI 980B)

SECTION 3

OPERATION

3.1 STARTUP PROCEDURE

The general procedures for turn-on and warm-up of the data display terminal are covered in the appropriate Tektronix instruction manual.

3.2 NORMAL OPERATION

All controls, their function and all operating modes of the data display terminal are identified and described in the appropriate Tektronix instruction manual.

3.3 NSP SYSTEM OPERATION

The data display terminal interfaces with the NSP system to perform the following functions during modeling.

- 1) Graphic display of selected modeling results.
- 2) Crosshair cursor for selection modeling scene content for parameter dump to the ASR 733 data terminal.

Figure 3-1 represents a partial flowchart for the NSP modeling program NSPBLD showing those dispatch points that apply to the data display terminal. As indicated on figure 3-1, four software commands in the light and surface modules of NSPBLD affect the data display terminal. Figure 3-2 shows the field of view (FOV) ground span with respect to the model coordinate system within the gaming area. Nadir is determined by the response to the X and Y prompts under the set observer position (V) command. The FOV ground span area is determined by the response to the SPAN prompt under the V command.

3.3.1 LIGHT MODULE PROMPTS

The following are examples of the data display terminal applicable light module prompts with annotation.

DISPLAY LIGHTS (LIT>>D; 4010 OPTION)

STR #1= R1 thru G110

STR #2= R1 thru G110

SET OBSERVER POSITION (LIT>>V; 4010 OPTION)

X= X position for observer

Y= Y position for observer

SPAN= ground span covered by display

FIND LIGHT STRING (LIT>>F; 4010 OPTION)

POS CROSS X = return to command selection. Any other printing character = search for light string.

BLANK SCREEN (LIT>>B; 4010 OPTION)

Selecting B at light module prompt will blank the 4010 screen.

Operator responses to the display lights and set observer position prompts are in the same format as in modeling, refer to Software User's Document P/N 901181-118.

3.3.2 SURFACE MODULE PROMPTS

The following are examples of the data display terminal applicable surface module prompts with annotation.

DISPLAY FACES (SUR>>D; 4010 OPTION)

ITEM #1=	F1	F64
ITEM #2=	F1	F64

SET OBSERVER POSITION (SUR>>V; 4010 OPTION)

Same as Set Observer position in light module.

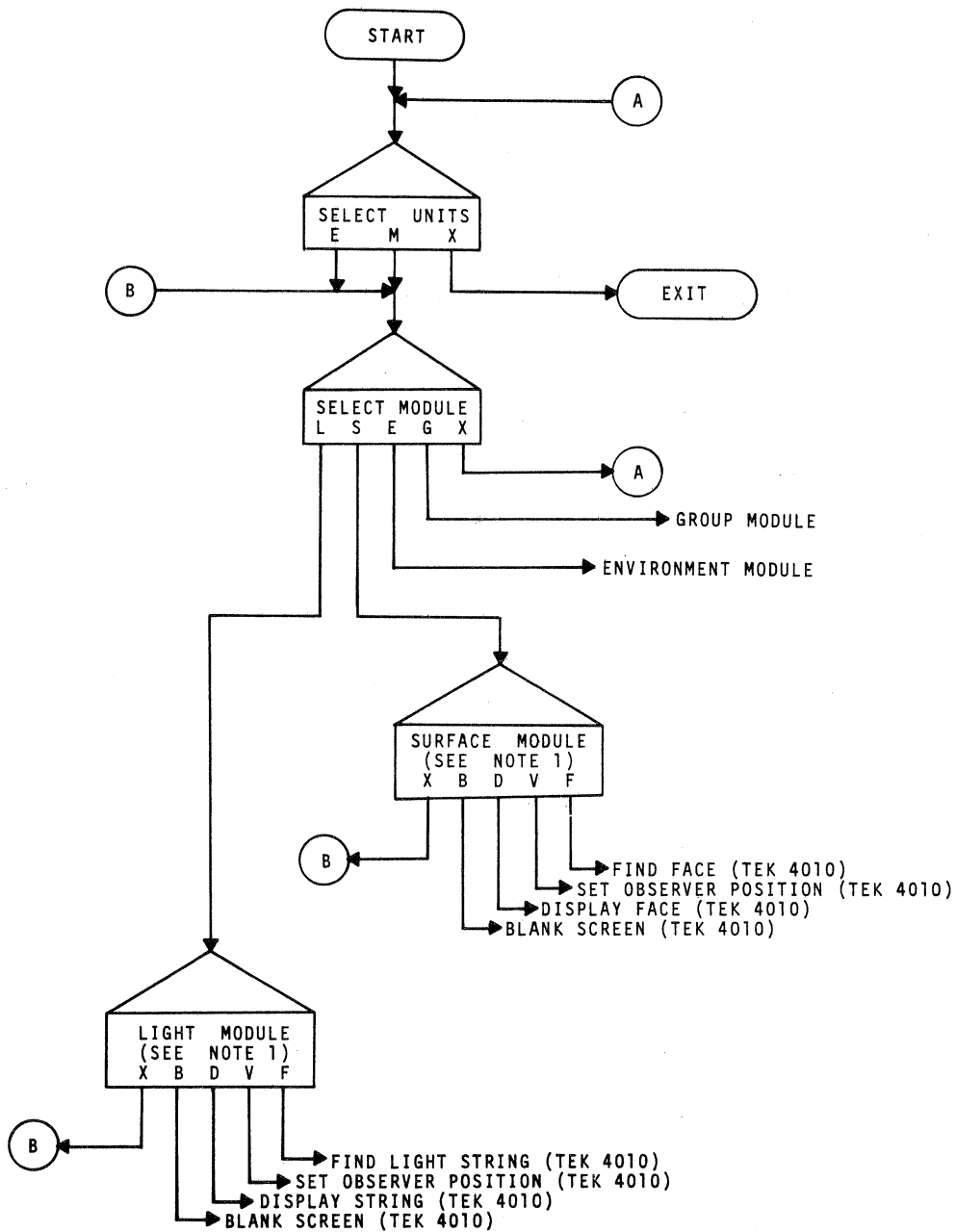
FIND FACE (SUR>>F; 4010 OPTION)

Same as Find Light String in light module.

BLANK SCREEN (LIT>>B; 4010 OPTION)

Selecting B at surface module prompt will blank 4010 screen.

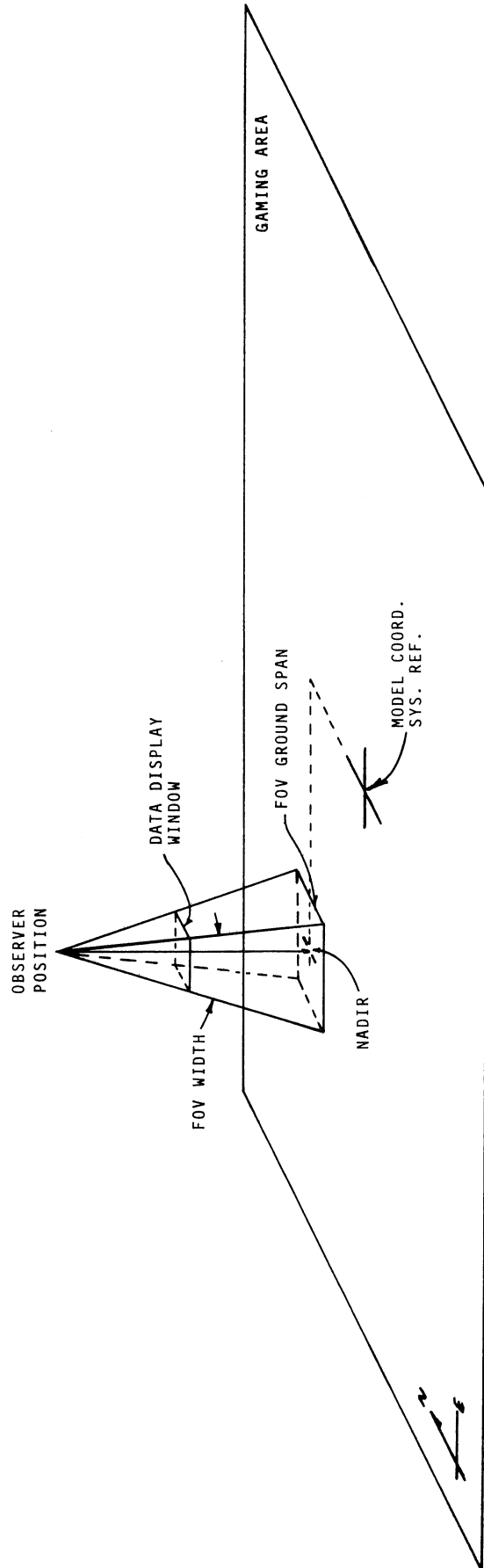
Operator responses to the display faces and set observer position prompts are in the same format as in modeling, refer to Software User's Document P/N 901181-118.



NOTE 1: ONLY APPLICABLE TEK 4010
DISPATCH POINTS ARE SHOWN

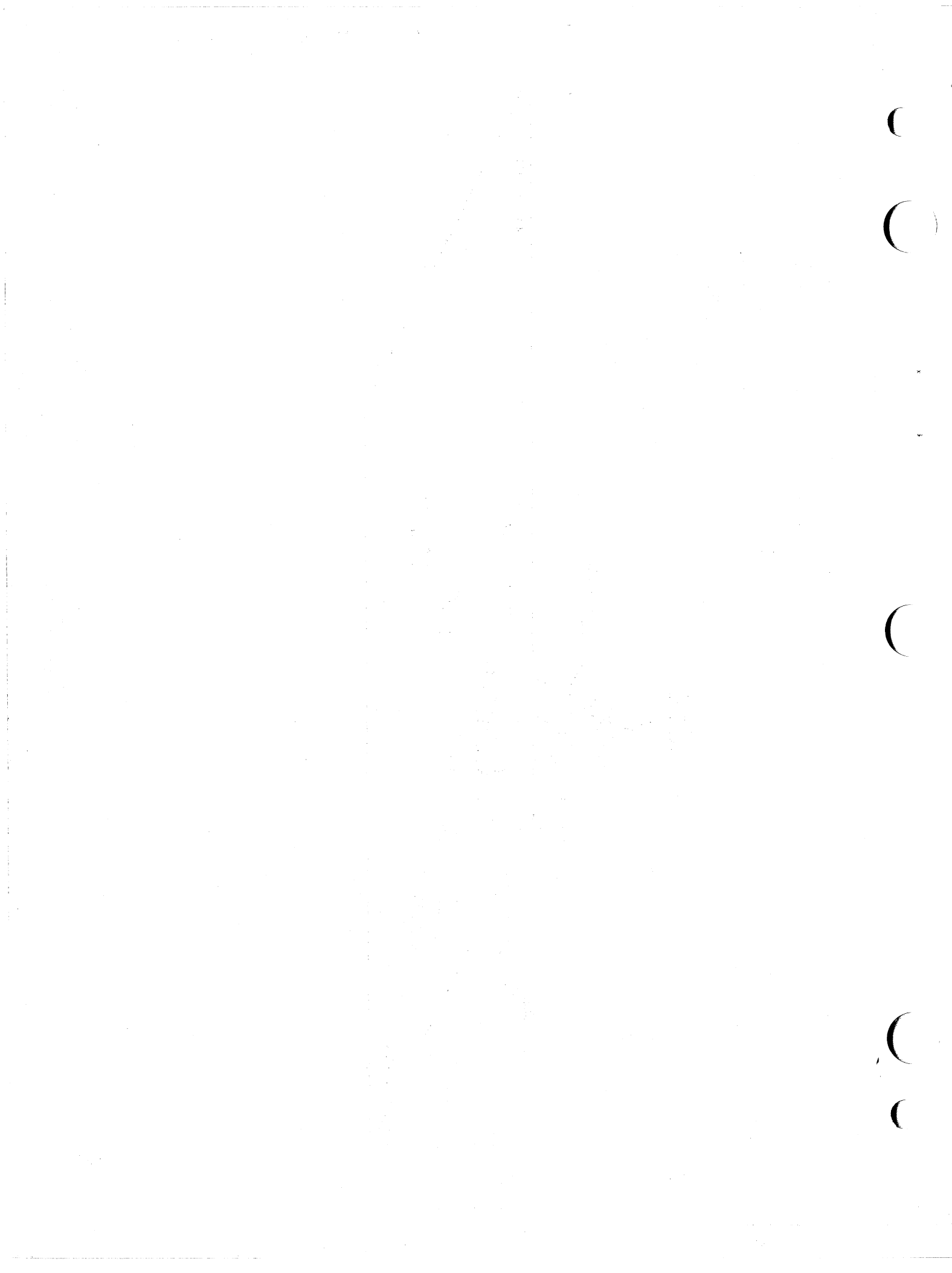
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FIGURE 3-1
NSPBLD PARTIAL FLOWCHART



901181-456

FIGURE 3-2
OBSERVER POSITIONING



SECTION 4

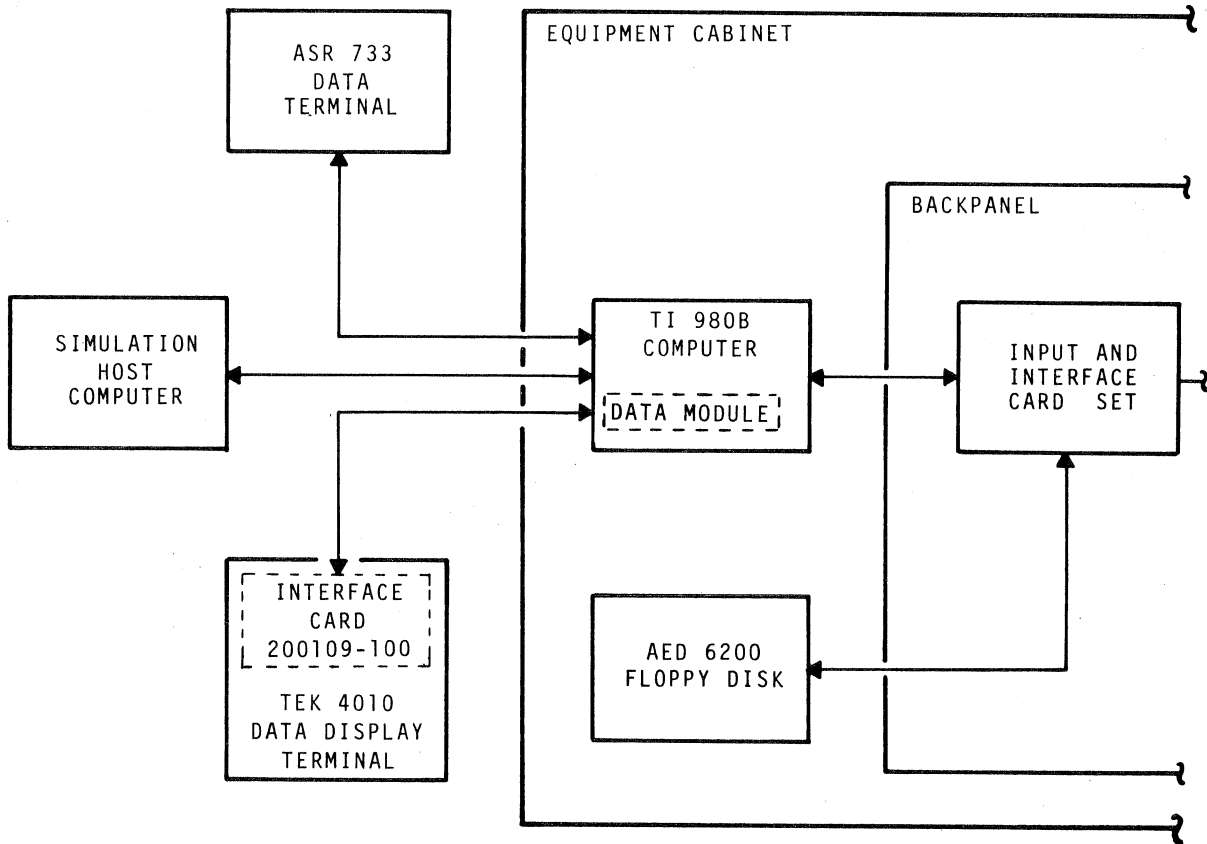
THEORY OF OPERATION

4.1 INTRODUCTION

The data display terminal is a part of the computer group of the NSP system. Its relationship to the other units of the computer group is shown on figure 4-1. The following tabulation is a list of I/O data and control signals used by the data display terminal in its application with the NSP system.

SIGNAL	FUNCTION
*BIT 1-8	Data to and from the terminal/computer.
*CPUNT	Data is about to be to the minibus by the computer interface.
*TSTROBE	Strobes data into the terminal to be displayed on the screen.
*TBUSY	Terminal is busy writing a character or vector, etc.
*CBUSY	Computer (interface) is busy accepting a character.
*HOME	Master reset for all logic (originates from keyboard).
*LOCAL	Directs input source to assert *TSTROBE.
614kHz & 4.9MHz	Clock signals.

*These data and control signal sources must pull the signal line low where active.



901181-457

FIGURE 4-1
 DATA DISPLAY TERMINAL-TO-NSP SYSTEM
 INTERFACE BLOCK DIAGRAM

4.2 TERMINAL INTERFACE CARD (P/N 200109-100)

The terminal interface is a specialized device incorporating three main features. First, it allows bidirectional communication between the TI 980B computer (via the data module) and the Tektronix 4010 Computer Display Terminal. Second, it buffers data flowing into the interface from the computer to compensate for the difference in processing times of the extremely fast computer and the very slow terminal. Third, it transforms the data received from the computer (airport model data base numbers) into a data format which is acceptable to the terminal.

Three general points of information are necessary to consider: (1) terminology, (2) interface resets, and (3) buffering. In order to simplify discussion, the Tektronix 4010 Computer Display Terminal is always referred to as "terminal." The TI 980B computer and the integral data module are referred to collectively as "computer." The terminal interface will be referred to as "interface."

A power-up reset has been incorporated to ensure that the interface is initialized to a known state when power is turned on. Two other resets, the computer reset and the terminal reset (RESET key on terminal keyboard), will also reset the interface.

The terminal can receive data from the computer at a rate from 3 to 5 microseconds per data word, but the terminal can only accept data at a rate from 0.8 to 2.6 milliseconds. For this reason the interface can buffer up to 64 eleven-bit words of data received from the computer.

4.2.1 OPERATING MODES

Eleven modes of operation are available. The first mode, or initial mode, of operation is entered when the interface receives a reset signal. In this mode the interface performs two functions: (1) it receives data from the terminal and sends it to the computer as described under the "data flow" section, (2) it waits for a command word to be received from the computer.

One of the remaining 10 operating modes is entered when the interface receives one of the ten eleven-bit command words. The remaining six invalid commands, with LSBs ranging in value from hex A through hex F, are essentially ignored and the interface waits for a valid command.

The ten operating modes and the corresponding hex commands are as follows:

<u>Operating Mode</u>	<u>Command (hex)</u>
1) Maintenance-alpha (MA)	400
2) Alpha (A)	401
3) Maintenance-dot (MD)	402
4) Dot (D)	403
5) Maintenance-line (ML)	404
6) Line (L)	405
7) Maintenance-crosshair (MX)	406
8) Crosshair (X)	407
9) Maintenance-read-position (MR)	408
10) Read position (R)	409

In some cases ASCII characters or Cartesian coordinate data are sent after the commands are sent. The coordinate data are ten bits wide and the ASCII data are only eight bits wide. The eleventh bit in both cases must be a "zero" since it is the distinguishing feature between a command and a data word. If a data word is sent when the interface is expecting a command word, the data will be ignored and no action is taken until a command is received.

In the following paragraphs the five nonmaintenance modes of operation are first discussed, followed by an explanation of the maintenance mode.

The alpha command is issued for the purpose of printing alphanumeric characters on the terminal screen. The command is sent, followed by as many alphanumeric ASCII characters as desired. These characters are sent to the terminal preceded by the ASCII terminal control character "US." The interface responds to a new mode when a new command word is sent.

A dot command is issued for the purpose of printing dots at specific coordinates on the terminal screen. The command is sent, followed by as many ten-bit YX data pairs (a YX data pair is a ten-bit "Y" coordinate data word followed by a ten-bit "X" coordinate data word) as desired. If the pair is not complete (only the Y coordinate data word is sent) before another command is sent, then the Y data word is ignored and the command is processed normally.

A dot command causes the ASCII terminal control character "GS" to be sent to the terminal. For each complete YX data pair sent, a set of five special ASCII characters plus a terminating "GS" is sent. The five special characters and the order in which they occur in the data stream are designated as two-letter acronyms: YH, YL, XH, XL, XL. The first letter indicates whether the data word is an "X" or a "Y" coordinate data point. The second letter indicates whether the data word contains the H (High), or most significant, five bits or the L (Low), or least significant, five bits of the coordinate data word. The seven-bit ASCII data character that corresponds to each of these special words is generated by appending a two-bit prefix to the five-bits of data as follows:

<u>2-letter Acronym</u>		<u>Prefix (2 bits)</u>	<u>Data (5 bits)</u>
XH or YH	=	01	X MSBs or Y MSBs
XL	=	10	X LSB's
YL	=	11	Y LSB's

A line command is issued for the purpose of drawing lines on the terminal screen. The command is sent followed by as many YX pairs as desired. The first YX pair is the starting point and connected lines will be drawn to points designated by succeeding YX pairs. The terminal data are formed the same as for the dot command, except that the terminating ASCII "GS" is not sent.

When a crosshair command is sent, the terminal displays a movable crosshair, and the interface waits for another command. The command causes two ASCII characters, "ESC" and "SUB," to be generated by the interface and transmitted to the terminal.

A read-position command is issued, normally following a crosshair command, for the purpose of querying the position of the terminal crosshair. The command causes the ASCII characters, "ESC" and "ENQ," to be sent to the terminal. Immediately following the transmission of the two ASCII characters, the interface receives the crosshair position and transmits it to the computer, one word at a time, as it is ready to receive it.

The five maintenance modes operate in the same fashion as the corresponding five nonmaintenance modes in that the same data are generated by the interface. The only difference is that the data, rather than being routed to the terminal, are routed back to the computer. This allows for cross-checking of interface-generated data. After one of the maintenance commands is issued, the data thus generated must be read by the computer.

Data Flow

For the interface to receive data from the computer, a number of signal interactions take place. The computer must place data on the data lines (*DF0 through *DF10) and issue a *DOR signal, indicating to the interface that a word is ready. When the interface has accepted the data, an acknowledge signal, *DOA, is issued by the interface and *DOR is deactivated.

For the interface to send data to the terminal, a certain sequence of events takes place. First, the signal *CPUNT is lowered for at least 5 microseconds in order to seize the terminal bus. A data word is then placed on the bus lines (*TIO0 through *TIO7) for the latter two clock pulses (clock = 1.6ms) of *CPUNT. Another signal, *TSTROBE, is then activated for two clock pulses along with *CPUNT and the data. The signal *TSTROBE is then released. The data word remains for another two clock pulses. The signal *CPUNT remains for only four more clocks if there is not another data word, or is held active if another data word is to be presented at the outputs.

For the interface to receive data from the terminal, the interface first receives a *CSTROBE signal indicating a data word is waiting on the bus. The interface latches the data and activates a *CBUSY signal to the terminal, indicating the interface is processing data and is not ready to receive more data.

For the interface to transmit data to the computer, a data word is placed on the computer input lines (*DT0 through *DT7) and a ready signal, *DIR, is brought low. As soon as the computer has taken the data, an acknowledge signal, *DIA, is generated by the computer, thus deactivating *DIR and allowing the interface to output more data.

4.2.2 BLOCK DIAGRAM COMPONENT FUNCTIONAL DESCRIPTION

Computer/FIFO Ready/ACK SYNC

This device is a four-state synchronous logic machine that synchronizes the ready and acknowledge signals (*DOR, *DOA) between the computer and the FIFO. Looping on the "busy-bit" with the TI assembly language write direct single (WDS) instruction is allowed.

FIFO

The FIFO is 12 bits wide (only 11 bits are used) and 64 words long. It buffers the input from the computer to the interface.

FOR Sync

Consisting of a flip-flop, this device synchronizes the FOR signal from the FIFO to the clock, producing the FORH signal for use as a condition input to the condition select. (Asynchronous signals are not acceptable as inputs to the condition select. Only a combinational logic pathway exists from the condition select input to microprogram sequence input, and an asynchronous test input to the sequence causes it to cycle improperly.)

Start Address Decoder

This device is a 32-word by 8-bit PROM, but only 16 locations are used. It decodes the command words and sends indirect microcode starting addresses of the function to the microprogram. The function is found in the four LSBs of the control word. The eleventh bit must be a "1," indicating that a command word, as opposed to a data word, is being sent.

TBUSY Sync

This device synchronizes the asynchronous TBUSY signal to the system clock.

Condition Select

This device allows selection of a conditional input on which a test can be performed by the microcode.

Next Address Control

This device determines the source of the microprogram next-address from one of the following: (1) pipeline register, (2) stack, (3) program counter, (4) internal register.

Microprogram Sequencer

This device maintains three next-address sources: (1) internal register, (2) stack, (3) program counter.

Microinstruction PROM

This PROM is 256 words by 8 bits and contains the microprogram.

Pipeline Register

This register latches the particular microinstruction being executed during a particular clock cycle.

Constants PROM

This PROM contains two kinds of data: (1) special control characters used to communicate with the terminal and (2) constants used during transformation of data received from the computer to be sent to the terminal.

Power-Up Reset

This device sends a reset pulse to the reset collector when power is turned on.

Reset Collector

This logic collects resets from three different sources: (1) power-up reset from interface, (2) reset from computer (*MASTER RESET Signal), (3) reset from terminal (*HOME Signal).

Microprocessor

Transformation of coordinate positions to corresponding ASCII characters is performed in the microprocessor section. Data necessary to define one command, one character, or one point are buffered until all are collected before being sent to the terminal.

Data Director

This device routes data from the proper source to the proper destination, depending on the mode in which the interface is operating. Data are routed in one of the following three pathways: (1) computer to terminal, (2) computer to computer, and (3) terminal to computer. A data word that is flowing to the computer is buffered until it is read by the computer.

Computer Input Driver

This device is used to invert and drive the data down the cable connecting the interface to the computer.

Terminal/Computer Ready/Ack Sync

In order to transfer data from the terminal to the computer, this device does the following:

- 1) Accepts a data strobe (*CSTROBE) from the terminal when it has data on the bus
- 2) Sends an input-ready signal (*DIR) to the computer
- 3) Sends a busy signal (*CBUSY) to the terminal
- 4) Sends a signal (LATCH) to the data director in order to latch the data from the terminal
- 5) Accepts an acknowledge signal (*DIA) from the computer after it has read the data latched at the data director outputs

Bus Seizure Generator

A signal to the terminal (*CPUNT) is generated in order to seize the bus for two purposes:

- 1) To keep the bus in a terminal-input mode before and while sending data to terminal
- 2) To keep the bus from affecting data flow through the data director during a maintenance mode of operation.

TSTROBE Sync

This synchronizes the interface-generated TSTROBE signal to the terminal clock.

Clock Generator

Accepts a clock signal from the terminal and dispenses it to the interface components.



SECTION 5

MAINTENANCE

5.1 INTRODUCTION

The basic periodic maintenance requirements and troubleshooting of the data display terminal is covered in the appropriate Tektronix instruction manual. The procedures listed in this section are designed to evaluate the overall performance of the unit as part of the NSP system.

5.2 DATA DISPLAY TERMINAL TROUBLESHOOTING

When a picture can not be drawn on the Tektronix 4010 data display terminal, any one of four devices can be the cause of the problem. These devices are: 1) Interface card (P/N 200109-100), 2) connecting cable (P/N 101373-030), 3) Tektronix 4010 data display terminal, and 4) the data module cards in the TI 980B computer. In order to isolate the problem area, four separate tests are required and should be run in the following sequence:

- 1) Interface initialization
- 2) TI data module card test
- 3) Tektronix 4010 stand-alone test
- 4) Interface card (200109-100) performance assurance test.

Testing can be terminated as soon as the inoperative device is isolated. A flow diagram of the recommended troubleshooting procedure is shown on figure 5-1, and will be helpful in determining the proper course of action.

When the device is isolated, the technician should refer to the appropriate section of the manual for the correct procedure for the repair of the device.

The technician should also refer to the appropriate section of the manual for the correct procedure for the repair of the device.

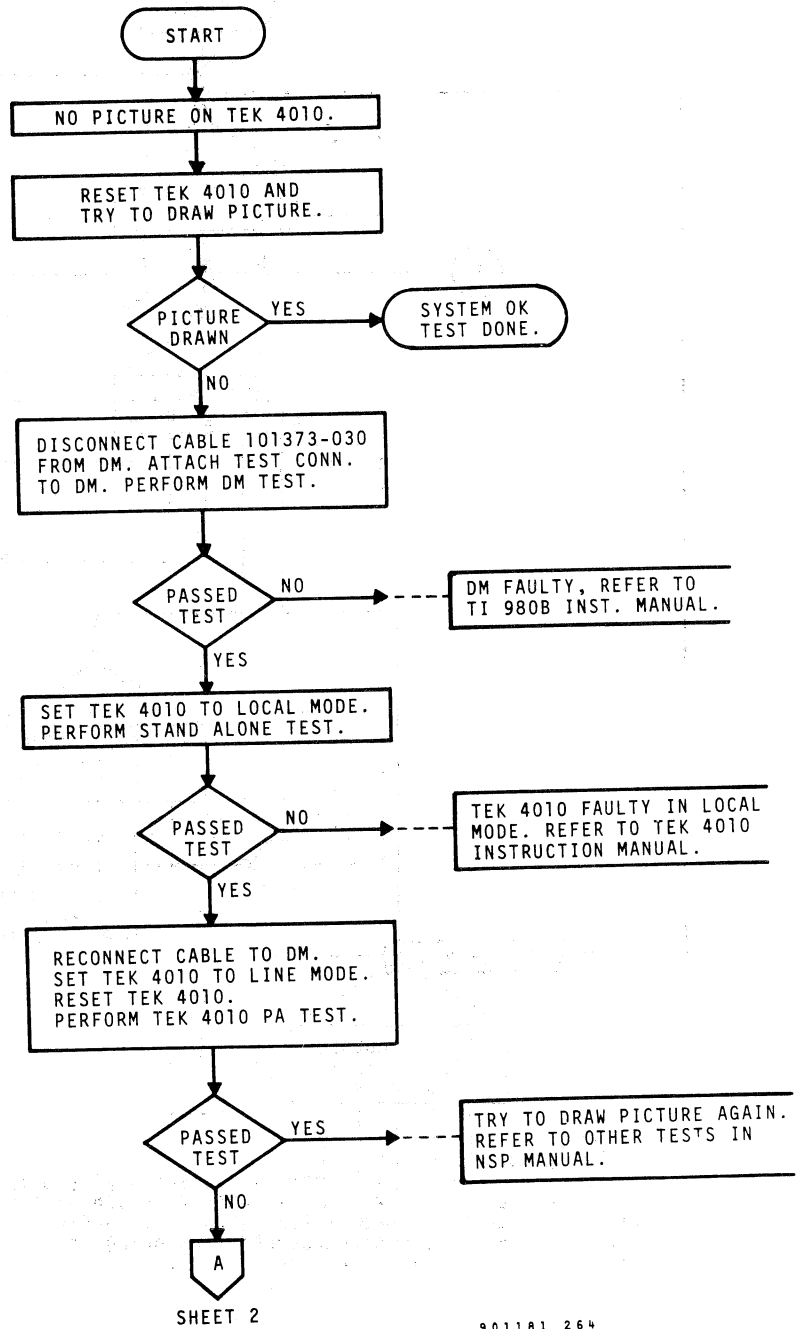
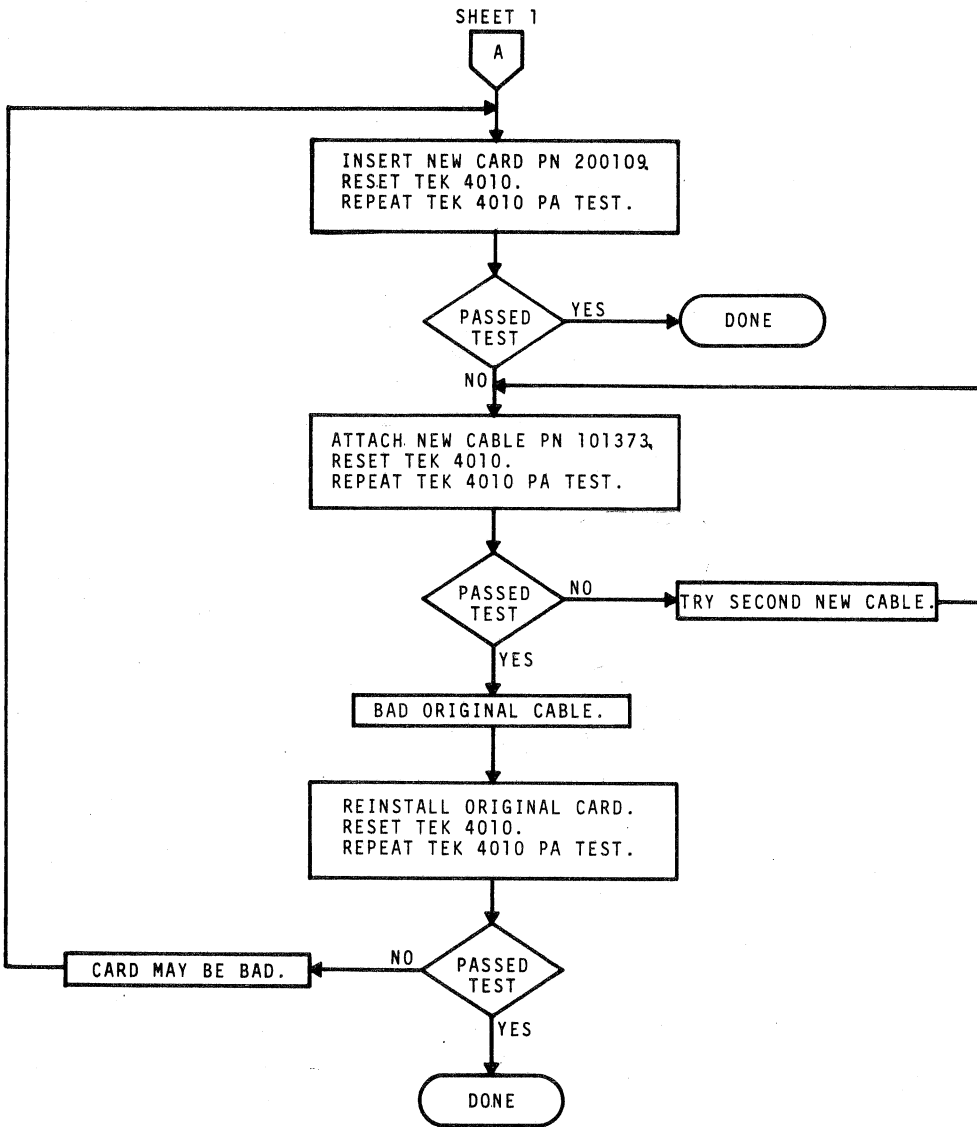


FIGURE 5-1, SHEET 1
TEKTRONIX 4010 DATA DISPLAY TERMINAL
TROUBLESHOOTING FLOW DIAGRAM



901181 265

DM = TI 980B DATA MODULE.
PA TEST = PERFORMANCE ASSURANCE TEST (SEE TEK 4010 INST. MAN.).
RESET = DEPRESS RESET KEY ON TEK 4010 KEYBOARD.
TEK 4010 = TEKTRONIX 4010 DATA DISPLAY TERMINAL.
CARD = INTERFACE CIRCUIT CARD PART NO. 200109-100.
CABLE = INTERFACE CABLE PART NO. 101373-030.

FIGURE 5-1, SHEET 2
TEKTRONIX 4010 DATA DISPLAY TERMINAL
TROUBLESHOOTING FLOW DIAGRAM

5.2.1 INITIALIZE INTERFACE CARD TEST

When the display fails for some reason, depress the RESET key of the display keyboard. Try normal data entry from the keyboard to see if command words are accepted. If not, continue to the data module card test.

5.2.2 TI DATA MODULE CARD TEST

The data module is tested by a diagnostic test on the TI diagnostic cassette tape. The test of the data module assigned to the data display terminal (device address 14) is the same as the test for the data module assigned to the host computer (device address 5C) shown on figure 5-19 in the NSP Operation and Maintenance Manual. To run the test for the data module assigned to the data display terminal, complete the following steps.

- 1) Disconnect data cable P/N 101373-030 from the data module upper card edge connector and install the DM test connector (refer to paragraph 5.3.2.1.3 in the NSP manual.
- 2) Load the TI diagnostic cassette tape according to the procedure listed in subsection 3.2.3.1 of the NSP manual.
- 3) Advance the tape to the data module test and execute the program.
- 4) Monitor the printout on the ASR 733 for any required operator responses.
- 5) When required, enter device address 14.
- 6) When the test is complete, remove the DM test connector and reconnect data cable P/N 101373-030.

5.2.3 TEKTRONIX 4010 STAND-ALONE TEST

To determine if the Tektronix 4010 data display terminal is operating correctly in the local mode, complete the procedure listed in the maintenance manual for the unit. (This test does not verify the correct operation in the line mode). If the unit fails the local mode test, proceed with fault isolation as outlined in the unit's troubleshooting procedure. If the unit passes the local mode test, continue to the performance assurance test listed in the following subsection.

5.2.4 TEKTRONIX 4010 PERFORMANCE ASSURANCE TEST

The following procedure will test the Tektronix 4010 data display terminal (in the LINE mode), interface card (200109-100), and cable (101373-030) as one unit. Isolation of the individual units is accomplished by substitution, one unit at a time. The following test is actually comprised of subtests. If any of the subtests fail, the whole test is considered to have failed. The test can be halted at any point.

<u>STEP</u>	<u>INSTRUCTION</u>	<u>RESPONSE</u>
1.	Verify that power is on for the AED 6200, TI 980B and ASR 733 is on.	The corresponding power indicators will be on.
2.	Complete diskette program loading procedure using the diskette containing the Tektronix 4010 diagnostic test. Complete the following steps in the sequence listed.	Responses will be displayed on the 4010 CRT or printed on the ASR 733 printout. When ready the program prints *READY* on the ASR 733 printout.
3.	Type at the ASR 733: //ASSIGN.4.KEY	
4.	Press the RETURN key.	The ASR 733 will beep when it is ready for another entry.
5.	Type on the ASR 733 //ASSIGN,E,KEYO.	
6.	Press the RETURN key	
7.	Type on the ASR 733 //EXECUTE,FDO,TEKTST. Refer to figure 5-5.	
8.	Press the RETURN key	TEKTRONIX 4010 DIAGNOSTIC V01-34A TYPE THE ALPHABET ON THE 4010 KEYBOARD LETTERS A-Z SHOULD APPEAR ON 4010 CRT ARE CHARACTERS CORRECT? TYPE "Y" OR "N"

9. Type on the 4010:
ABCDEFGHIJKLMN**P**QRSTUVWXYZ
The alphabet must be typed
in less than 30 seconds or
an error message will be
printed. If a failure occurs
at this point.
When the last character
of the alphabet has
been typed all the
alphabet will be dis-
played on the CRT
screen in the upper
left corner.
10. Type on the ASR 733: Y
11. Press the RETURN key
TEST MAINTENANCE DOT
PATH
TEST MAINTENANCE LINE
MODE VISUAL DOT TEST
DISPLAY CORRECT?
TYPE "Y" OR "N"
12. Check that the image on the
screen matches the dot pattern
shown in figure 5-2.
The CRT displays the
image shown in figure
5-2.
13. Type on the ASR 733: Y
14. Press the RETURN key
VISUAL LINE TEST
DISPLAY CORRECT?
TYPE "Y" OR "N"
15. Check that the image on the
screen matches the line pat-
tern shown in figure 5-3.
The CRT displays the
image shown in figure
5-3.
16. Type on the ASR 733: Y
17. Press the RETURN key
CROSSHAIR AND READ
POSITION TEST MOVE
CROSSHAIR INTO UPPER
LEFT QUADRANT TYPE
"CR" ON CONSOLE WHEN
POSITIONED. Refer to
figure 5-4.
18. Using the thumb wheels to the
right of the keyboard on the
Tektronix 4010 display, move
the crosshairs until the
intersections of the crosshairs
is in the upper left quadrant.
19. Press the RETURN key
MOVE CROSSHAIR INTO
UPPER RIGHT QUADRANT

TYPE "CR" ON CONSOLE
WHEN POSITIONED.

20. Do as instructed using the
thumb wheels

21. Press the RETURN key

MOVE THE CROSSHAIR
INTO LOWER LEFT
QUADRANT TYPE "CR" ON
CONSOLE WHEN
POSITIONED.

22. Do as instructed

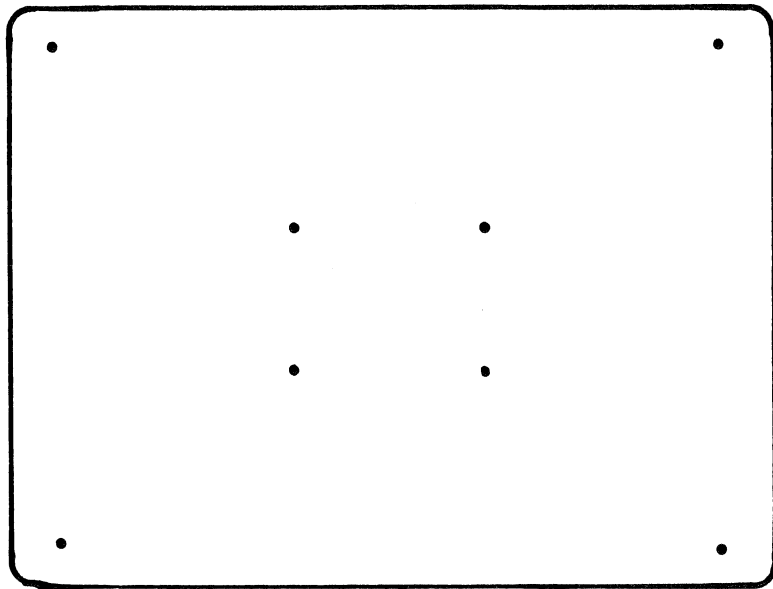
23. Press the RETURN key

MOVE CROSSHAIR INTO
LOWER RIGHT QUADRANT
TYPE "CR" ON CONSOLE
WHEN POSITIONED.

24. Do as instructed

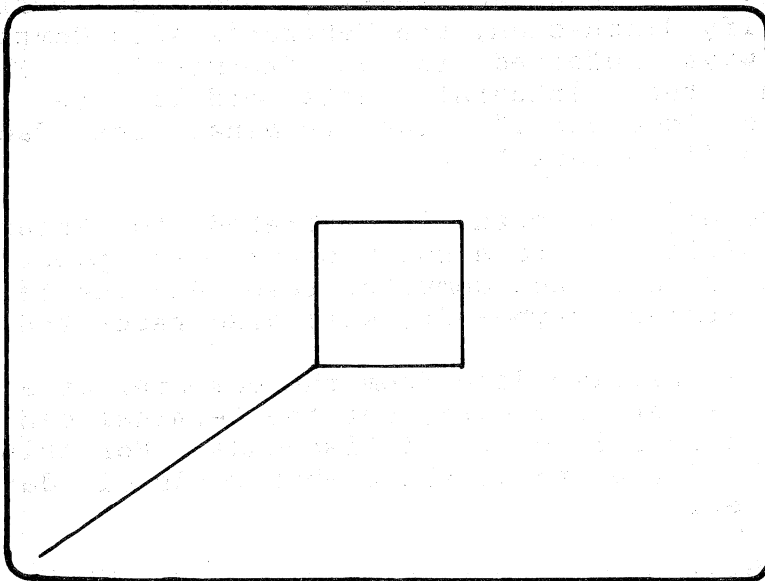
25. Press the RETURN key

DONE



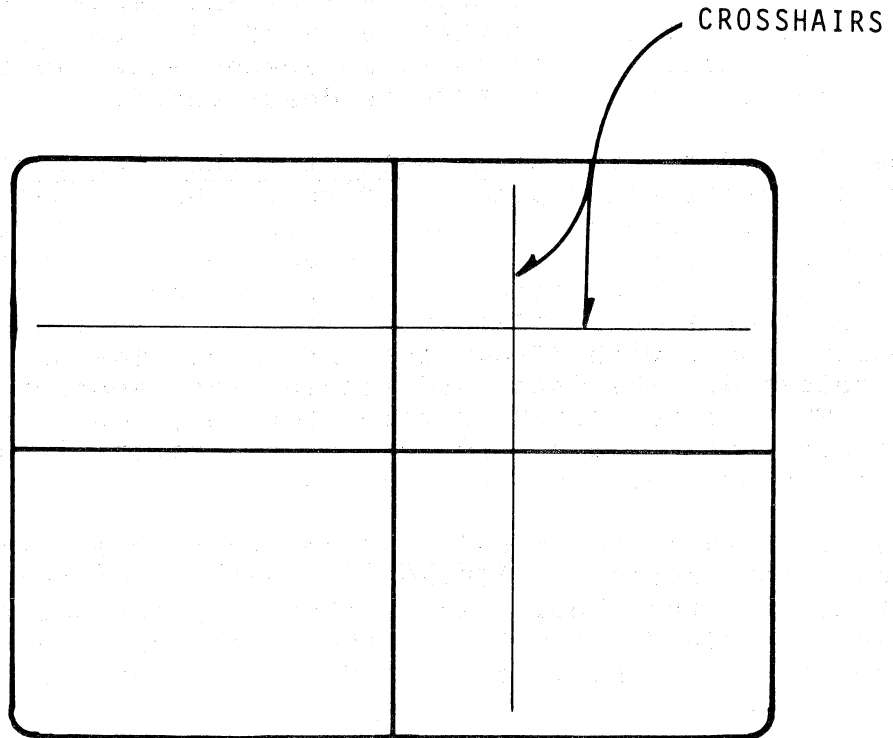
901181-238

FIGURE 5-2
TEKTRONIX 4010 DIAGNOSTIC TEST
VISUAL DOT TEST



901181-239

FIGURE 5-3
TEKTRONIX 4010 DIAGNOSTIC TEST
VISUAL LINE TEST



901181-240

The position of the crosshairs is determined by the setting of the thumbwheels on the keyboard.

FIGURE 5-4
TEKTRONIX 4010 DIAGNOSTIC TEST
CROSSHAIR AND READ POSITION TEST

TEKTRONIX 4010 USER'S 901181-116
MAINTENANCE

NSPMON V01-04

◆READY◆

//ASSIGN,E,KEY0.

//ASSIGN,4,KEY.

//EXECUTE,FDO,TEKTST.

TEKTST V02-01

TEKTRONIX 4010 SUBSYSTEM DIAGNOSTIC
TYPE THE ALPHABET ON THE 4010 KEYBOARD
LETTERS A-Z SHOULD APPEAR ON 4010 CRT
ARE CHARACTERS CORRECT? TYPE "Y" OR "N"

Y

TEST MAINTENANCE DOT PATH

TEST MAINTENANCE LINE PATH

VISUAL DOT TEST

DISPLAY CORRECT? TYPE "Y" OR "N"

Y

VISUAL LINE TEST

DISPLAY CORRECT? TYPE "Y" OR "N"

Y

CROSSHAIR AND READ POSITION TEST

MOVE CROSSHAIR INTO UPPER LEFT QUADRANT

TYPE "CR" ON CONSOLE WHEN POSITIONED.

MOVE CROSSHAIR INTO UPPER RIGHT QUADRANT

TYPE "CR" ON CONSOLE WHEN POSITIONED.

MOVE CROSSHAIR INTO LOWER LEFT QUADRANT

TYPE "CR" ON CONSOLE WHEN POSITIONED.

MOVE CROSSHAIR INTO LOWER RIGHT QUADRANT

TYPE "CR" ON CONSOLE WHEN POSITIONED.

DONE

◆READY◆

Operator-typed responses are underlined.

FIGURE 5-5
TEKTRONIX 4010 DATA DISPLAY TERMINAL SUBSYSTEM
DIAGNOSTIC TEST SAMPLE PRINTOUT

SECTION 6

PARTS LIST

6.1 INTRODUCTION

Table 6-1 is a parts list for the E&S manufactured data cable, and the interface card and its components. Information provided includes, reference designation where applicable, nomenclature, the E&S part number, and the figure number of the illustration depicting the location of the item.

TEKTRONIX 4010 USER'S 901181-116
PARTS LIST

Ref. Desig.	Nomenclature	E&S Part Number	Fig. No.
	Data Cable	101373-030	2-3/4
	Data Interface Circuit Card.	200109-100	2-3
C1	Capacitor	804102-475	6-1
C2	Capacitor	804102-475	6-1
C3	Capacitor	804115-333	6-1
C9	Capacitor	804115-333	6-1
C10	Capacitor	804128-107	6-1
C11	Capacitor	804115-333	6-1
C38	Capacitor	804115-333	6-1
R1	Resistor	803201-102	6-1
R2	Resistor	803201-302	6-1
R3	Resistor	803201-471	6-1
U12	Integrated Circuit	807816-653	6-1
U13	Integrated Circuit	807400-646	6-1
U14	Integrated Circuit	807474-646	6-1
U15	Integrated Circuit	807493-646	6-1
U16	Integrated Circuit	807657-646	6-1
U18	Integrated Circuit	807301-726	6-1
U22	Integrated Circuit	807791-055	6-1
U23	Integrated Circuit	807791-055	6-1
U24	Integrated Circuit	807791-055	6-1
U25	Integrated Circuit	807441-646	6-1
U26	Integrated Circuit	807657-646	6-1
U30	Integrated Circuit	807441-646	6-1
U31	Integrated Circuit	807411-055	6-1
U32	Integrated Circuit	807791-055	6-1
U33	Integrated Circuit	807791-055	6-1
U34	Integrated Circuit	807791-055	6-1
U35	Integrated Circuit	807657-646	6-1
U36	Integrated Circuit	807657-646	6-1
U38	Integrated Circuit	807301-726	6-1
U40	Integrated Circuit	807408-646	6-1
U41	Integrated Circuit	807416-646	6-1
U42	Integrated Circuit	807808-016	6-1
U43	Integrated Circuit	807808-016	6-1
U44	Integrated Circuit	807808-016	6-1
U45	Integrated Circuit	807416-055	6-1
U46	Integrated Circuit	807400-646	6-1
U51	Integrated Circuit	807699-331	6-1
U52	Integrated Circuit	807808-016	6-1
U53	Integrated Circuit	807808-016	6-1
U54	Integrated Circuit	807808-016	6-1
U55	Integrated Circuit	807739-055	6-1
U56	Integrated Circuit	807400-646	6-1
U58	Integrated Circuit	807301-726	6-1

	Alternate Number		
U60	Integrated Circuit	807651-055	6-1
U61	Integrated Circuit	807400-646	6-1
U62	Integrated Circuit	807807-716	6-1
U63	Integrated Circuit	807806-727	6-1
U64	Integrated Circuit	807806-727	6-1
U65	Integrated Circuit	807739-055	6-1
U66	Integrated Circuit	807305-612	6-1
U68	Integrated Circuit	807305-612	6-1
U70	Integrated Circuit	807416-646	6-1
U71	Integrated Circuit	807474-646	6-1
U72	Integrated Circuit	807411-055	6-1
U73	Integrated Circuit	807765-038	6-1
U74	Integrated Circuit	807765-038	6-1
U75	Integrated Circuit	807765-038	6-1

TABLE 6-1
PARTS LIST

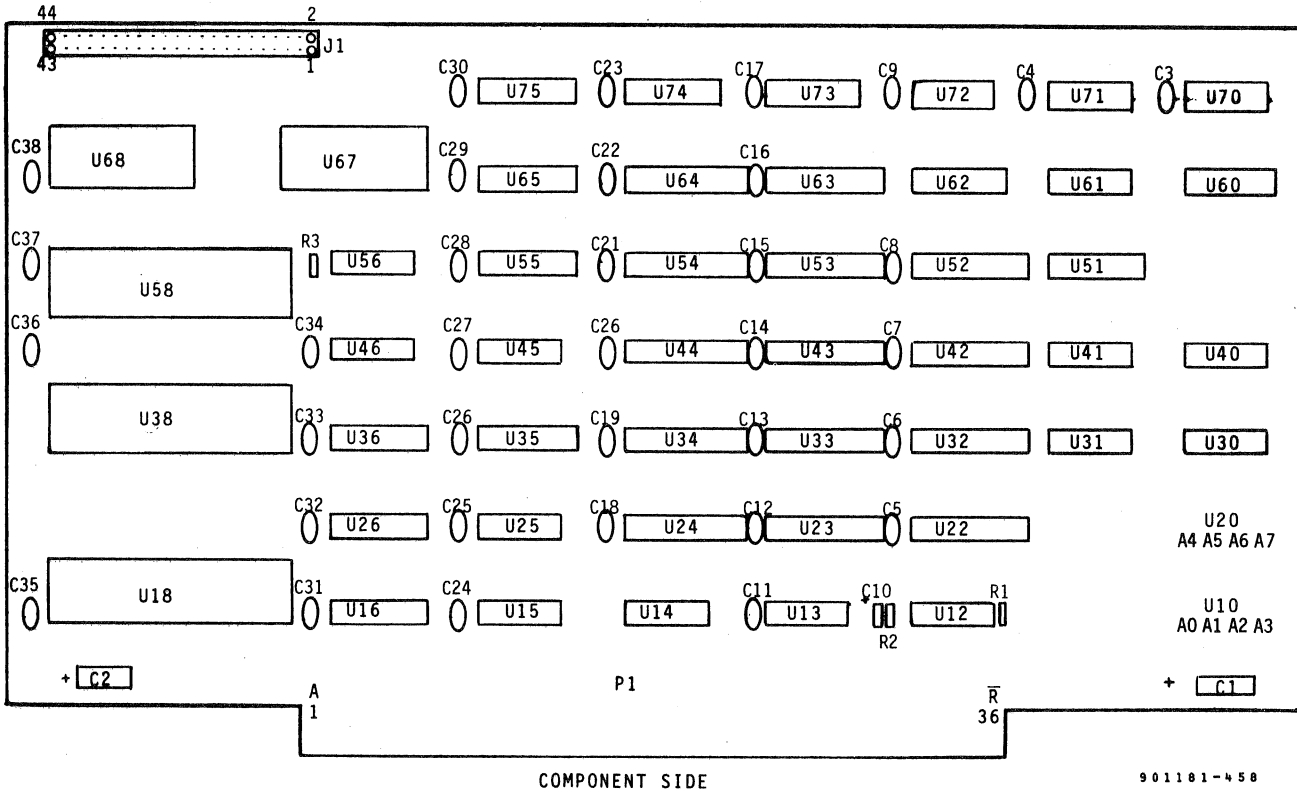


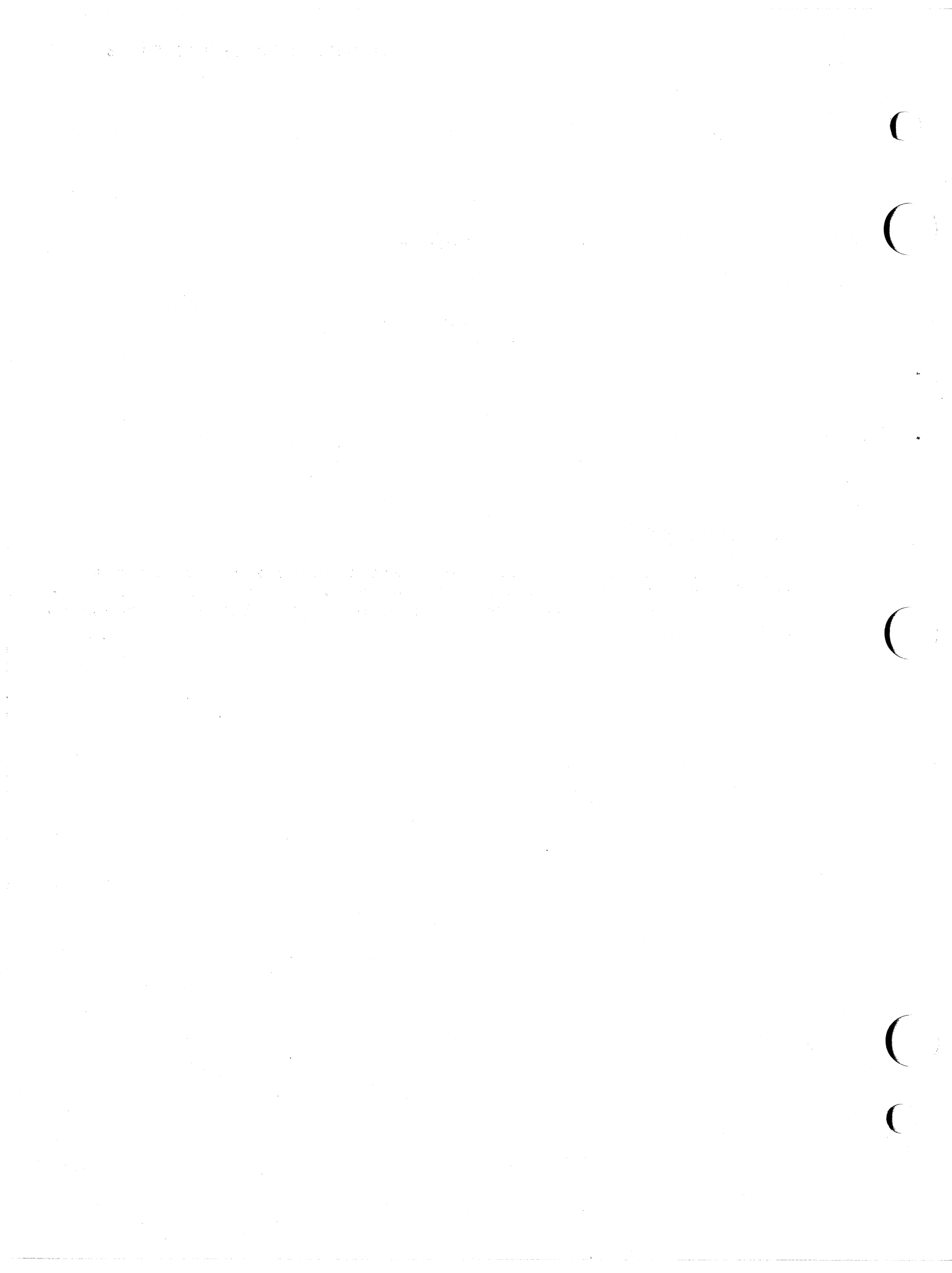
FIGURE 6-1
DATA INTERFACE CIRCUIT CARD

SECTION 7

DRAWINGS

7.1 INTRODUCTION

This section contains the eight sheets of the logic diagram for the interface circuit board P/N 200109-100. The location of the electronic components shown on the logic diagram is shown on figure 6-1.



ZONE	LTR	REV	DATE	APPROVED
A-1			22 MAR 77	D. Spivey
A-2			9/15/80	D. Spivey
DRAWING CHANGES				
DRAWING CHANGES				

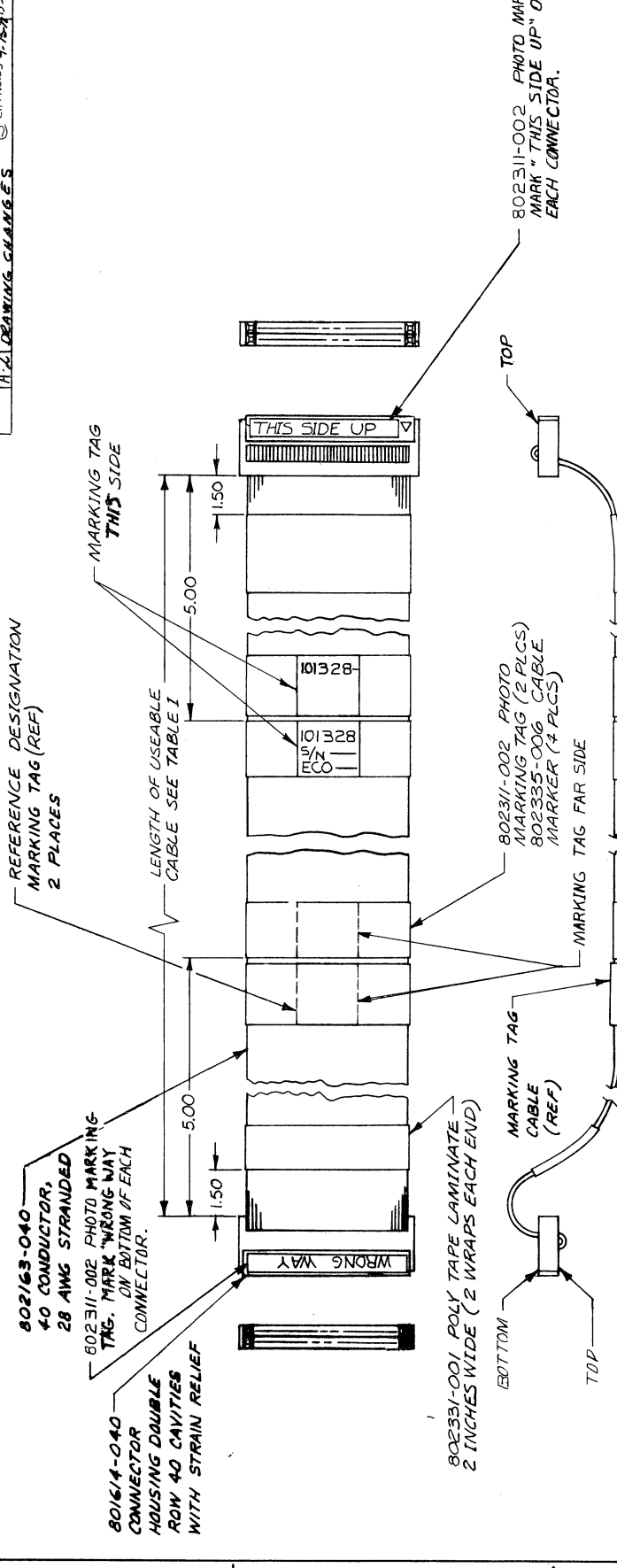


TABLE I

ECO E & S PART NO.	CABLE LENGTH	APPROVED
A2 101328-002	2 FEET	D. Spivey 22 MAR 77
A2 101328-003	3 FEET	D. Spivey 22 MAR 77
A2 101328-004	4 FEET	D. Spivey 22 MAR 77
A2 101328-007	7 FEET	D. Spivey 22 MAR 77

SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO. DRAWN See 101328-002 3-28-77		CABLE RIBBON, 40 COND.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		SIZE CODE IDENT NO C 53938	
TOLERANCES ON .XX ± .XXX ± ANGLES ±		REV TABLE I	
CHECKED BY [Signature] 12 APR 77		SCALE NONE	
MECH		101328-TAB	
ELEC P. Spivey 12 APR 77		SHEET 1 OF 1	
PROJ. ENG			
APPROVED			
MATERIAL SEE PARTS LIST			
FINISH			
MULTI-USE USED ON			
APPLICATION			

101328-74B

REVISIONS		
ZONE	LTR	DESCRIPTION
A2		DELETE NOTE 1. CHANGE MARKING (LESS)
		DATE 10-5-78
		APPROVED [Signature] 05-06-78

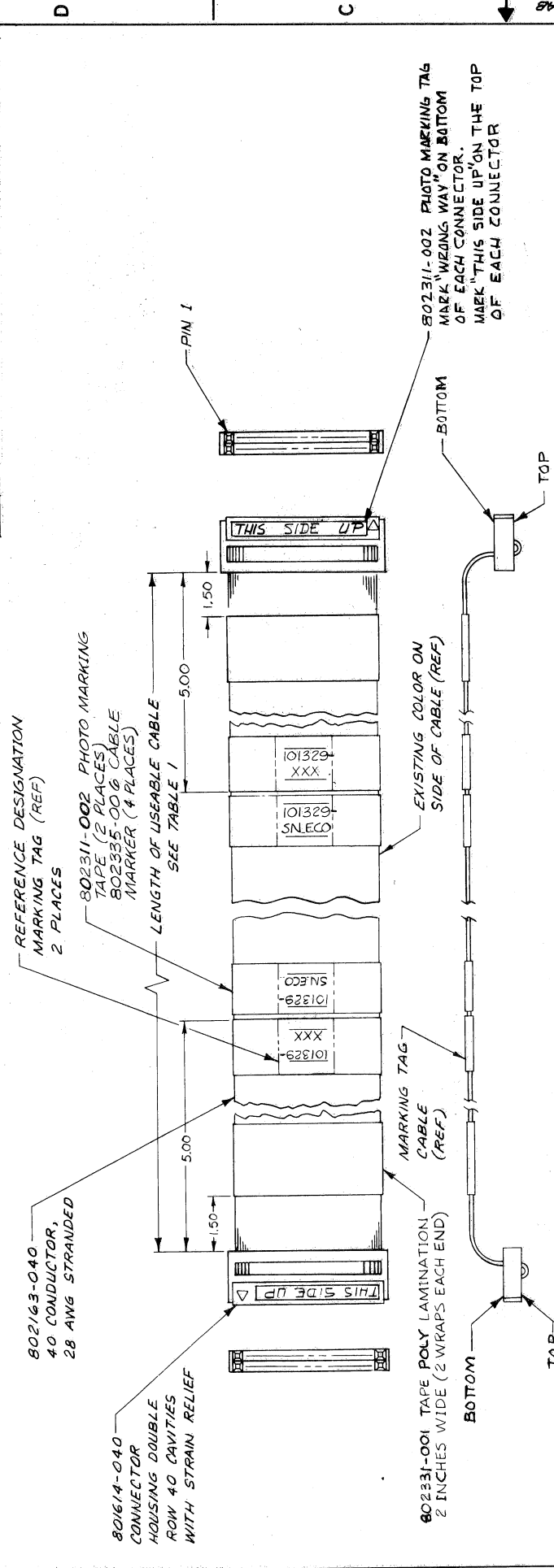


TABLE I

ECO E F S PART NO.	CABLE LENGTH
A2	2 FEET

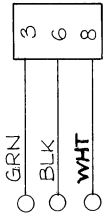
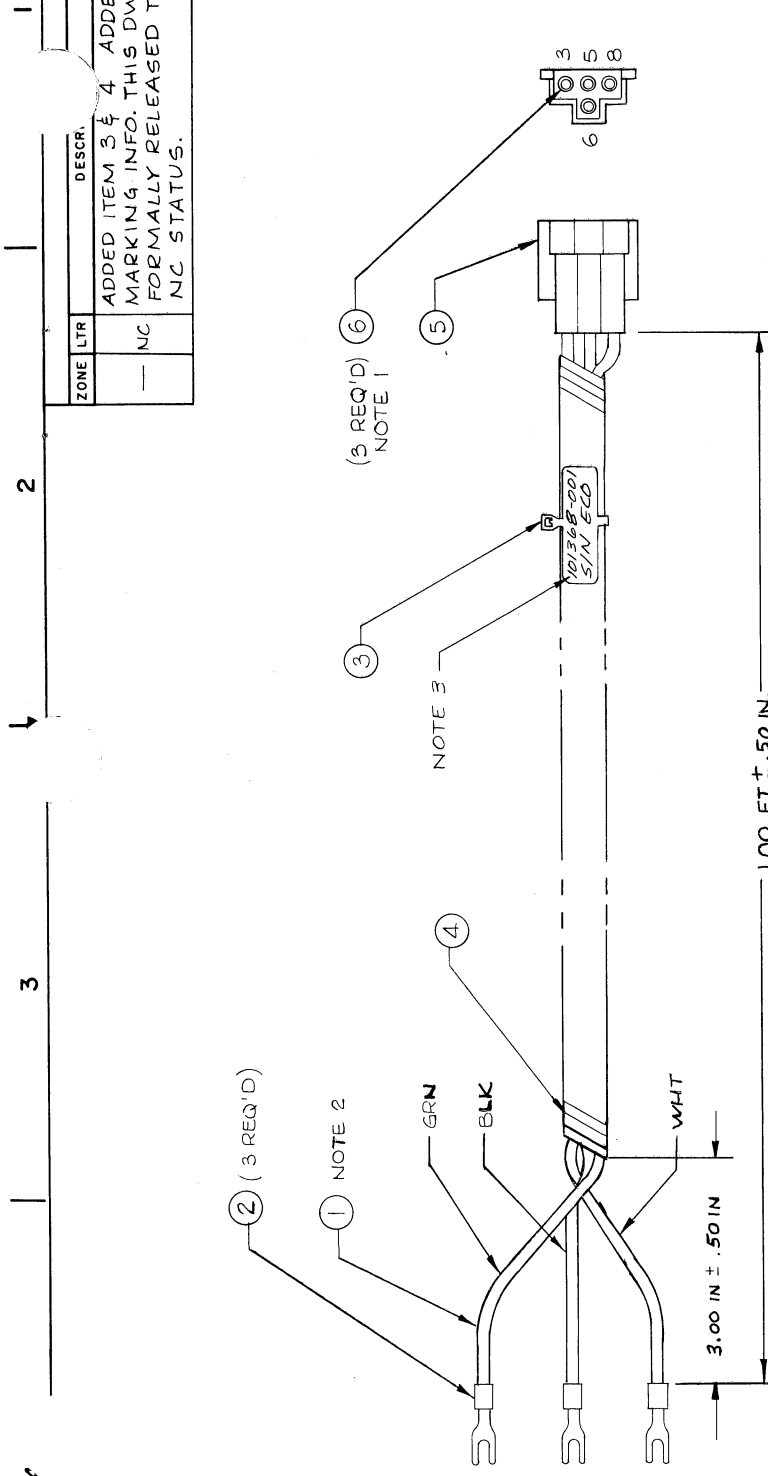
SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN <i>See 10/13/78</i> 9-28-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CHECKED <i>See 10/13/78</i> 12 APR 77	
TOLERANCES ON		MECH	
.XX ±		ELEC <i>See 10/13/78</i> 12 APR 77	
.XXX ±		PROJ. ENG	
ANGLES ±		APPROVED	
✓			
MATERIAL		SIZE CODE IDENT NO	
MULTI-USE		C 53938	
NEXT ASSY USED ON		REV	
APPLICATION		TABLE I	
		SCALE NONE	
		SHEET 1 OF 1	
		CABLE ASSEMBLY, FLAT RIBBON, 40 COND. 2 REVERSE	

4 3 2 1

D C B A

B 101329-748

ZONE	LTR	DESCR.	APPROVED
—	NC	ADDED ITEM 3 & 4. ADDED MARKING INFO. THIS DWG FORMALLY RELEASED TO NIC STATUS.	15 AUG 77 [Signature]
			8-13-77



SCHEMATIC

3. MARK ASSY NO. S/N, ECC NO., USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.
2. LOOSELY TWIST ALL WIRES TOGETHER.
1. DO NOT INSTALL ITEM (6) INTO ITEM (5) AT POSITION 5. UNLESS OTHERWISE SPECIFIED:
- NOTES:

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTRACT NO.	
TOLERANCES ON		DRAWN ADS	8-10-77
.XX ±		CHECKED Jim Pino	8-15-77
.XXX ±		MECH	
ANGLES ±		ELEC <i>Jim Pino</i>	15 AUG 77
✓		PROJ. ENG	
		APPROVED J. Pino	8-15-77
MATERIAL	SEE PARTS LIST	SEE SEPARATE PARTS LIST	
200154-100	NOVONVIEW	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
NEXT ASSY	USED ON	ASSY, CABLE POWER, 230V (+5V POWER SUPPLY)	
APPLICATION		SIZE CODE IDENT NO	REV
		C 53938	101368-001 NC
		SCALE NONE	SHEET 1 OF 1

D

2

3

D

C

C

B

B

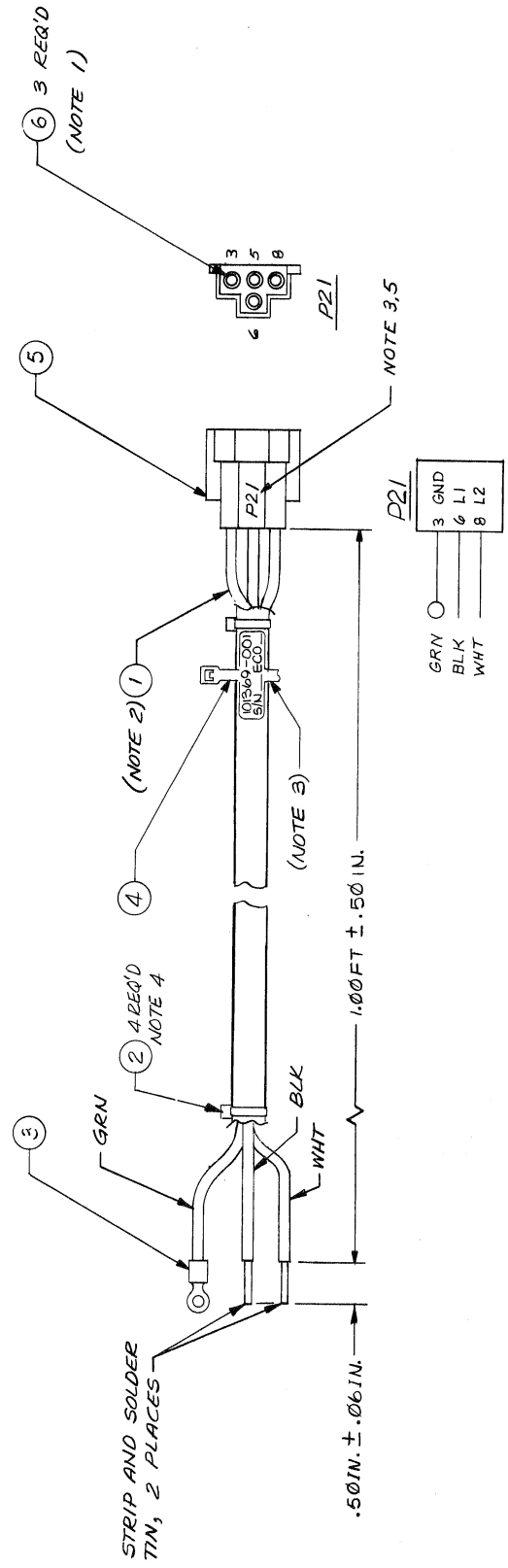
A

A

101368-001

4 3 2 1

REVISIONS			
ZONE LTR	DESCRIPTION	DATE	APPROVED
-	ITEM 3 WAS 802133-006, ADDED REF DES P21, NOTE 5 MW	2-7-78	<i>[Signature]</i>
A1			9/5/77



SCHEMATIC

5. MARK P21 ON BOTH SIDES OF CONNECTOR.
 4. PLACE ITEM (2) APPROX. EVERY 3 INCHES.
 3. MARK ASSY NO., SIN, ECO NO. & CONNECTOR "P21" USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT. .09 INCH HIGH CHARACTERS. LOOSELY TWIST ALL WIRES TOGETHER.
 1. DO NOT INSTALL ITEM (6) INTO ITEM (5) AT POSITION 5.
- UNLESS OTHERWISE SPECIFIED:
NOTES

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON		CONTRACT NO.	SEE SEPARATE PARTS LIST
.XX ±		DRAWN: D. CARROLL 8-3-77	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
.XXX ±		CHECKED: J. [Signature] 5-AUG-77	
ANGLES ±		MECH	
✓		ELEC [Signature] 15-AUG-77	
		PROJ. ENG	
		APPROVER [Signature] 15 AUG 77	
MATERIAL	SEE PARTS LIST		ASSY, CABLE, POWER, 250V (+12V POWER SUPPLY)
200152-100	NOVOTVIEW		
NEXT ASSY	USED ON		
	APPLICATION		
SIZE CODE IDENT NO	C 53938		
SCALE	NONE		
REV	AJ		
SHEET	1		

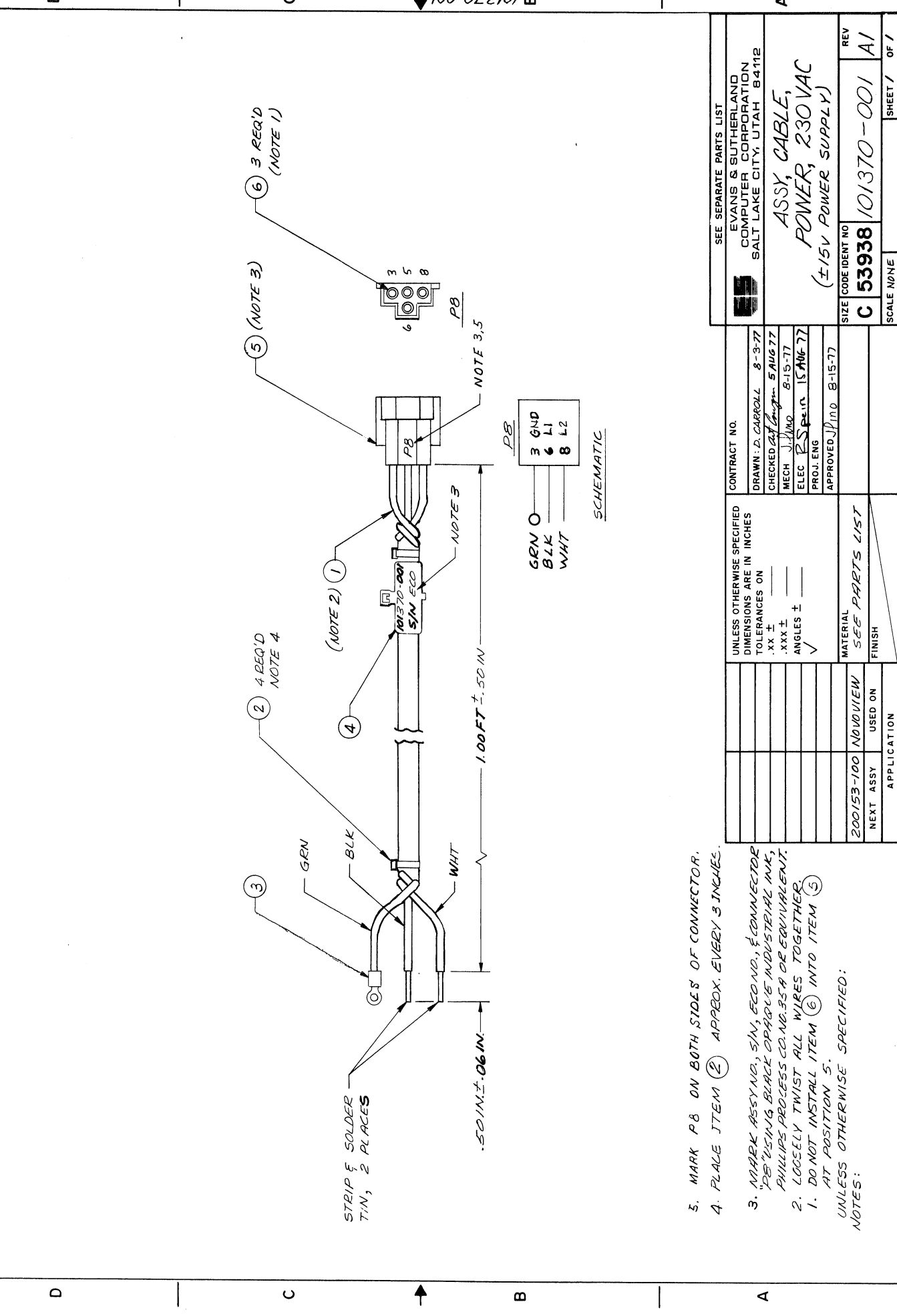
D C B 101369-001 A

ZONE	LTR	DESC.	ITE	APPROVED
AI	AI	ADDED PB REF DES & NOTES 5 MW	2-7-78	R Spe. 15

DATE	BY	CHKD	APP'D

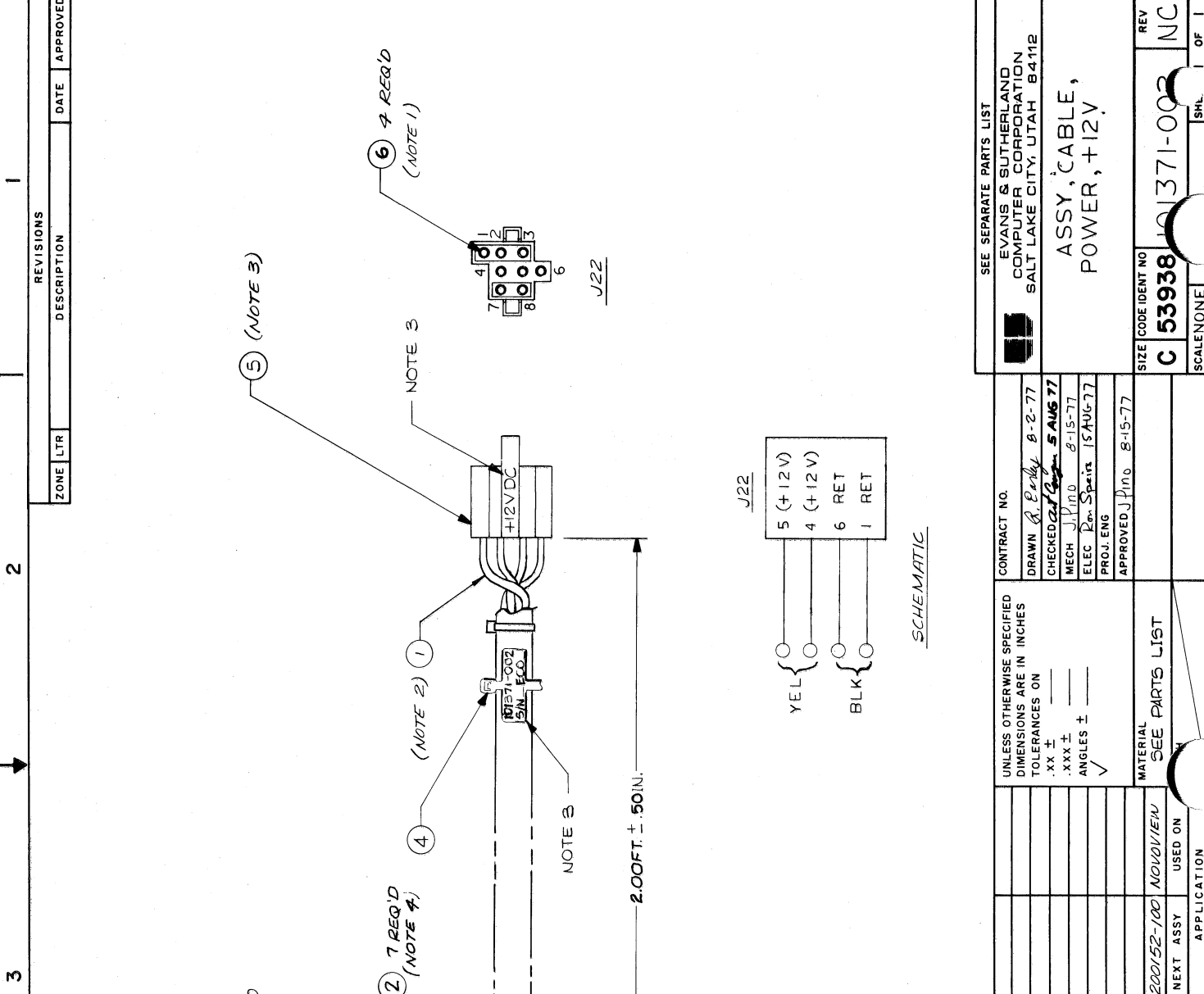
CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XXX ± .XXX ± ANGLES ±
DRAWN: D. CARROLL 8-3-77	MECH: J. MOG 8-15-77
CHECKED BY: [Signature] 5 AUG 77	ELEC: R. [Signature] 15 AUG 77
APPROVED: J. [Signature] 8-15-77	

200153-100	NO VIEW	USED ON	APPLICATION
NEXT ASSY			



5. MARK PB ON BOTH SIDES OF CONNECTOR.
 4. PLACE ITEM ② APPROX. EVERY 3 INCHES.
 3. MARK ASSY NO., S/N, ECO NO., & CONNECTOR "PB" USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 55-A OR EQUIVALENT.
 2. LOOSELY TWIST ALL WIRES TOGETHER.
 1. DO NOT INSTALL ITEM ⑥ INTO ITEM ⑤ AT POSITION 5.
- UNLESS OTHERWISE SPECIFIED:
NOTES:

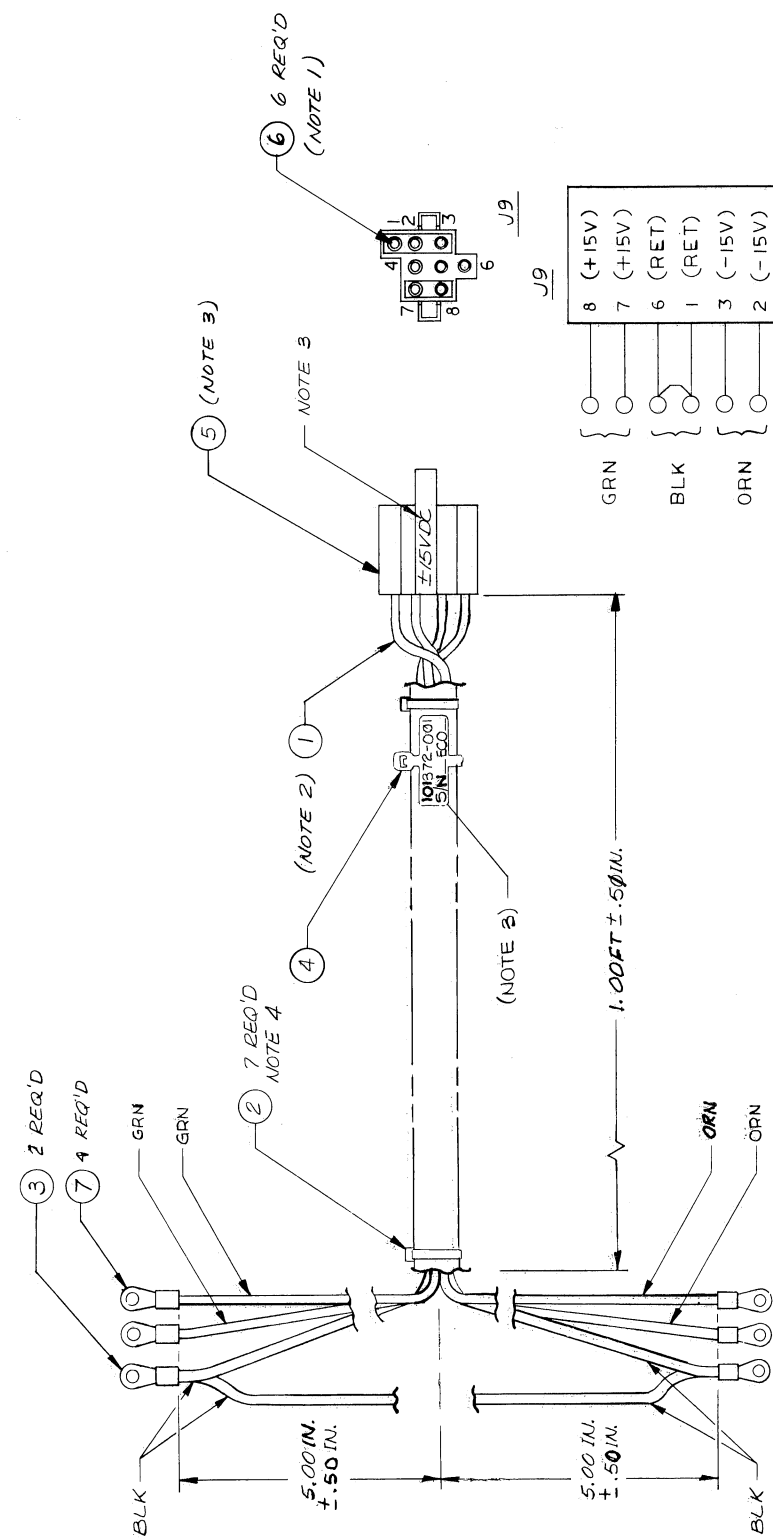
SEE SEPARATE PARTS LIST	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
ASSY, CABLE, POWER, 230 VAC (±15V POWER SUPPLY)	
SIZE CODE IDENT NO	REV
C 53938	101370-001 AI
SCALE NONE	SHEET / OF /



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTRACT NO.	SEE SEPARATE PARTS LIST
TOLERANCES ON .XX ±	DRAWN <i>A. Early</i> 8-2-77	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
.XXX ±	CHECKED <i>AT</i> 5 AUG 77	ASSY, CABLE, POWER, +12V
ANGLES ±	MECH J. J. MO. 8-15-77	SIZE CODE IDENT NO C 53938
✓	ELEC <i>Per Specs</i> 15AUG-77	SCALE NONE
	PROJ. ENG	REV NC
	APPROVED J. J. MO. 8-15-77	REV NC
MATERIAL		SIZE OF 1
SEE PARTS LIST		
APPLICATION		
200152-100 NOV/VIEW		
NEXT ASSY USED ON		

- PLACE ITEM 2 APPROX. EVERY 3 INCHES
- MARK +12VDC ON CABLE AS SHOWN, ALSO MARK ASSY NO., SIN, ECO NO., & CONNECTOR "J22" USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.
- LOOSELY TWIST ALL WIRES TOGETHER. DO NOT INSTALL ITEM 6 INTO ITEM 5 AT POSITIONS 2, 3, 7 & 8. UNLESS OTHERWISE SPECIFIED: NOTES:

ZONE	LTR	DESCRIPTION	DATE	APPROVED
—	NC	ADDED ITEMS 4, ADDED WIRE COLORS. ADDED MARKING INFO. THIS DWG FORMALLY RELEASED TO NC REV STATUS.	8-14-77	R Speir
AI	AI	ADDED ITEM 7.	8-31-77	J Pino



SCHEMATIC

1. PLACE ITEM ② APPROX EVERY 3 INCHES.
 2. MARK ±15VDC ON CABLE APPROX AS SHOWN
 3. MARK ASSY NO., S/M, ECO NO., CONNECTOR "J9" USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.
 4. DO NOT TWIST ALL WIRES TOGETHER.
 5. DO NOT INSTALL ITEM ⑥ INTO ITEM ⑤ AT POSITIONS 4 & 5.
- UNLESS OTHERWISE SPECIFIED:
NOTES:

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTRACT NO.	
XX ±		DRAWN R. Bailey 8-3-77	
.XXX ±		CHECKED J. Pino 5 AUG 77	
ANGLES ±		MECH J. Pino 8-15-77	
✓		ELEC J. Pino 15 AUG 77	
		PROJ. ENG J. Pino 8-15-77	
		APPROVED J. Pino 8-15-77	
200153-100	NOV/OVIEW	SEE SEPARATE PARTS LIST	
NEXT ASSY	USED ON	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
APPLICATION		ASSY, CABLE, POWER, ±15V	
		SIZE	REV
		C 53938	101372-001 AI
		SCALE	SHEET 1 OF 1

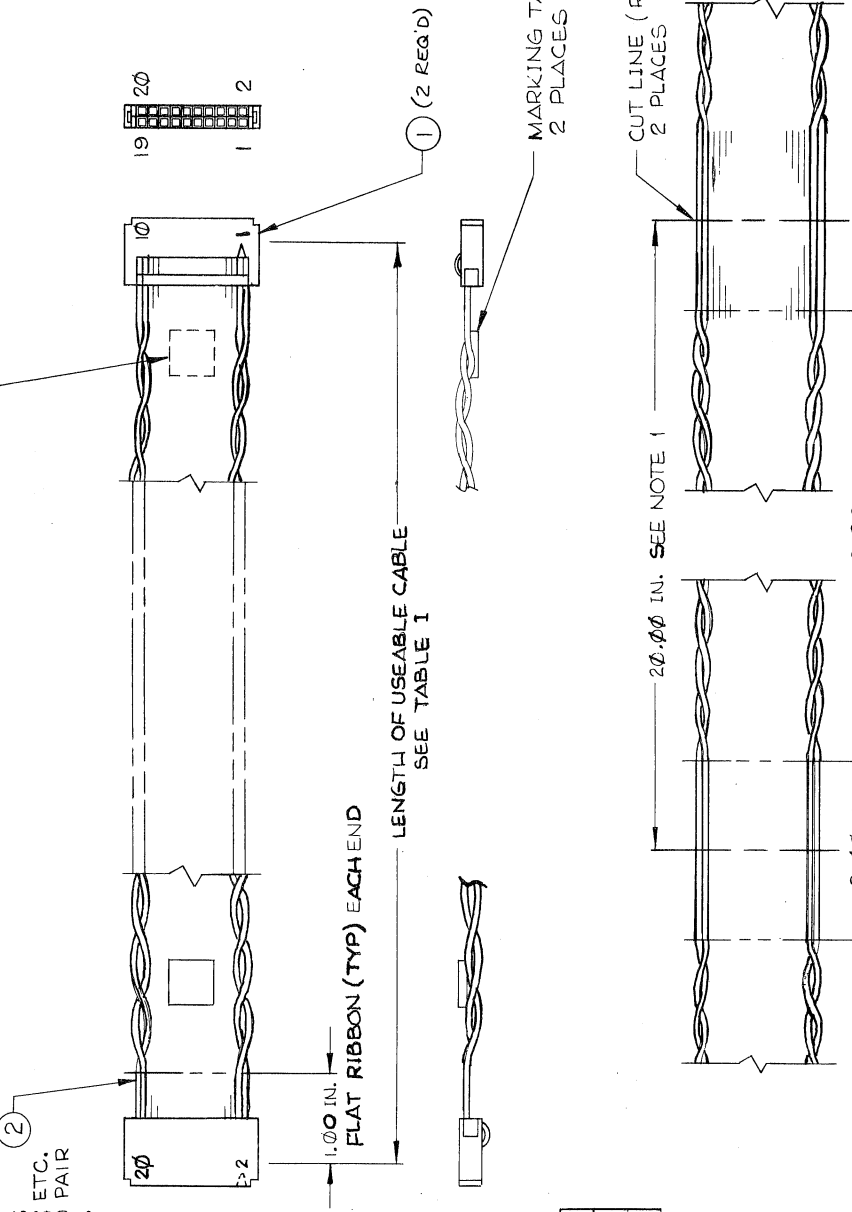
3 2 1

D C B A

B101372-001

ZONE	LTR	REVISIONS			
		DESCRIPTION	DATE	APPROVED	

MARKING TAG, CABLE (2 PLACES)
FAR SIDE MARKING DESIGNATIONS
SPECIFIED AT NEXT LEVEL OF ASSEMBLY.



BRN, BEIGE; RED, BEIGE;
ORN, BEIGE; YEL, BEIGE; ETC.
(TYP. EXAMPLE TWISTED PAIR
COLOR COMBINATIONS).

TABLE I

E&S PART NO.	CABLE LENGTH	ECO
101374 - 002	3 FT 4 IN	NC
101374 - 004	5 FT	NC

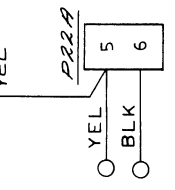
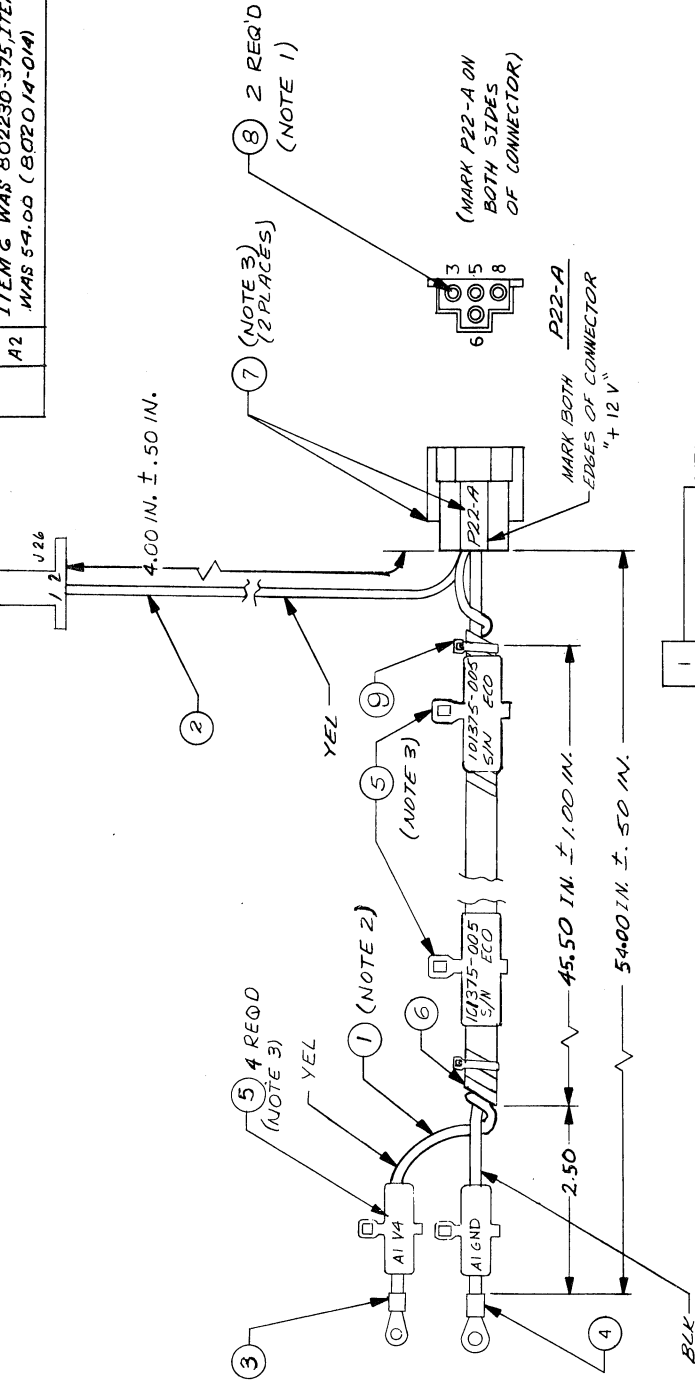
NOTES:
1. WHEN CUTTING CABLE, CUT LINES ARE
IN 20 INCH INCREMENTS. CUT TO BE
CENTERED IN 2.0 INCH FLAT RIBBON
AREA. (SEE FIG. A).
SEE TABLE I FOR EXACT CABLE LENGTHS.

TYPICAL CABLE DIMENSIONS
FIG. A

SEE SEPARATE PARTS LIST		CONTRACT NO.	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		DRAWN ADS/HJW 6-9-77	
ASSY, CABLE FLAT TWISTED (SCOPE DRIVER)		CHECKED Jim P 8-15-77	
SIZE CODE IDENT NO C 53938		MECH	
SCALE NAME ET 1 OF 1		ELEC 25/10/15 15/10/17	
REV NC		PROJ. ENG	
REVISIONS		APPROVED Jim P 8-15-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		MATERIAL SEE PARTS LIST	
TOLERANCES ON .XX ± .50		NONVIEW	
.XXX ±		NEXT ASSY USED ON	
ANGLES ±		FINISH	
APPLICATION			

B 101374-TAB

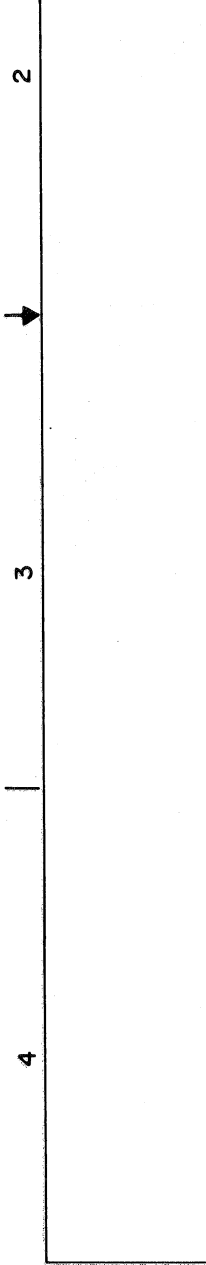
ZONE LTR	DESCR.	DATE	APPROVED
A1	EXTENSIVE DWG CHANGES ADDED ITEMS 9, 10, & 11 TO P/L.	9-1-77	R. Spivey 9/19/77
A2	ITEM 6 WAS 802230-375, ITEM 1 WAS 5-9-00 (802014-014)	2-7-78	R. Spivey 9 Feb 78



3. ALL MARKING ON CONNECTOR AND TAGS TO BE DONE USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO., NO. 35A OR EQUIVALENT.
 2. LOOSELY TWIST ALL WIRES TOGETHER.
 1. DO NOT INSTALL ITEM 8 INTO ITEM 7 AT POSITIONS 3 AND 8.
 UNLESS OTHERWISE SPECIFIED:
 NOTES:

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTRACT NO.	
TOLERANCES ON	.XX ± .50	DRAWN	8-3-77
ANGLES ±	✓	CHECKED BY	5 AUG 77
		MECH	8-13-77
		ELEC	15 AUG 77
		PROJ. ENG	
		APPROVED	8-15-77
MATERIAL	SEE PARTS LIST	SEE SEPARATE PARTS LIST	
FINISH		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
200/52-100	NOV/01/14	ASSY, CABLE POWER, +12V	
NEXT ASSY	USED ON	SIZE CODE IDENT NO	REV
	APPLICATION	C 53938	101375-005
		SCALE/NOTE	A2
			SHEET 1 OF 1

REVISIONS			
ZONE LTR	DESCRIPTION	DATE	APPROVED
A1	ADDED ITEM 8 ITEM 4 OTY WAS 2. CHANGED CABLE LENGTH 2 PLACES.	9-1-77	<i>[Signature]</i> 9/19/77
A2	ITEM 5 WAS 802230-375 MW	2-7-78	<i>[Signature]</i> 9/26/78



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTRACT NO.
.XX ±	DRAWN <i>R. Boyle</i> 8-3-77
XXX ±	CHECKED <i>[Signature]</i> 5 AUG 77
ANGLES ±	MECH J.P.M.D. 8-15-77
✓	ELEC <i>[Signature]</i> 15 AUG 77
MATERIAL	PROJ. ENG
SEE PARTS LIST	APPROVED <i>[Signature]</i> 9-15-77

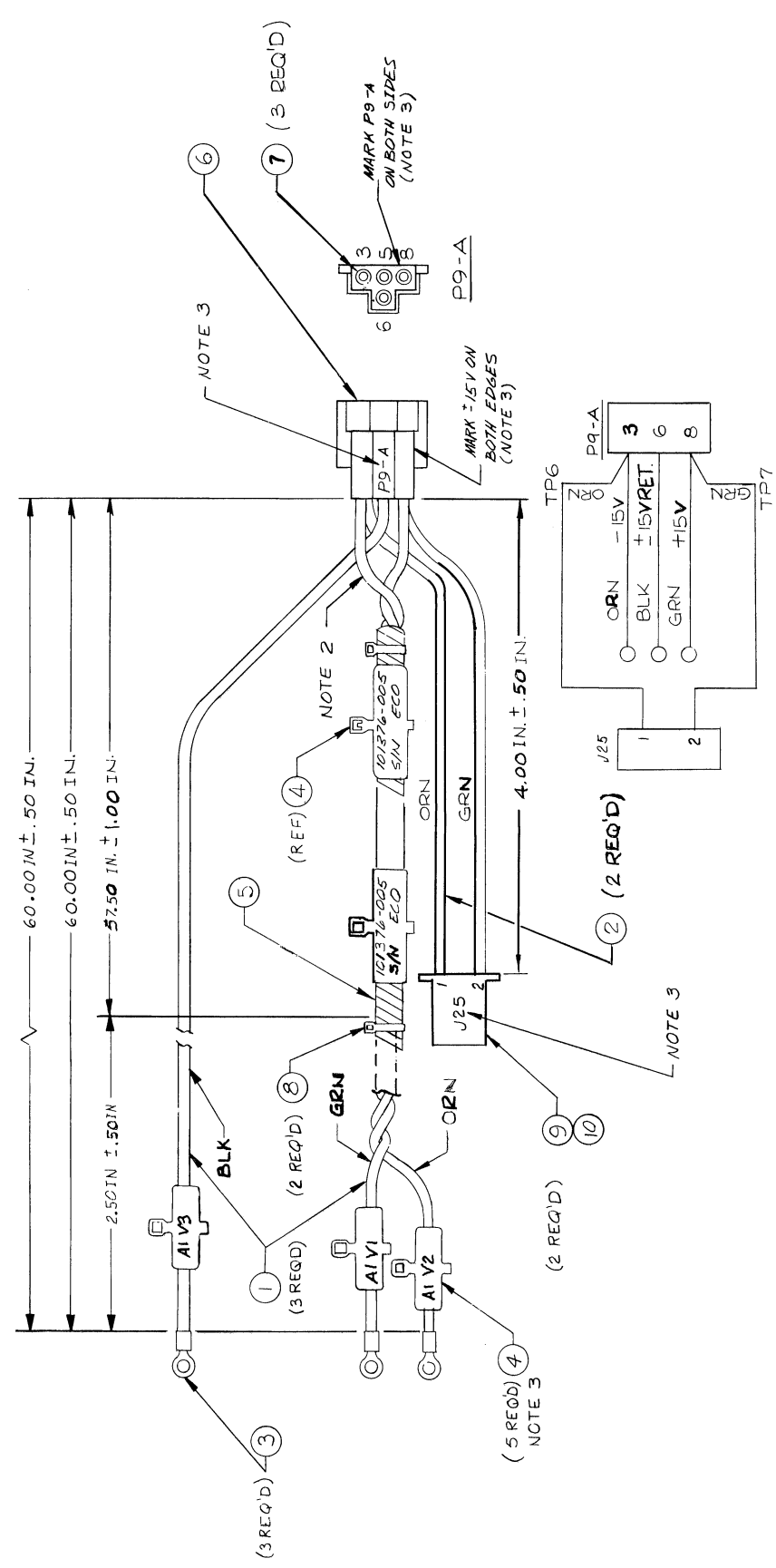
SEE SEPARATE PARTS LIST	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
ASSY, CABLE POWER, +12V	REV A2
SIZE CODE IDENT NO C 53938 101375-007	SCALE NONE
REV	SF
OF	I

3. ALL MARKING ON CONNECTOR AND TAGS, TO BE DONE USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 3514 OR EQUIVALENT.
2. LOOSELY TWIST ALL WIRES TOGETHER.
1. DO NOT INSTALL ITEM (7) INTO ITEM (6) AT POSITIONS 2 AND 7.
- UNLESS OTHERWISE SPECIFIED:
- NOTES:

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTRACT NO.
.XX ±	DRAWN <i>R. Boyle</i> 8-3-77
XXX ±	CHECKED <i>[Signature]</i> 5 AUG 77
ANGLES ±	MECH J.P.M.D. 8-15-77
✓	ELEC <i>[Signature]</i> 15 AUG 77
MATERIAL	PROJ. ENG
SEE PARTS LIST	APPROVED <i>[Signature]</i> 9-15-77

SEE SEPARATE PARTS LIST	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
ASSY, CABLE POWER, +12V	REV A2
SIZE CODE IDENT NO C 53938 101375-007	SCALE NONE
REV	SF
OF	I

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A1	EXTENSIVE PARTS LIST & DWG CHANGES	9/9/77	J Pmo
	SC	ADD NOTE 4. CHANGE WIRE COLOR	9/27	P Spain
	A3	ITEM 5 WAS 80223D-375 MW	2-7-78	P Spain



4. GRN WIRE MARKED A1V1.
ORN WIRE MARKED A1V2.
- 3 ALL MARKING ON CONNECTORS & TAGS TO BE DONE USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.
2. LOOSELY TWIST GRN & ORN WIRES AS SHOWN.
1. DO NOT INSTALL ITEM (7) INTO ITEM (6) AT POSITION 5.
UNLESS OTHERWISE SPECIFIED:
- NOTES:

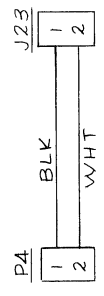
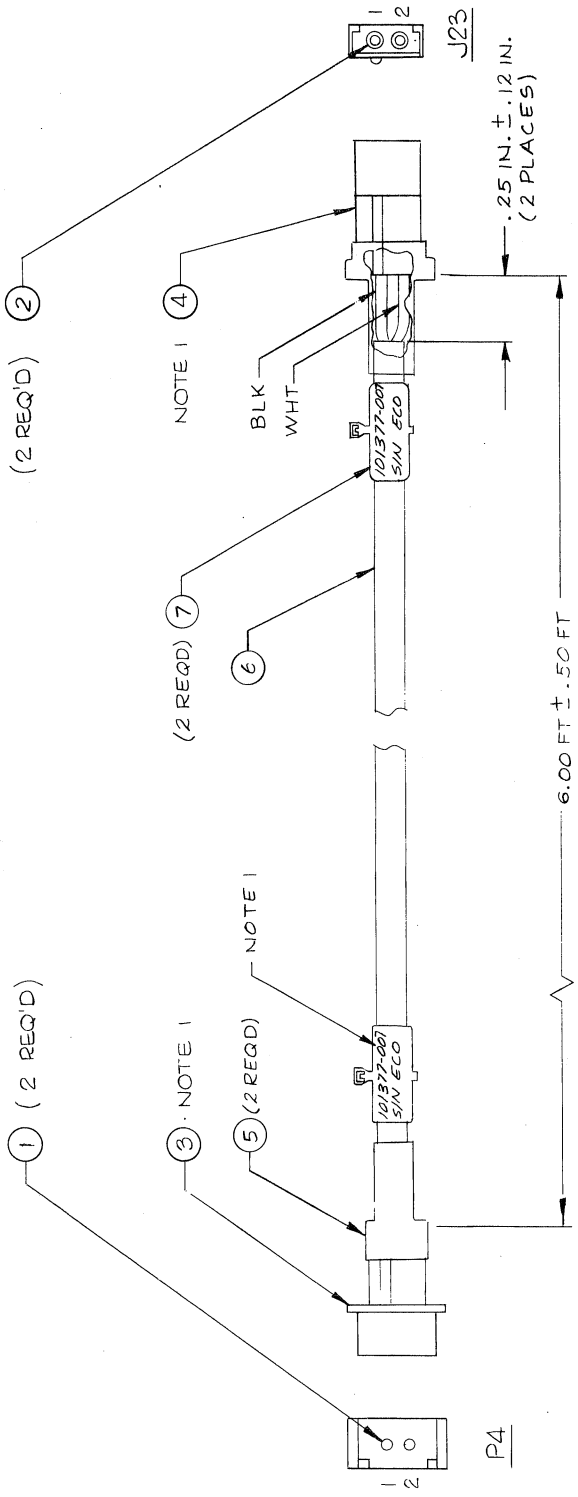
SCHMATIC

SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN ADS/SLMWAY 8-9-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		MECH 8-15-77	
TOLERANCES ON		ELEC P Spain 15 AUG 77	
XX ± .50		APPROVED J Pmo 8-15-77	
.XXX ±			
ANGLES ±			
✓			
MATERIAL		SIZE CODE IDENT NO	
SEE PARTS LIST		C 53938	
FINISH		SCALE NONE	
200/53-100 NOV/07/EW		SHEET 1 OF 1	
NEXT ASSY USED ON		REV	
APPLICATION		101376-005	
		A3	
		ASSY, CABLE, POWER (±15V POWER SUPPLY)	

D C B A

101376-005

REVISIONS		
ZONE	LTR	DESCRIPTION
B-2	AI	CHANGED DIMENSION FROM 7.00 TO 6.00
		DATE 9/19/77
		APPROVED <i>[Signature]</i>



SCHMATIC

SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO. DRAWN BY <i>[Signature]</i> 8-10-77 CHECKED BY <i>[Signature]</i> 8-15-77 MECH ELEC <i>[Signature]</i> 1/5/80/677 PROJ. ENG APPROVED BY <i>[Signature]</i> 8-15-77	
ASSY, CABLE THERMAL SENSOR	
SIZE CODE IDENT NO C 53938	REV AI
SCALE NONE	101377-007
	SHEET 1 OF 1

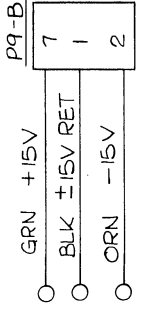
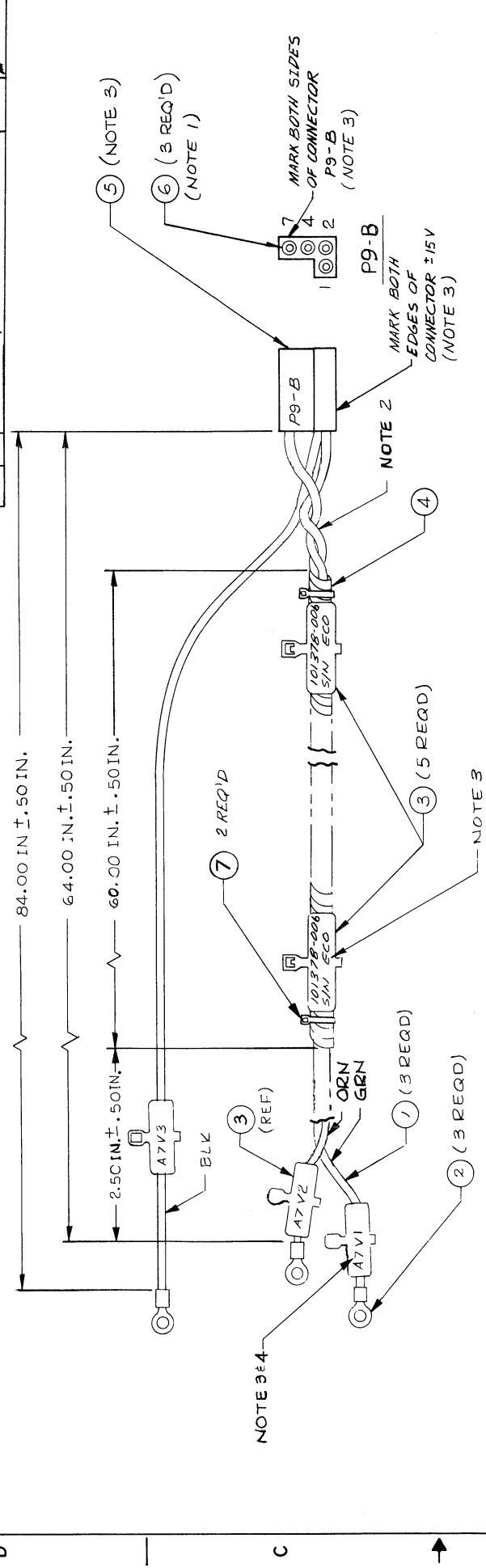
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	TOLERANCES ON
.XX ±	
.XXX ±	
ANGLES ±	
✓	
MATERIAL	SEE PARTS LIST
200104-100	NOV/VIEW
NEXT ASSY	USED ON
APPLICATION	

1. MARK ASSY NO., S/N, ECO NO., CONNECTORS "P4" & "J23", USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.

UNLESS OTHERWISE SPECIFIED:

NOTES

ZONE LTR	DESCRIPTION	DATE	APPROVED
A1	EXTENSIVE PARTS LIST AND DRAWING CHANGES	4-1-77	[Signature]
A2	ADD NOTE 4. CHANGE WIRE COLOR	9-27-77	[Signature]
A3	ITEM 4 WAS 802230-375 64.00 WAS 48.00, 60.00 WAS 45.50 MW	2-7-78	[Signature]



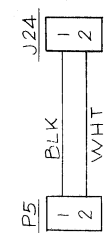
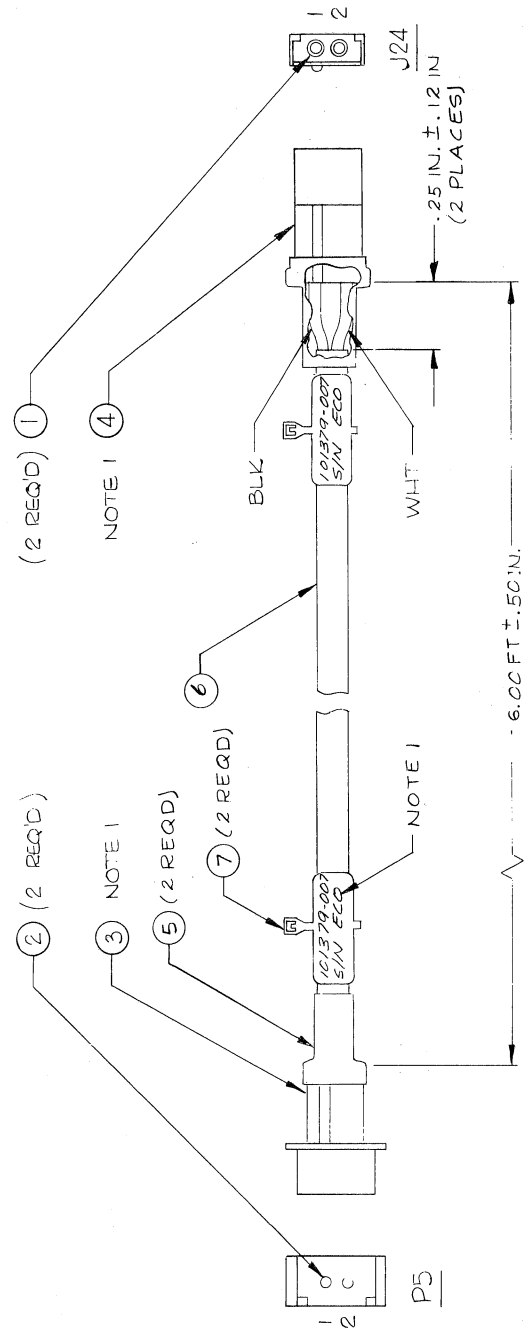
SCHEMATIC

4. GRN WIRE MARKED ATV1
ORN WIRE MARKED ATV2
 3. MARKING ON CONNECTOR AND TAGS TO BE DONE USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT .09 HIGH CHARACTERS.
 2. LOOSELY TWIST GRN AND ORN WIRE TOGETHER.
 1. DO NOT INSTALL ITEM ⑥ INTO ITEM ⑤ AT POSITION 4.
- UNLESS OTHERWISE SPECIFIED:
- NOTES:

SEE SEPARATE PARTS LIST		CONTRACT NO.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON		MATERIAL		APPLICATION	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		DRAWING NO. 101378-006		.XX ±		200153-100 NOVONVIEW		NEXT ASSY USED ON	
ASSY, CABLE, POWER SUPPLY		CHECKED BY [Signature] 8-15-77		.XXX ±		SEE PARTS LIST		FINISH	
(± 15V POWER SUPPLY)		MECH [Signature] 15 AUG 77		ANGLES ±		NOVONVIEW		NOVONVIEW	
SIZE CODE IDENT NO		PROJ. ENG [Signature] 8-15-77		✓		NOVONVIEW		NOVONVIEW	
C 53938		APPROVED BY [Signature] 8-15-77				NOVONVIEW		NOVONVIEW	
SCALE NONE						NOVONVIEW		NOVONVIEW	
101378-006						NOVONVIEW		NOVONVIEW	
REV						NOVONVIEW		NOVONVIEW	
A3						NOVONVIEW		NOVONVIEW	
SHEET 1 OF 1						NOVONVIEW		NOVONVIEW	

4 3 2 1

REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	
B-2	AI	CHANGED DIMENSION FROM 7.00 TO 6.00	R Space



SCHMATIC

1. MARK ASSY NO., S/N, ECO NO., CONNECTORS P5 & J24 USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.
UNLESS OTHERWISE SPECIFIED:

NOTES

SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN ADS/HUMWAY 8-10-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CHECKED J Pind 8-15-77	
TOLERANCES ON		MECH	
.XX ±		ELEC 15 AUG 77 R Space	
.XXX ±		PROJ. ENG	
ANGLES ±		APPROVED J Pind 8-15-77	
MATERIAL		SIZE CODE IDENT NO	
SEE PARTS LIST		C 53938	
NEXT ASSY USED ON		REV	
200104-100		101379-007	
APPLICATION		A1	
THERMAL SENSOR		SCALE NONE	
SHEET		OF 1	

D C B A

101379-007 B

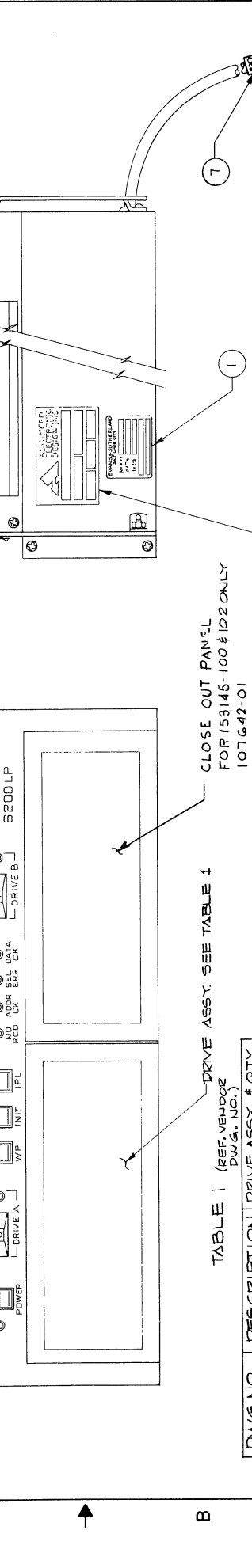
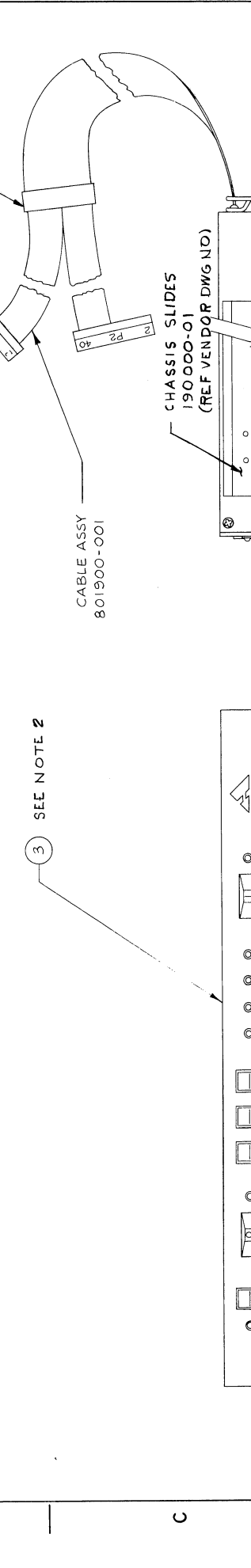


TABLE 1 (REF. VENDOR DWG. NO.)

DWG NO.	DESCRIPTION	DRIVE ASSY. #	QTY
153145-100	SINGLE DRIVE 6042	05481-03	(1)
153145-101	DUAL DRIVE 6042	05481-03	(2)
153145-102	SINGLE DRIVE 5042	05481-04	(1)
153145-103	DUAL DRIVE 5042	05481-04	(2)

2. FOR UNALTERED ITEM (E&S PROCUREMENT ONLY) REFER TO E&S PRODUCT STRUCTURE, FOR ALTERED ITEM REFER TO ASSEMBLY DRAWING 801901-001 FLOPPY DISK ASSY.

1. ITEMS 2 AND 4 THRU 8 ARE SHOWN FOR ASSEMBLY INFORMATION ONLY. REFER TO ASSEMBLY DRAWINGS 801900-001 & 801901-001 FOR ACTUAL MODIFICATION DETAILS.

NOTES:

ALTERED ITEM DRAWING

<p>EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112</p>		<p>CONTRACT NO. DRAWN: J. BAHLMANN 3-23-78</p>	<p>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .XXX ± ANGLES ± ✓</p>	<p>MECH ELEC PROJ. ENG APPROVED</p>	<p>SIZE CODE IDENT NO C 53938</p>	<p>REV SEE TABLE</p>
<p>ASSY, FLOPPY DISC STORAGE 6200P, W/SLIDE RAILS</p>		<p>CHECKED: <i>[Signature]</i> 4 APR 78 ELEC <i>[Signature]</i> 2 MAY 78 PROJ. ENG <i>[Signature]</i> 2 MAY 78</p>	<p>MATERIAL SEE NOTE 2.</p>	<p>200101-XXX NOV/OVIEW NEXT ASSY USED ON</p>	<p>SCALE NONE</p>	<p>SHEET 1 OF 1</p>

C

C

C

C

C

REV TABLE			REVISIONS				
-102	-101	-100	ZONE	LTR	DESCRIPTION	DATE	APPROVED
		A2	A5		CHANGED RESISTOR 803300-181 FROM 91.6 96.2 TO 81.72 81.80.	9-12-77	R Spais 12 SEP 77
		A3	—		WIRE CHANGE ONLY.	9-12-77	R Spais 12 SEP 77
		A4	A5		CHANGED RESISTOR 803300-221 FROM 87.72 87.80 TO 74.03 74.02.	9-12-77	R Spais 12 SEP 77
		A5	—		WIRE CHANGE ONLY	9-27-77	R Spais 28 SEP 77
		A6	—		WIRE CHANGES ONLY	11-1-77	J Duno 1 NOV 77
		A7	—		WIRE CHNG, INCORPORATED ECOA2 TO SUBASSY 101400-103	11-28-77	Ron Spais 13 DEC 77
		A8			REMARKED VCC & GND ZONE 2C, 2B MW	2-15-78	Ron Spais 16 Feb 78
		A9			REV UPDATE W/NO DWG CHNG	3-15-78	Ron Spais 16 APR 78
A2	A1	B1			REV UPDATE W/NO DWG CHNG	6 DEC 78	Ron Spais
A3					REV UPDATE W/NO DWG CHNG	1-22-79	Ron Spais

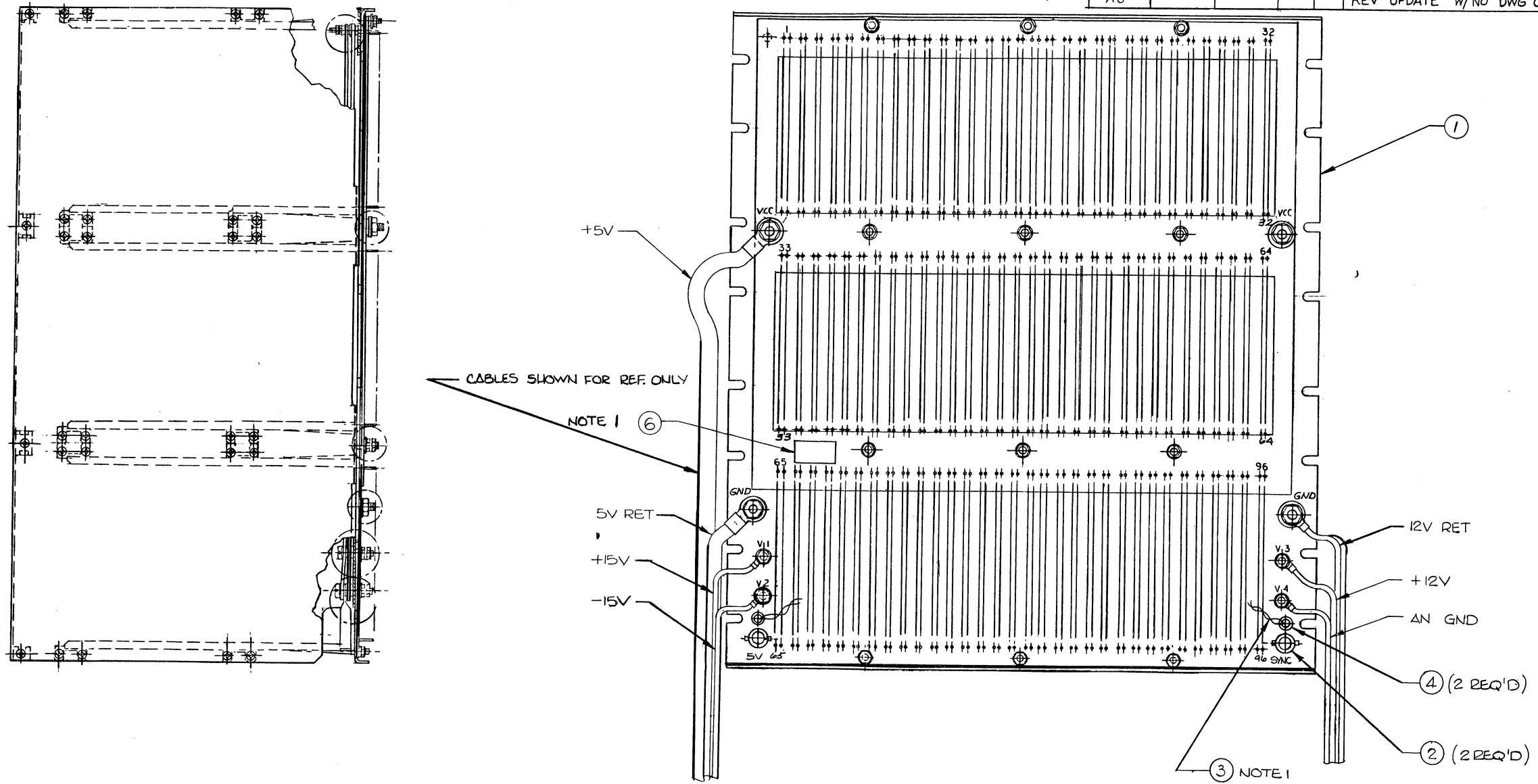


TABLE II
SEE NOTE 1

RESISTOR NO.	VALUE	LOCATION
803300-181	180 Ω	81.72 81.80
803300-221	220 Ω	74.03 74.02
80300-101	100 Ω	75.2 75.3 76.2 76.3 77.2 77.3 69.2 69.3

TABLE I

	SIG	GND
SYNC	83.9	83.11
5 V	65.2	65.1

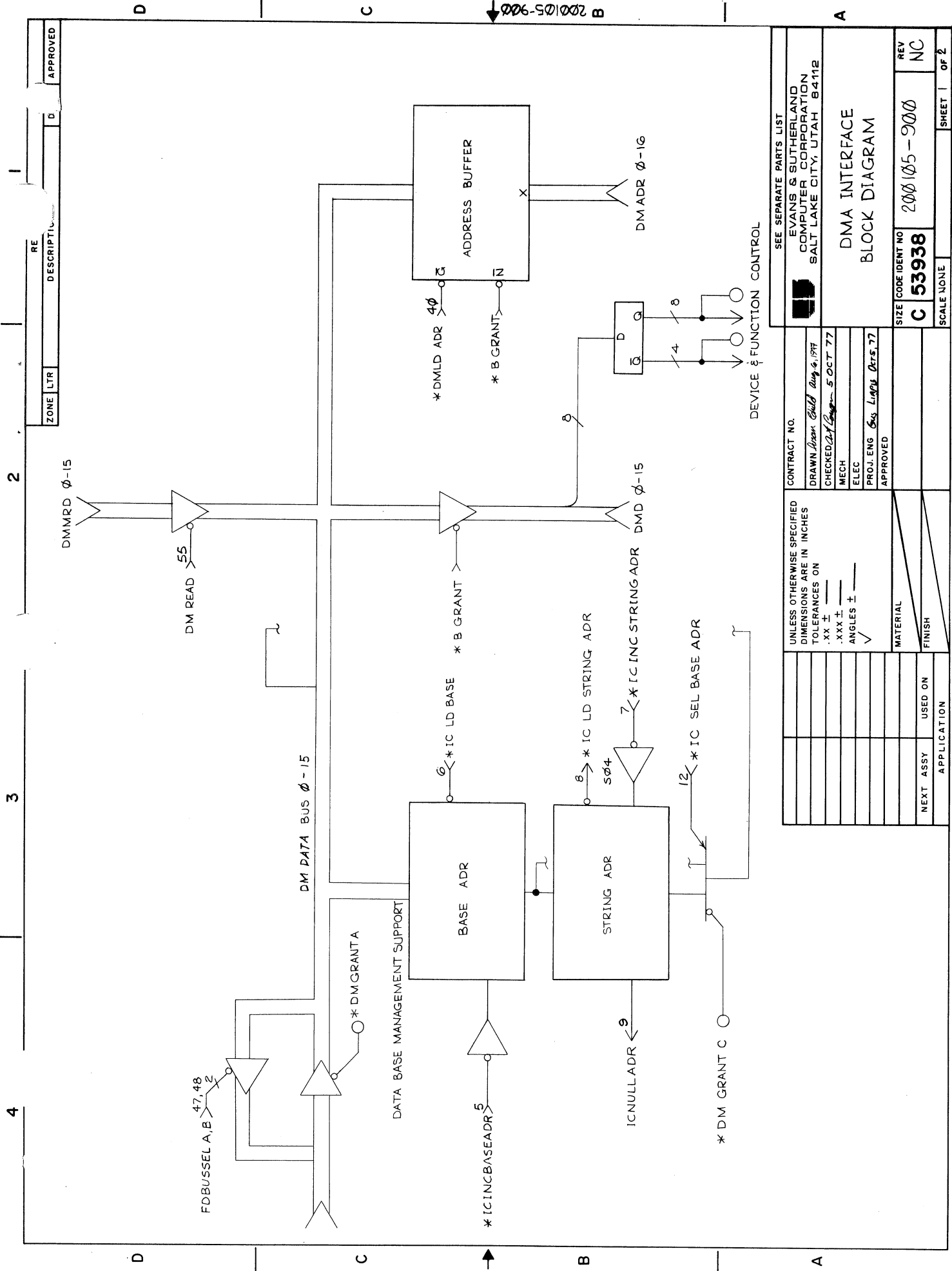
WIRE	LOCATION
802068-009	23.53 32.72

NOTES:
1. INSTALL (6) AFTER WIRE WRAPPING

SEE SEPARATE PARTS LIST

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON .XX± — .XXX± — ANGLES± — ✓ MATERIAL FINISH	CONTRACT NO. DRAWN AOS 7-21-77 CHECKED [Signature] 11 AUG 77 MECH [Signature] ELEC Ron Spais 11 AUG 77 PROJ. ENG. DESIG. AUTH. APPROVER [Signature] 11 AUG 77	TITLE ASSY, BACKPANEL 1, NOVOVIEW SP	REV. SEE TABLE
APPLICATION	DWG 200100-TAB	SCALE 1/8	SHEET 1 OF 1





2006-501002 B

ZONE LTR	RE	APPROVED
D	D	D

SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
DMA INTERFACE BLOCK DIAGRAM	
CONTRACT NO.	SIZE CODE IDENT NO
DRAWN <i>James Beld</i> Aug 6, 1977	C 53938
CHECKED <i>Pat Long</i> 5 OCT 77	2006105-900
MECH	SCALE NONE
ELEC	SHEET 1 OF 2
PROJ. ENG <i>Chris Lindley</i> Oct-5-77	REV NC
APPROVED	

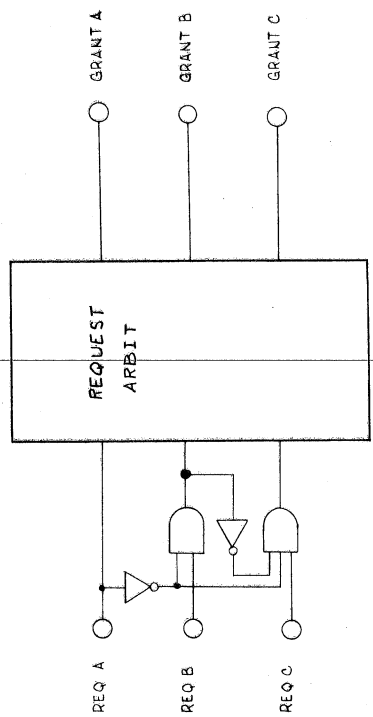
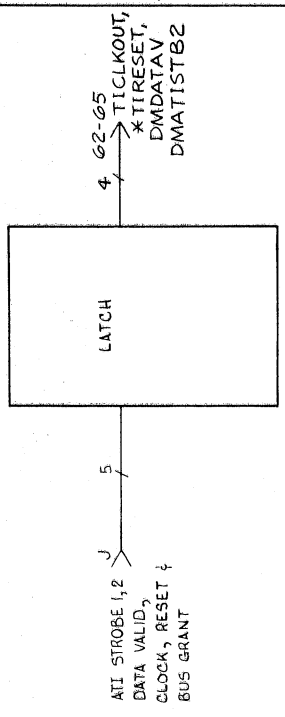
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON	
.XX ±	
.XXX ±	
ANGLES ±	
MATERIAL	
FINISH	
NEXT ASSY	USED ON
APPLICATION	

4 3 2 1

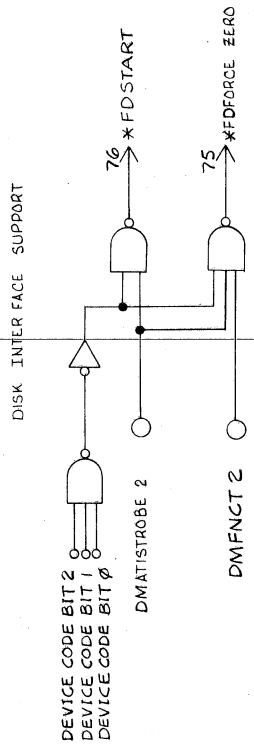
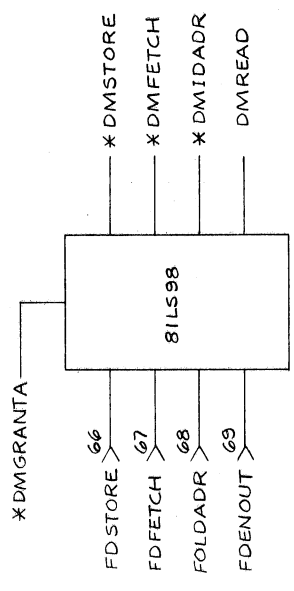
REVISIONS		DATE	APPROVED
ZONE	LTR		

INTERFACE CONTROL SIGNAL BUFFERING

MULTI-PORT ACCESS CONTROLLER

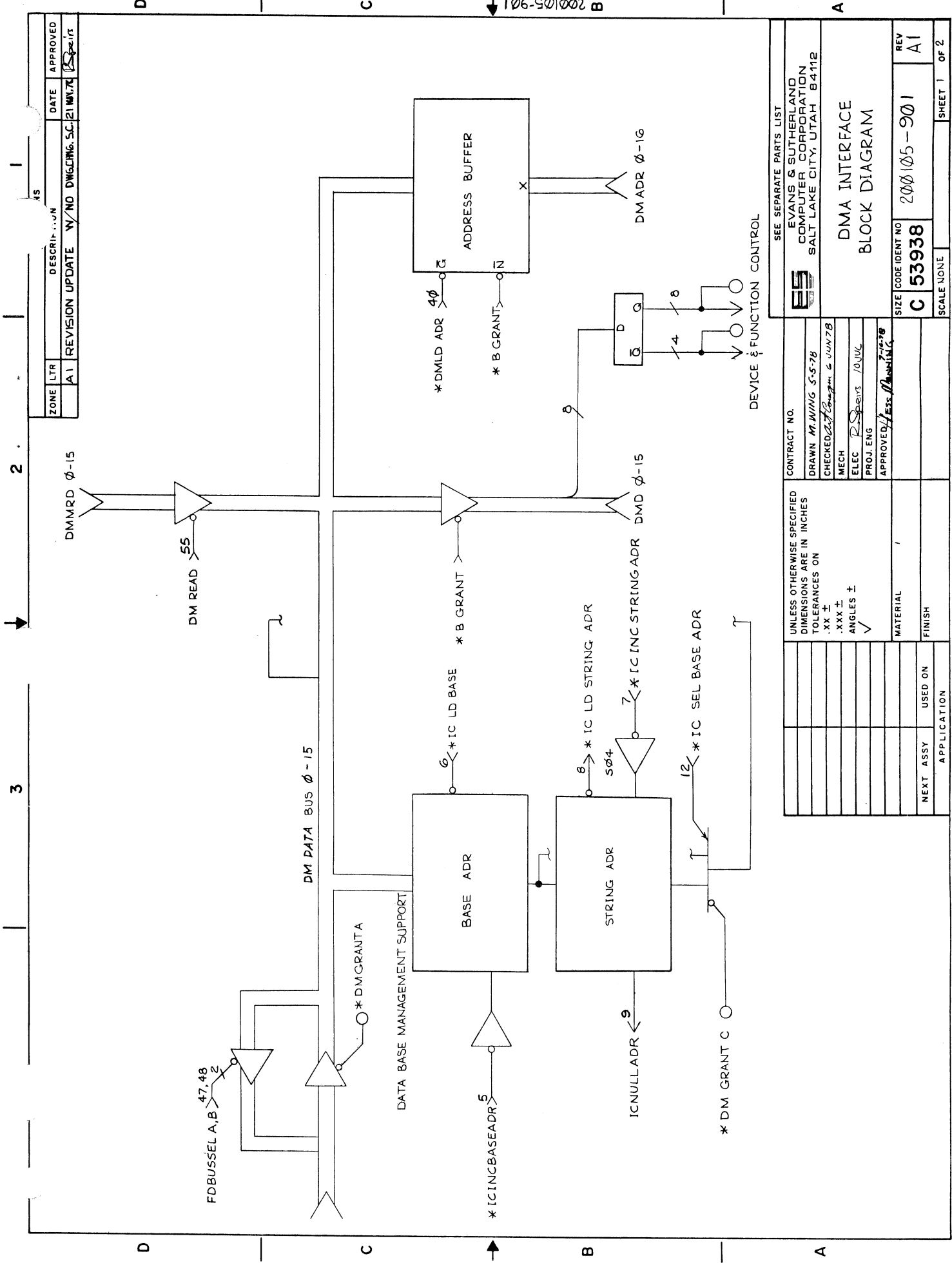


DIO TO DMA BUFFERED SIGNALS



SIZE CODE IDENT NO	REV	REV
C 53938	200105-900	NC
SCALE NONE		PAGE 2 OF 2

B 200105-900



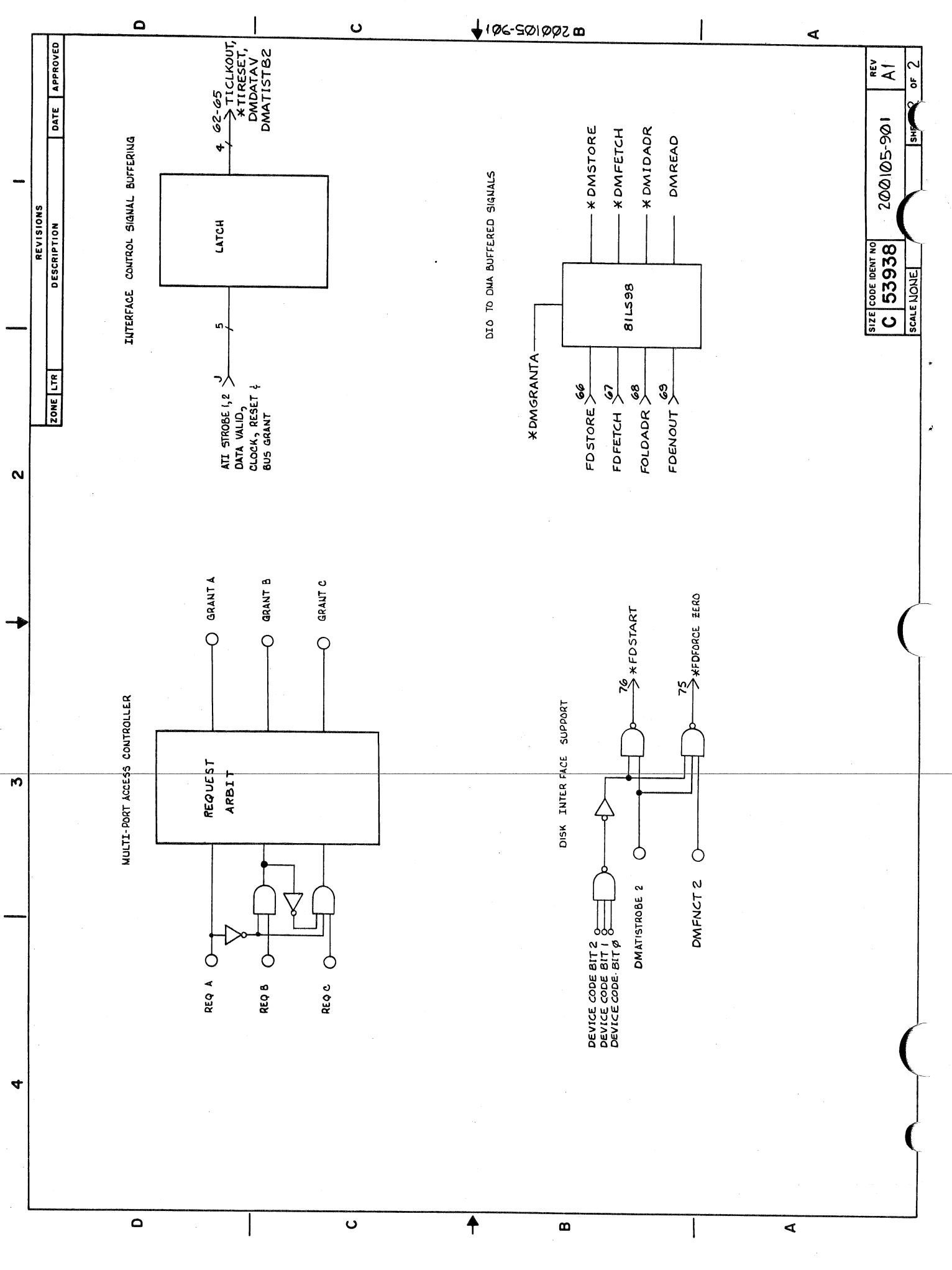
ZONE	LTR	DESCRIPTION	DATE	APPROVED
A1		REVISION UPDATE	W/NO DWG. CHG. SC. 21 MAR 74	[Signature]

D 2 3

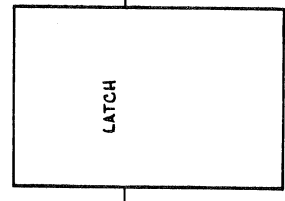
SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.	DRAWN M. WING 5-5-78
MECH	CHECKED [Signature] 6 JUN 78
ELEC	RS [Signature] 10 JUL
PROJ. ENG	APPROVED [Signature] 14 JUL 78
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .XXX ± ANGLES ±	
MATERIAL	FINISH
NEXT ASSY	USED ON
APPLICATION	
SIZE CODE IDENT NO	C 53938
REV	A1
SHEET 1	OF 2

DMA INTERFACE BLOCK DIAGRAM

200105-901



INTERFACE CONTROL SIGNAL BUFFERING



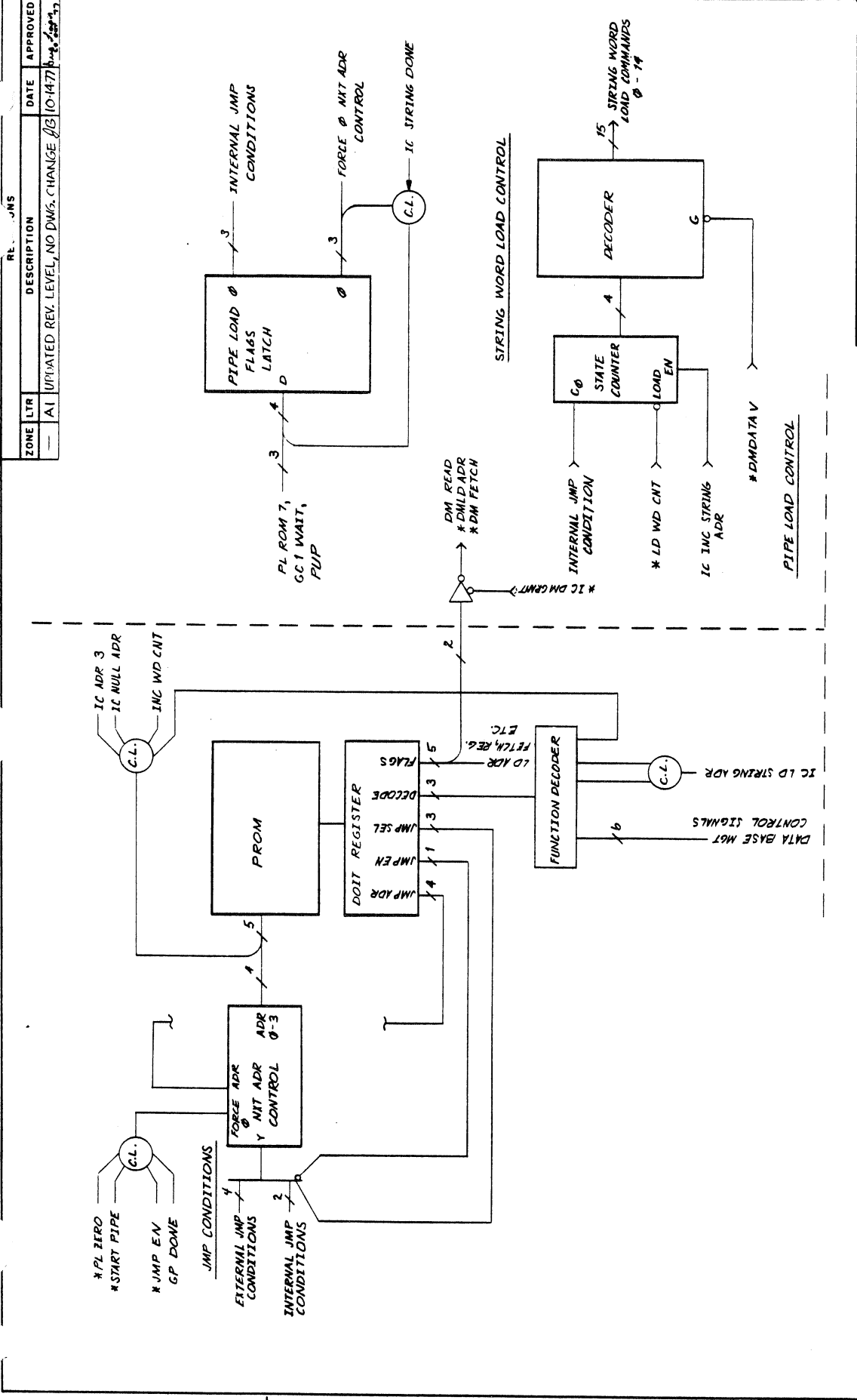
ATI STROBE 1,2
DATA VALID,
CLOCK, RESET &
BUS GRANT

62-65
TICKOUT,
*TIRESET,
DMATAV,
DMATISTB2

DIO TO DMA BUFFERED SIGNALS

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE

SIZE	CODE IDENT NO	REV	REV
C	53938	200105-901	A1
SCALE	NONE	SHEET	OF 2



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
INPUT CONTROL, CABLE W/W (BLOCK DIAGRAM)	
SIZE CODE IDENT NO	REV
C 53938	200106-900 AI
SCALE NONE	SHEET 1 OF 3

CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON
DRAWN D. CARROLL 8-9-72	.XX ±
CHECKED [Signature] 8-31-72	.XXX ±
MECH	ANGLES ±
ELEC	✓
PROJ. ENG [Signature] 8-31-72	MATERIAL
APPROVED	200/30-100 NOVONVIEW
	NEXT ASSY USED ON
	APPLICATION

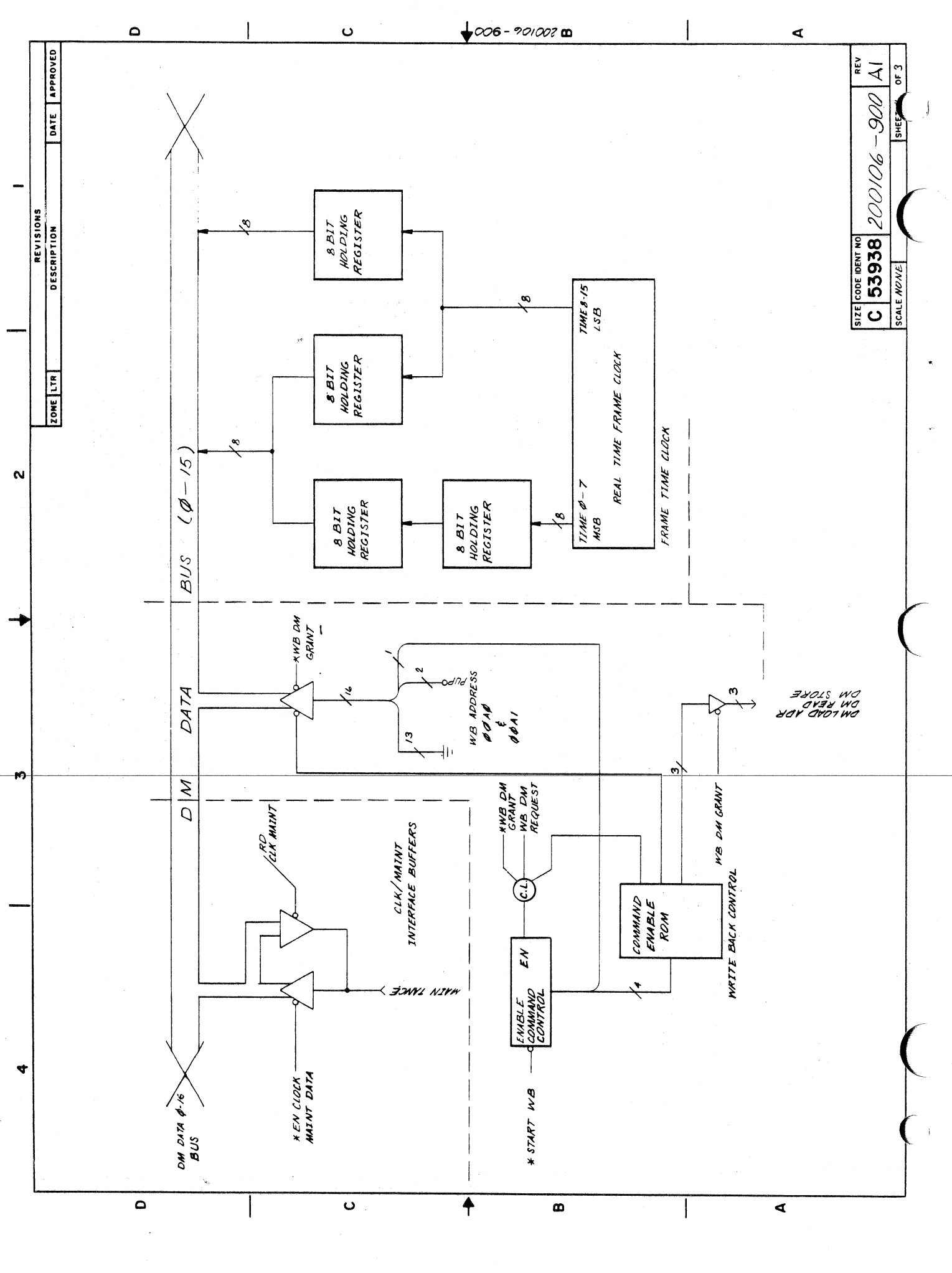
ZONE	REVISIONS	DESCRIPTION	DATE	APPROVED
A1		UPDATED REV. LEVEL, NO DWG. CHANGE	10-14-77	[Signature]

3 2 1

D C B A

REVISIONS			DATE	APPROVED
ZONE	LTR	DESCRIPTION		

SIZE CODE IDENT NO	REV
C 53938	A1
SCALE NONE	SHEET 3 OF 3



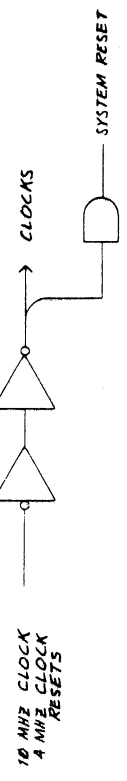
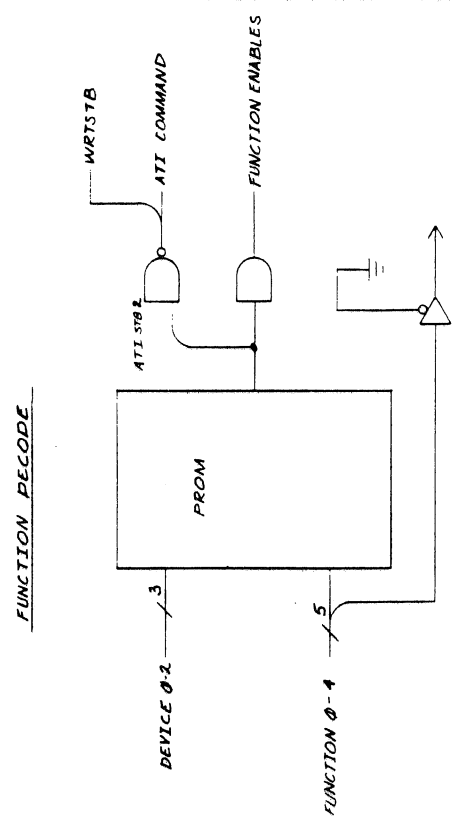
4 3 2 1

D C B A

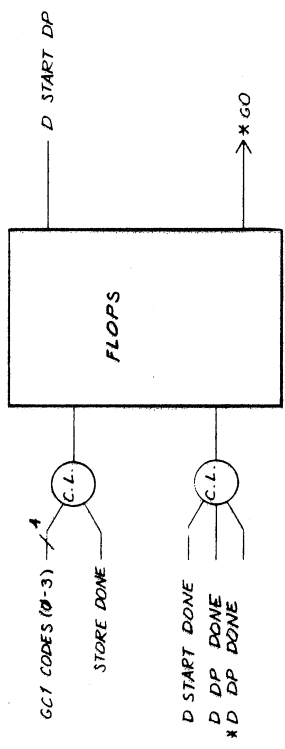
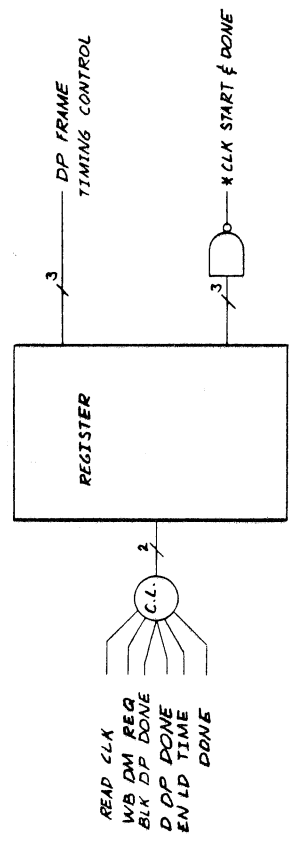
200106-900

REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	

FUNCTION DECODE



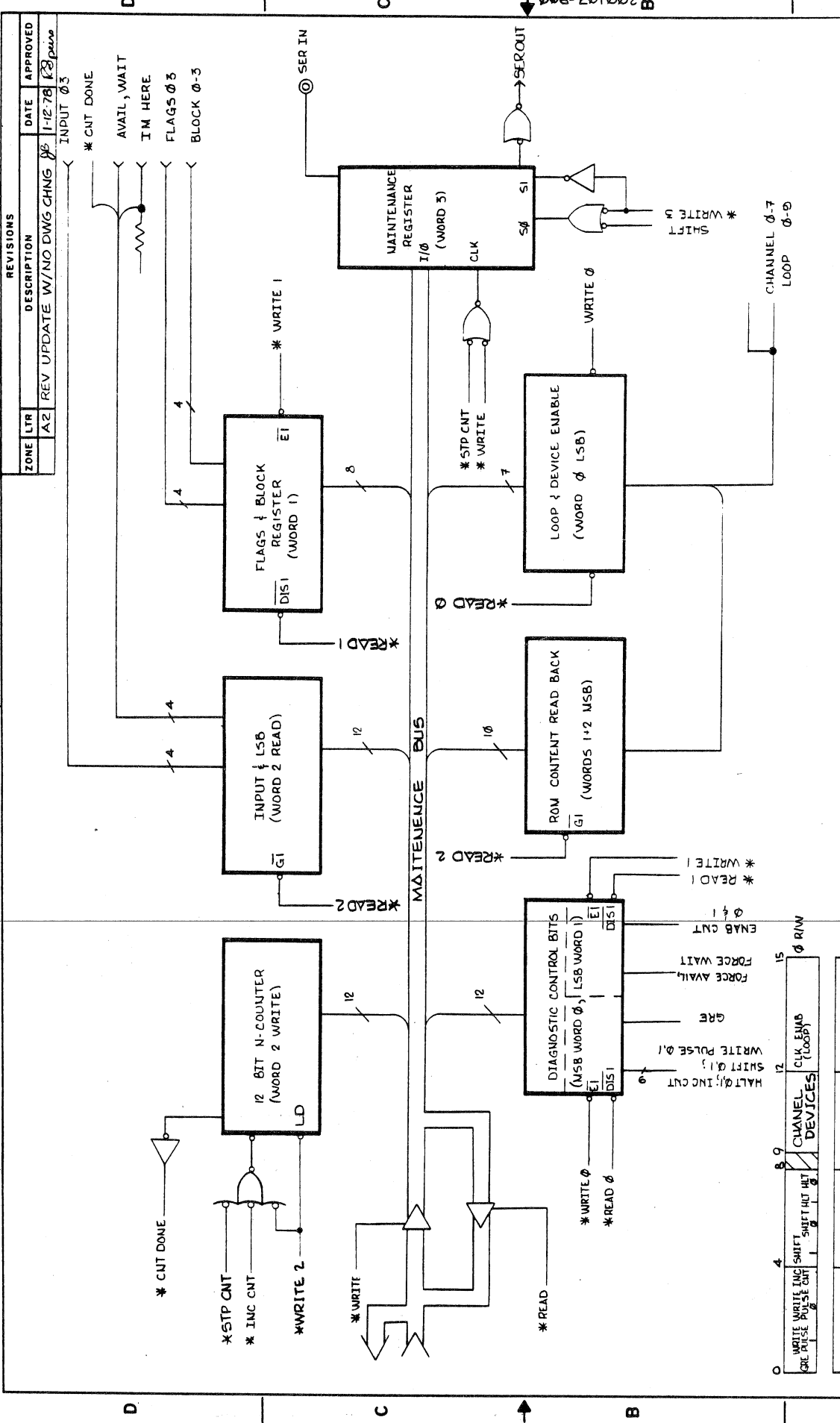
DMA I/F & FUNCTION DECODE



DP FRAME TIMING CONTROL

SIZE	CODE IDENT NO	REV
C	53938	200106-900
SCALE	NAME	SHEET 3 OF 3

005-901002 B



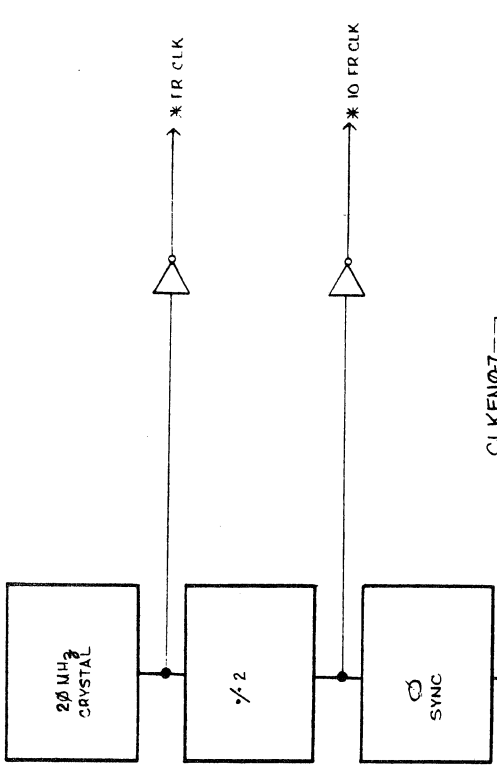
REVISIONS ZONE LTR DESCRIPTION W/NO DWG CHNG DATE APPROVED A2 REV UPDATE 1-12-78 1-12-78	
CONTRACT NO. 200107-900 DRAWN Sutherland 10/1/77 CHECKED [Signature] 17 OCT 77 MECH [Signature] ELEC [Signature] PROJ. ENG [Signature] 20 OCT 77 APPROVED	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .XXX ± ANGLES ±	
SEE SEPARATE PARTS LIST	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
CLOCK/MAINT BLOCK DIAGRAM	SIZE CODE IDENT NO C 53938 REV 200107-900 A3
SCALE NONE	SHEET 1 OF 2
APPLICATION	NOVONVIEW USED ON
MAINTENANCE REGISTER	
12 BIT N-COUNT	
ROM READ BACK	
ROM READ 3	
ROM READ 2	
ROM READ 1	
ROM READ 0	
FLAG BACK	
BLOCK	
CHANNEL DEVICES	

D 4 3 2 1 C B A

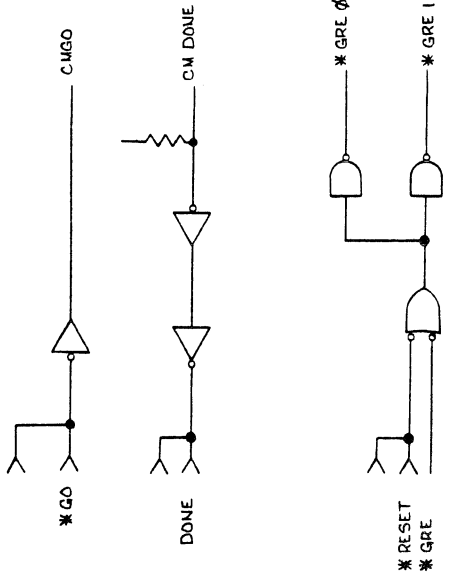
2 3

REVISIONS			
ZONE	DESCRIPTION	DATE	APPROVED

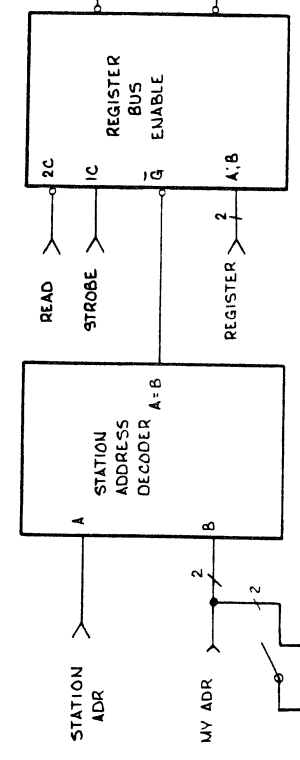
CLOCK GENERATION & DRIVERS



FEED THROUGH SIGNALS



STATION DECODE

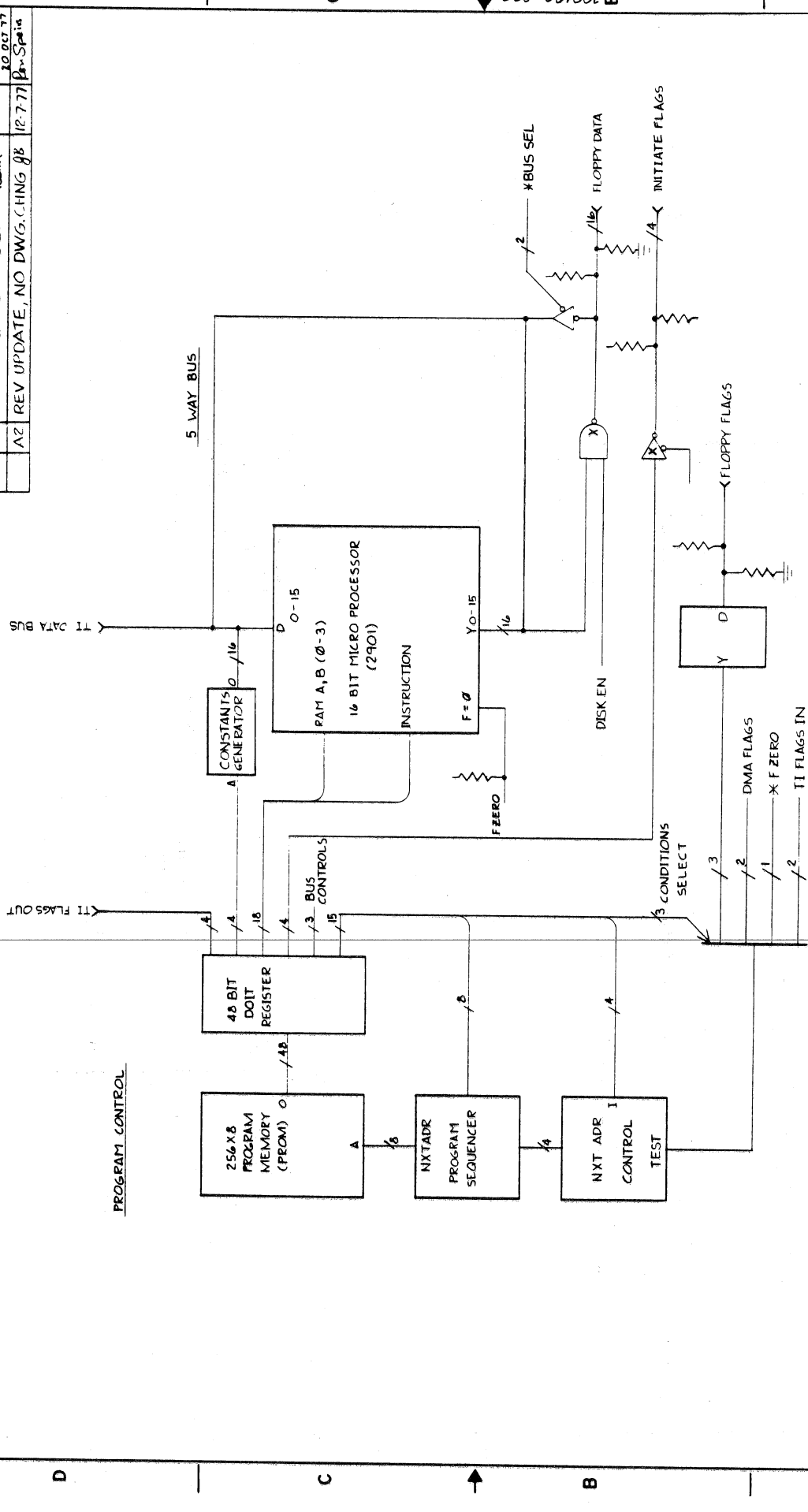


SYNCHRONOUS HALT LOGIC

Inputs: STOP 0, STOP 1, HLT 0, HLT 1, *WRITE 0-3, *READ 0-3.

Output: FORCE BLANK.

REVISIONS			
ZONE LTR	DESCRIPTION	DATE	APPROVED
A1	REVISION UPDATE W/NO DRAWING CHANGE.	9-28-77	<i>[Signature]</i>
A2	REV UPDATE, NO DWG. CHNG. gB	12-7-77	<i>[Signature]</i>



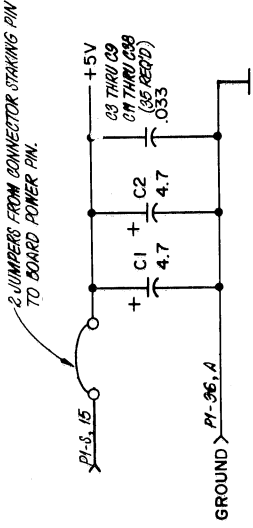
SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN: D. CARROLL 7-27-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CHECKED: <i>[Signature]</i> 8-30-77	
TOLERANCES ON		MECH	
.XX ±		ELEC	
.XXX ±		PROJ. ENG. <i>[Signature]</i> 1/3 Dec 77	
ANGLES ±		APPROVED	
MATERIAL		DISK INTERFACE, FLOPPY, AED, CABLE, W/W	
NEXT ASSY		SIZE CODE IDENT NO	
USED ON		C 53938 200108-900	
APPLICATION		SCALE NONE	
		REV	
		A2	
		SHEET 1 OF 2	

200108-900

4 3 2 1

REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	
-	A1	CHANGED VALUES OF R2, R3, AND C10. ADDED I.C.'S U12, U30, U31 AND U41. DELETED U50.	24 OCT 77 b. Howard

- NEXT MICROINSTRUCTION FROM ADDRESSES A0 THRU A7 ARE TEST POINTS IN LOCATIONS U10, 1, 3, 5, 7 AND U20, 1, 3, 5, 7 RESPECTIVELY.
- ON I.C.'S U63 & U64, PIN 8 IS GROUND AND PIN 2 IS POWER (+5V).
ON ALL 42 PIN I.C.'S (LOCATIONS U18, U30, U56), PIN 30 IS GROUND AND PIN 10 IS POWER (+5V).
ON ALL 24 PIN I.C.'S (LOCATIONS U66, U68), PIN 6 IS GND1, PIN 18 IS GND2 AND PIN 24 IS POWER (+5V).
ON I.C. U15, PIN 5 IS POWER & PIN 10 IS GROUND. NUMBERS SHOWN REFER TO CONNECTOR P1 & J1. ALL CAPACITANCE VALUES ARE EXPRESSED IN MICROFARADS.
- RESISTANCE VALUES ARE EXPRESSED IN OHMS, K DENOTES 1000.
RESISTORS ARE 1/4 WATT, ± 5% TOLERANCE.
ON ALL 20 PIN I.C.'S, PIN 10 IS GROUND AND PIN 20 IS POWER (+5V).
ON ALL 16 PIN I.C.'S, PIN 8 IS GROUND AND PIN 16 IS POWER (+5V).
ON ALL 14 PIN I.C.'S, PIN 7 IS GROUND AND PIN 14 IS POWER (+5V).
UNLESS OTHERWISE SPECIFIED;

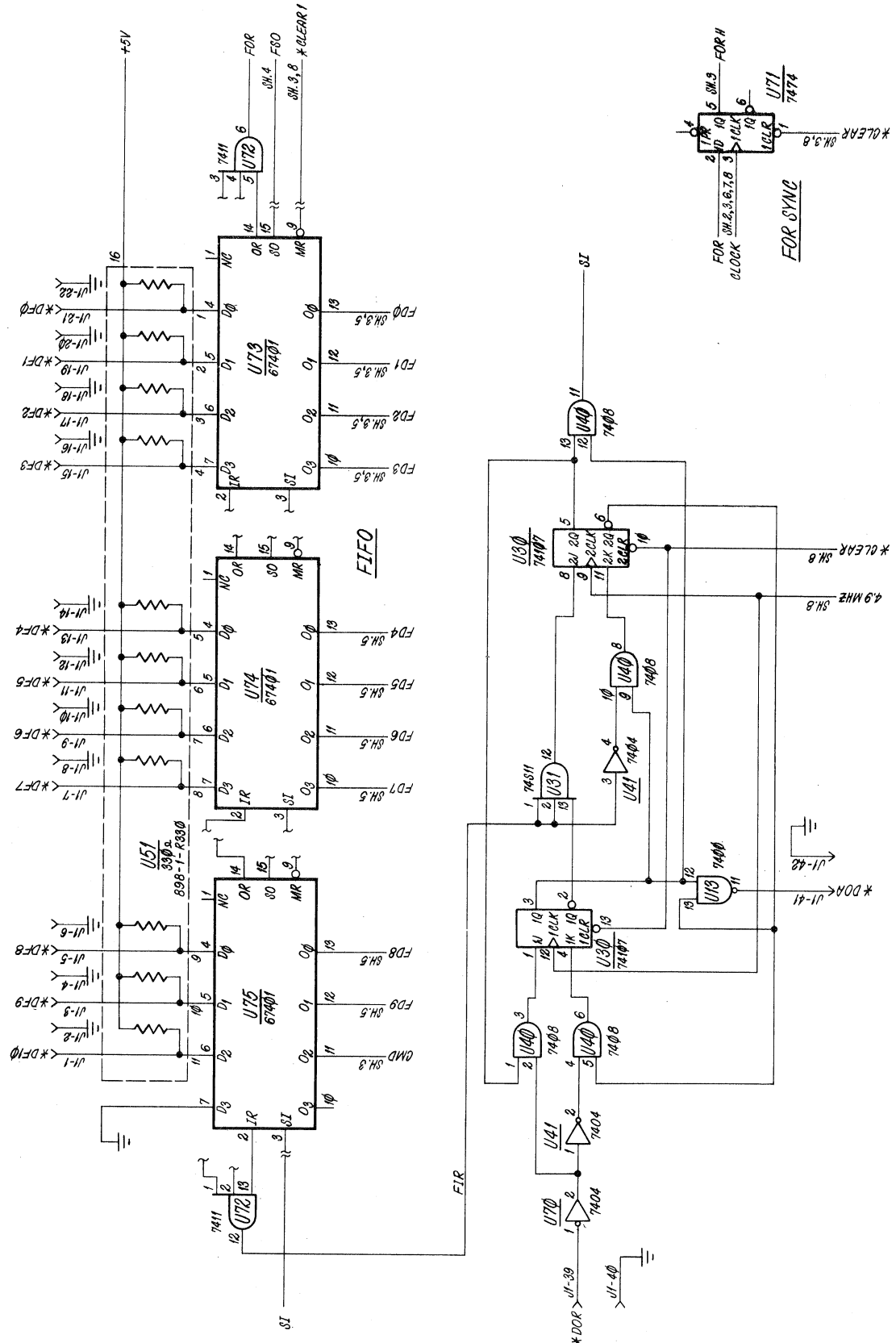


UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON		CONTRACT NO.	
.XX ±		DRAWN R. HOLLEY	9-21-77
.XXX ±		CHECKED <i>[Signature]</i>	30 SEP 77
ANGLES ±		MECH	
✓		ELEC <i>[Signature]</i>	24 OCT 77
		PROJ. ENG. D. <i>[Signature]</i>	24 OCT 77
		APPROVED	
200150-100	NOVOVIEW		
NEXT ASSY	USED ON		
APPLICATION			
MATERIAL			
FINISH			
SIZE CODE IDENT NO		C 53938	200109-600
SCALE		NONE	SHEET 1 OF 5
REV		A1	

SEE SEPARATE PARTS LIST
 EVANS & SUTHERLAND
 COMPUTER CORPORATION
 SALT LAKE CITY, UTAH 84112
 TERMINAL INTERFACE
 W/W

NOTES:

ZONE	LTR	REVISIONS	DESCRIPTION	DATE	APPROVED



1 2 3 4

D

C

B

A

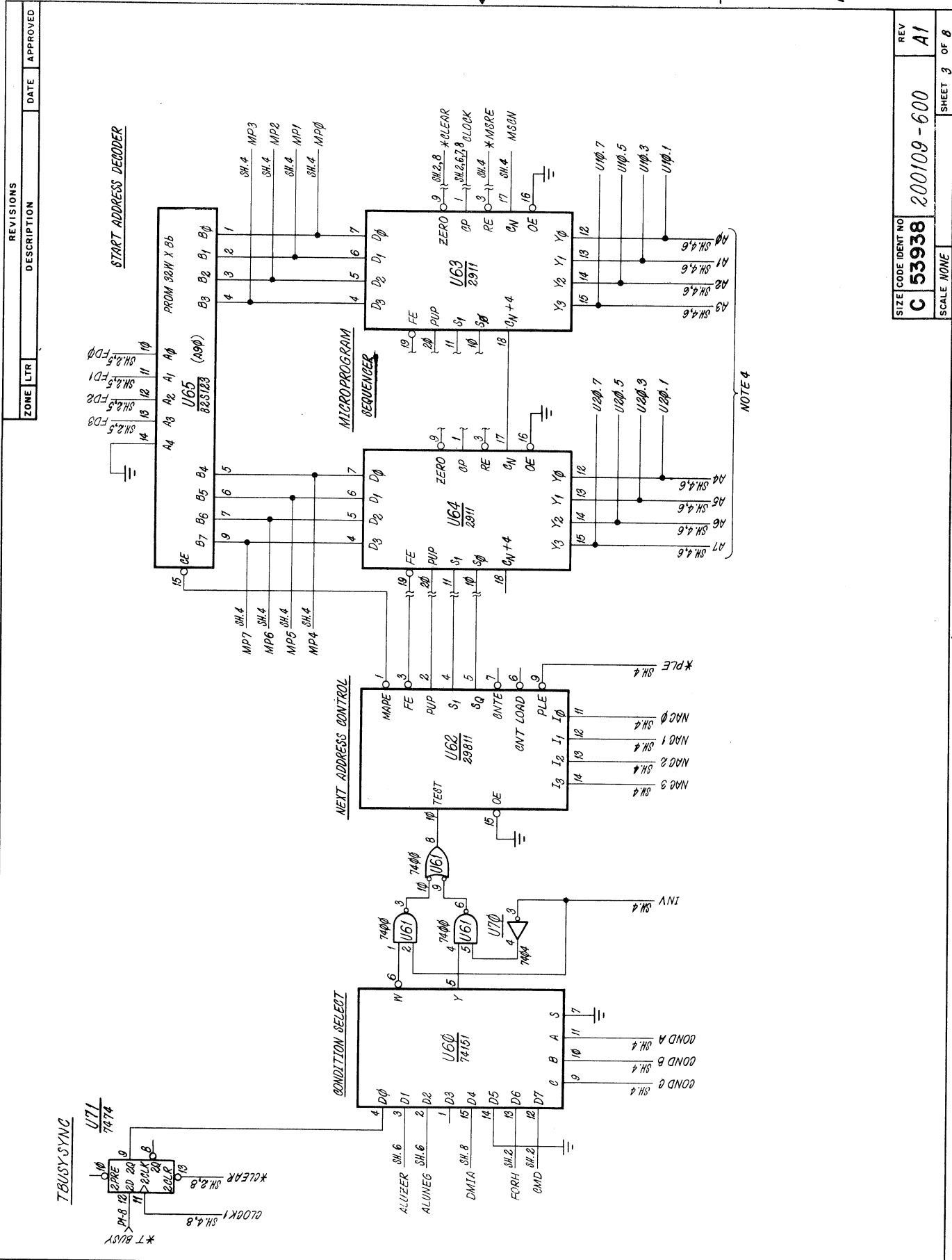
D

C

B

A

COMPUTER / FIFO
 READY / ACK
 SYNC



ZONE	LTR	DESCRIPTION	DATE	APPROVED

REV	DESCRIPTION

START ADDRESS DECODER

U65 2708 (A900) PROM 28W X 8b

U63 2911 MICROPROGRAM SEQUENCER

U64 2911 MICROPROGRAM SEQUENCER

U62 29811 NEXT ADDRESS CONTROL

U60 74151 CONDITION SELECT

U17 7444 INVERTER

U17 7474 T BUSY SYNC

MP1-MP7 MEMORY MODULES

SH.4, SH.4.6 ADDRESS BUFFERS

CLEAR, CLOCK, DMI1A, FORH, GMD CONTROL SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

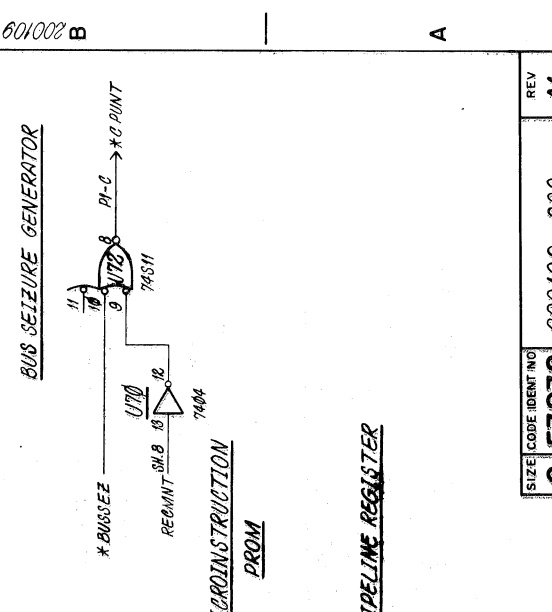
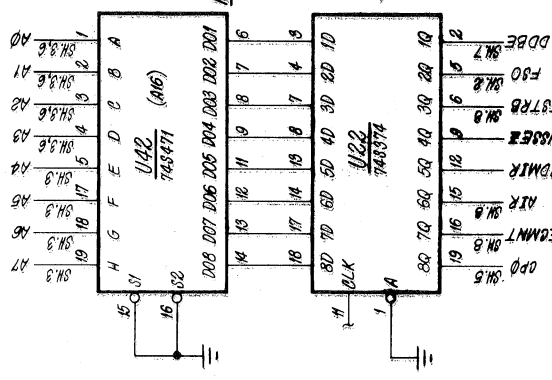
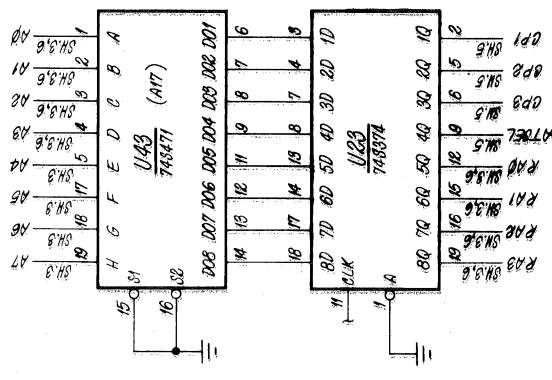
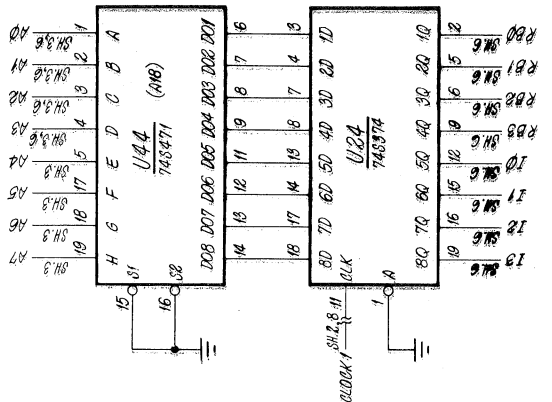
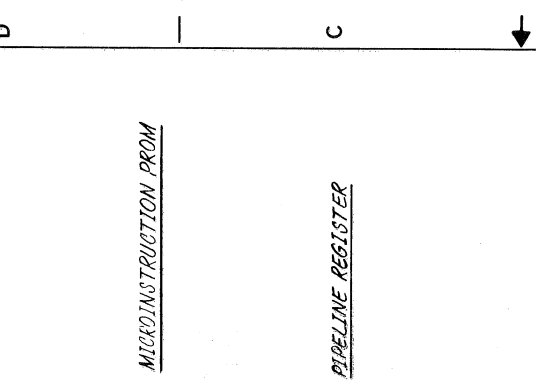
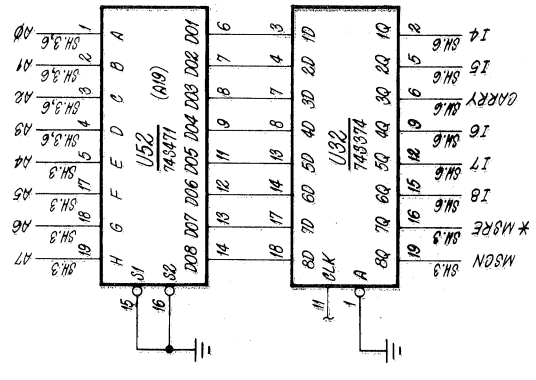
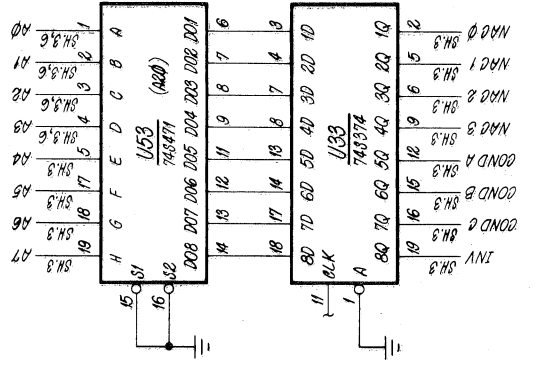
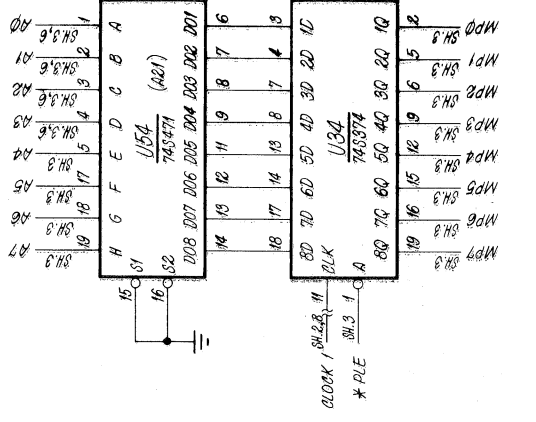
COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

COND A, COND B, COND C ADDRESS SIGNALS

SIZE CODE IDENT NO
C 53938
 200109-600
 REV
A1
 SCALE NONE
 SHEET 3 OF 8

ZONE	LTR	REVISIONS	DESCRIPTION	DATE	APPROVED



SIZE	CODE IDENT NO	REV
C	53938	A1

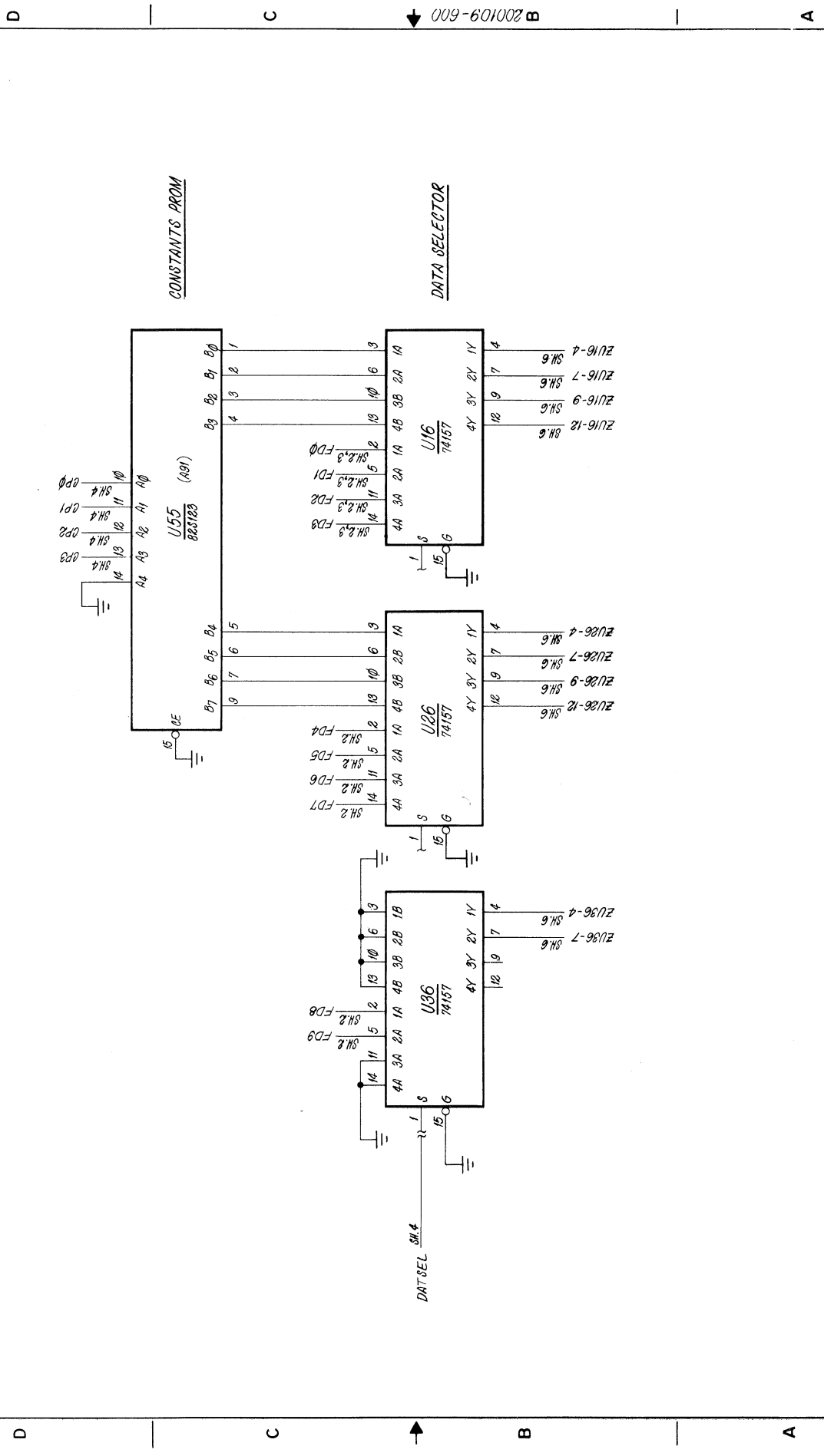
200109-600

SCALE NONE

SHEET 8 OF 8

REVISIONS		ZONE	LTR	DATE	APPROVED

2 3 4



CONSTANTS PROM

DATA SELECTOR

200109-600

SIZE CODE IDENT NO	REV
C 53938	A1
200109-600	
SCALE	SHEET OF
NONE	5 8

D

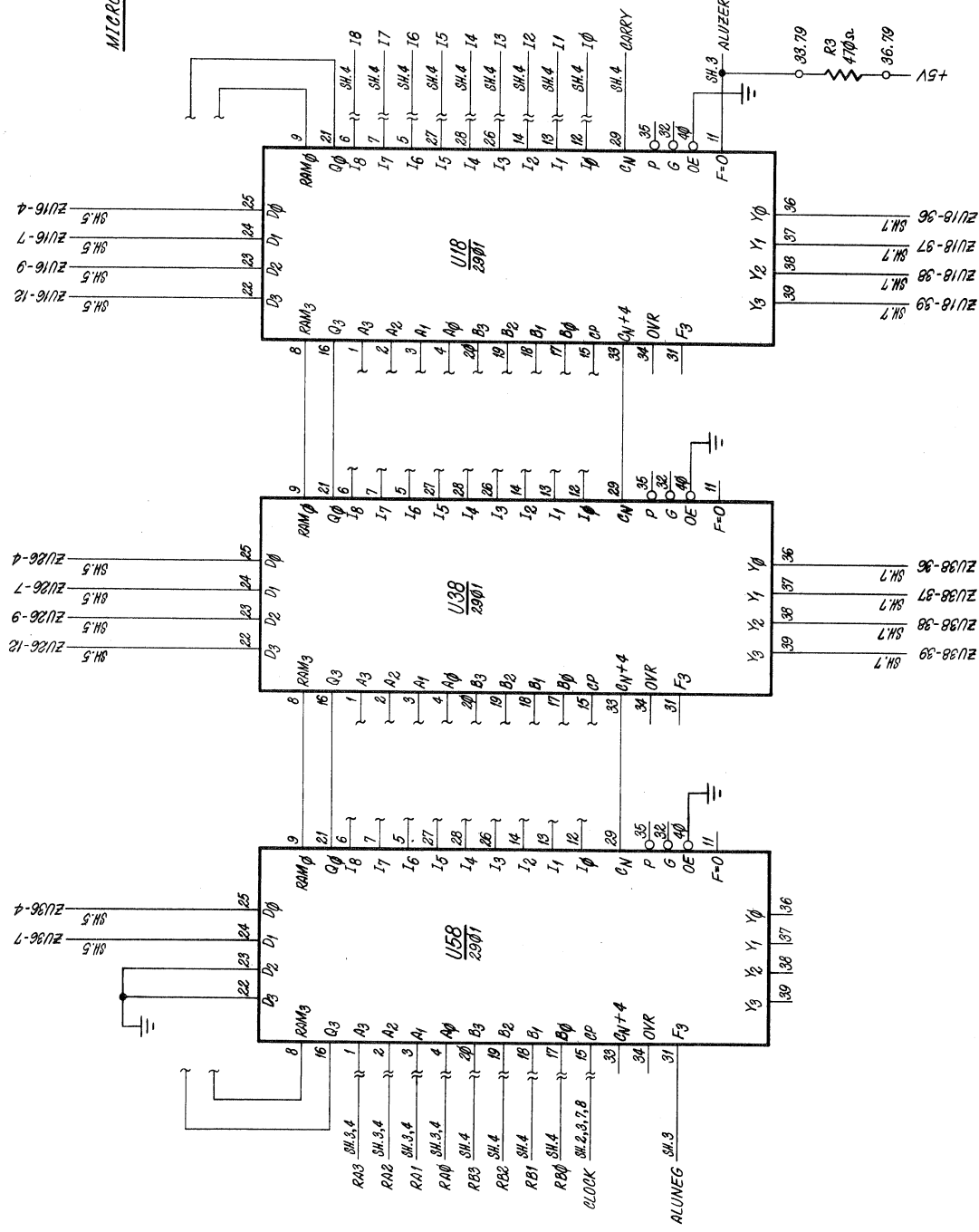
C

200109-600 B

A

ZONE	LTR	DESCRIPTION	DATE	APPROVED

MICROPROCESSOR



SIZE CODE IDENT NO	REV
C 53938	A1
SCALE NONE	SHEET 6 OF 8

D

C

B

A

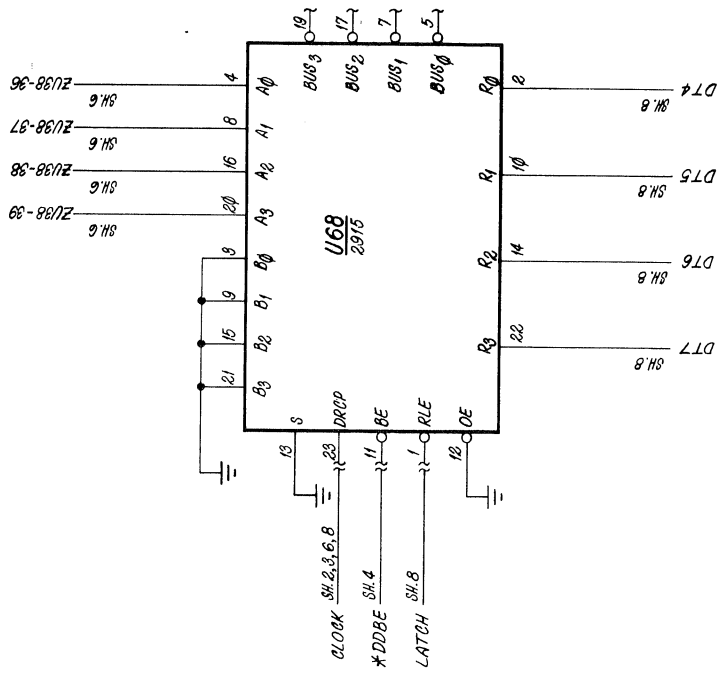
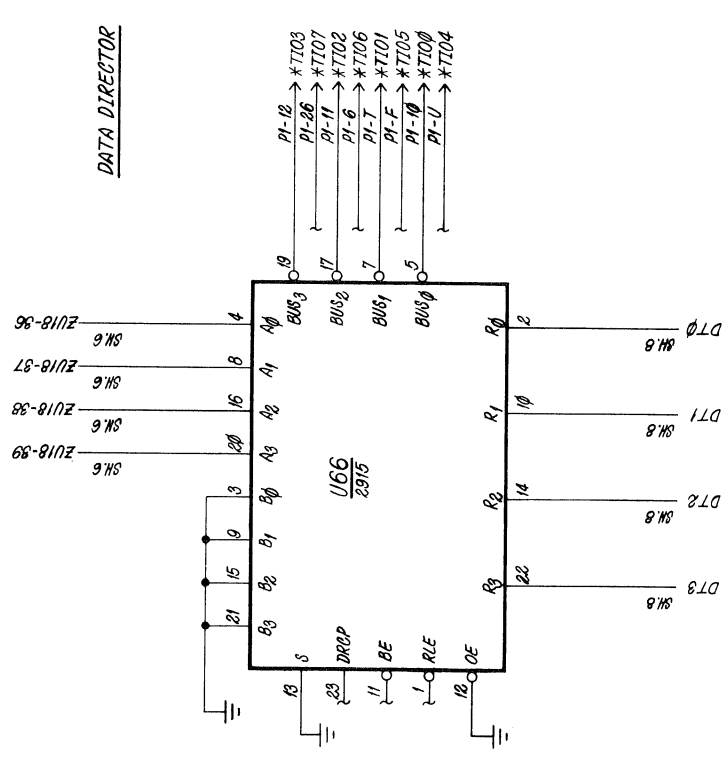
2

3

4

REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	

1 2 3 4

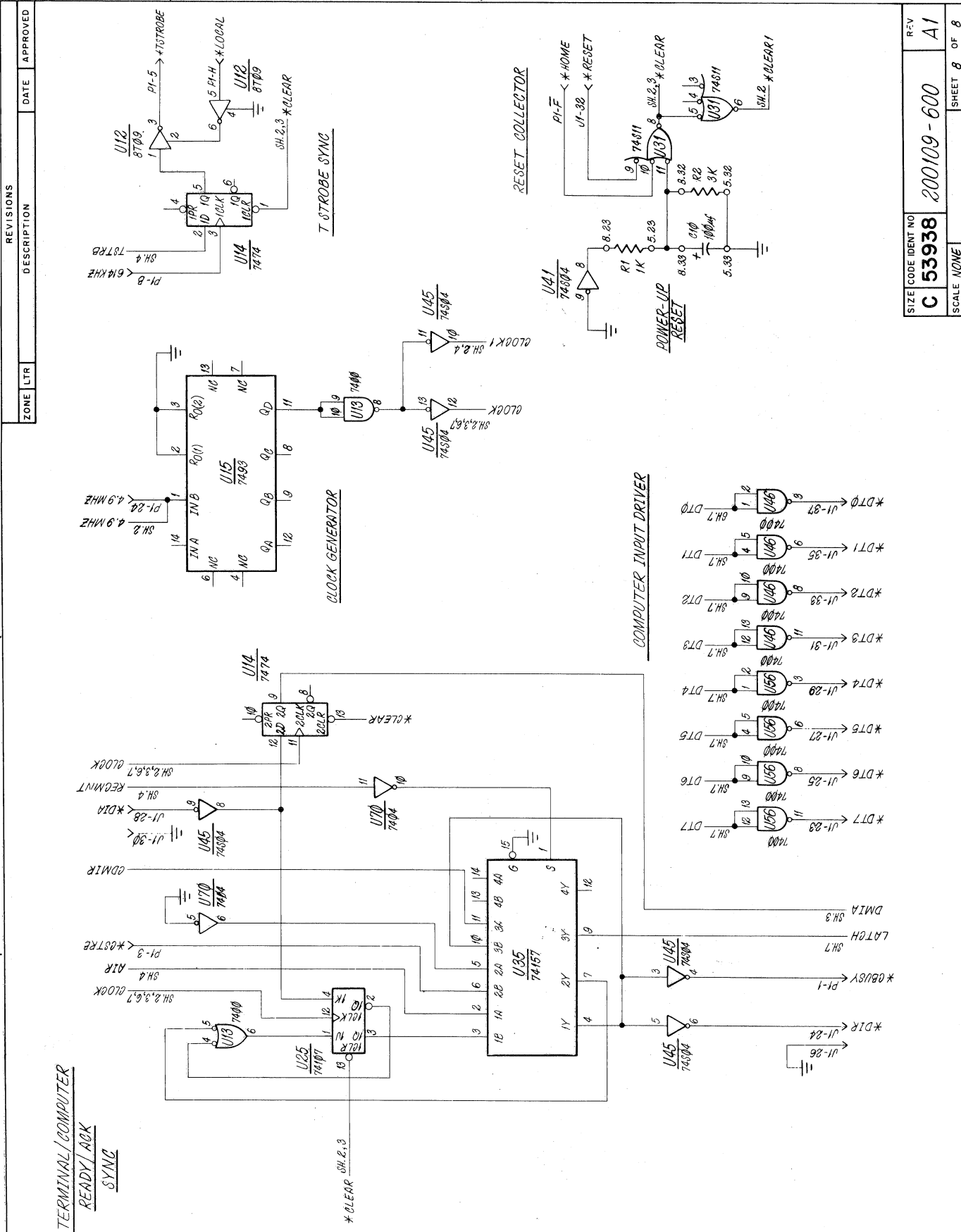


SIZE CODE IDENT NO	REV	SHEET 7 OF 8
C 53938	A1	
SCALE	200109-600	
	NONE	

200109-600

D C B A

D C B A



ZONE	LTR	DESCRIPTION	DATE	APPROVED

SIZE CODE IDENT NO	REV
C 53938	A1
SCALE NONE	SHEET 8 OF 8

TERMINAL COMPUTER
READY / LOCK
SYNC

T STROBE SYNC

CLOCK GENERATOR

RESET COLLECTOR

COMPUTER INPUT DRIVER

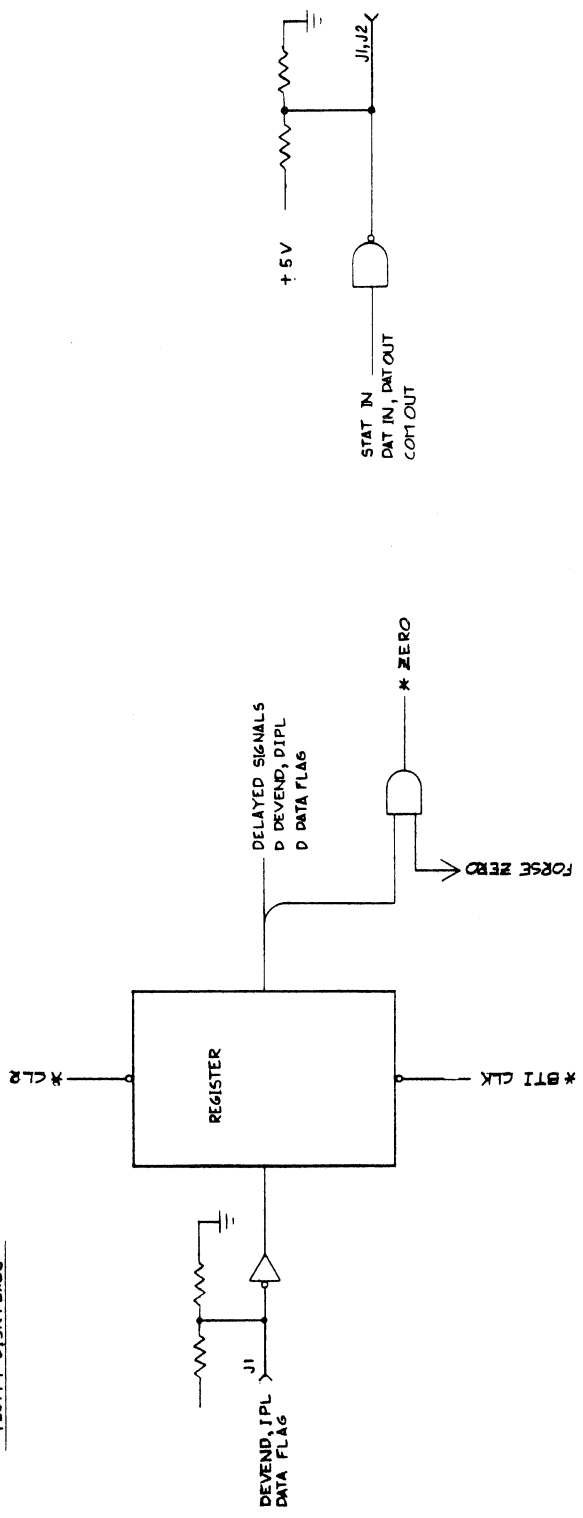
POWER-UP RESET

2

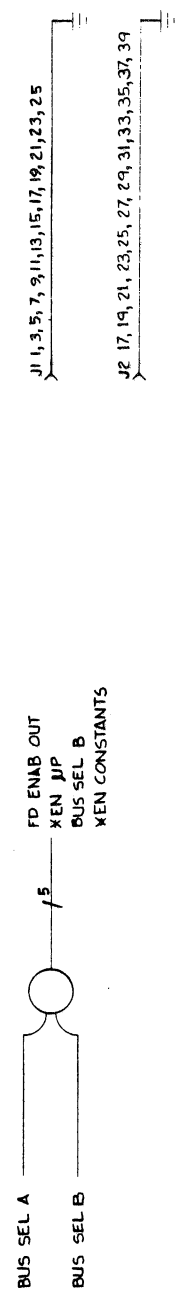
3

4

FLOPPY DISK FLAGS



BUS BUFFERING & CONTROL



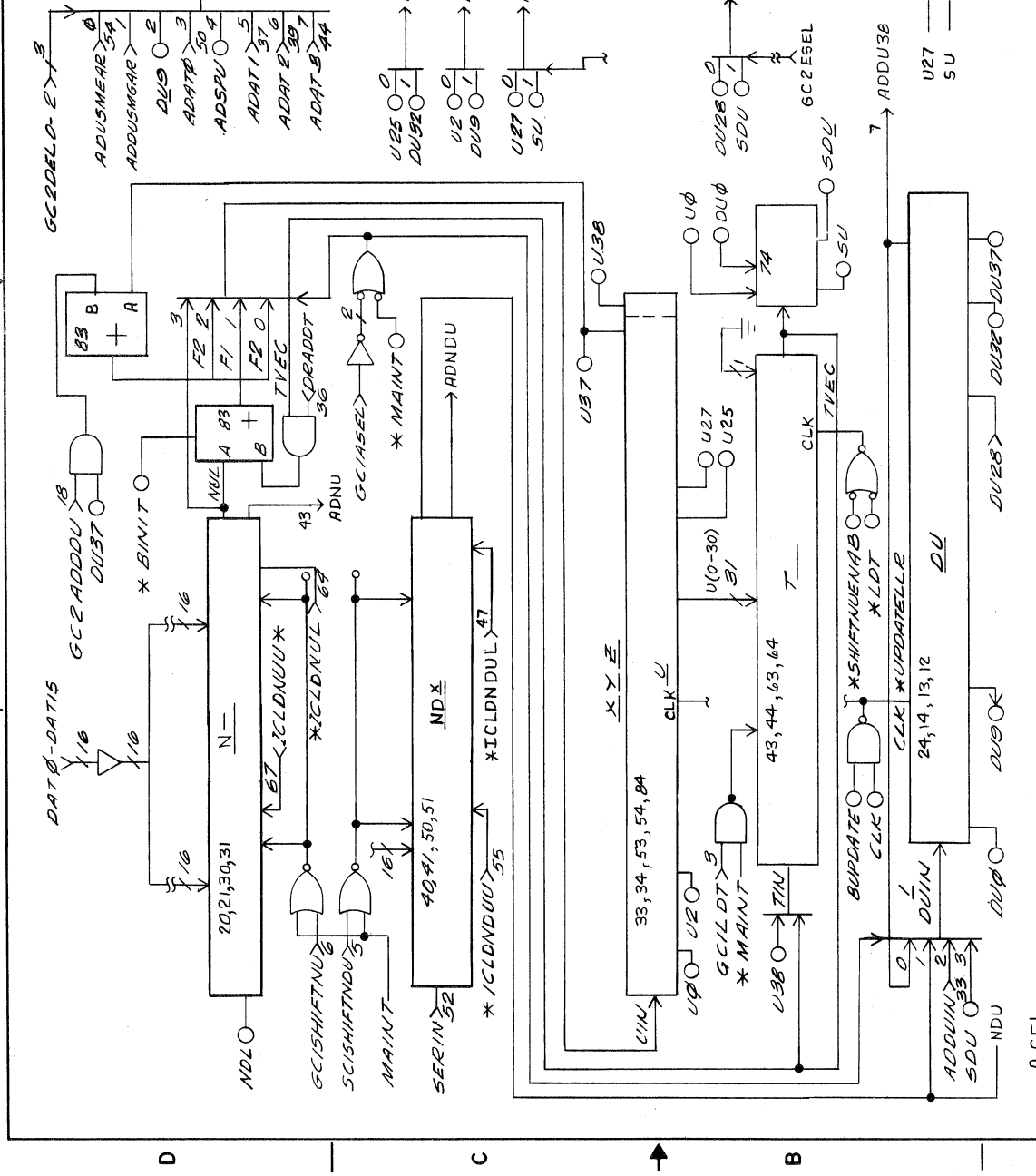
REVISIONS			
ZONE LTR	DESCRIPTION	DATE	APPROVED

SIZE CODE IDENT NO	REV	REV	SHEET 2 OF 2
C 53938	200108-900	A2	
SCALE	NONE		

D C B A

D C B A

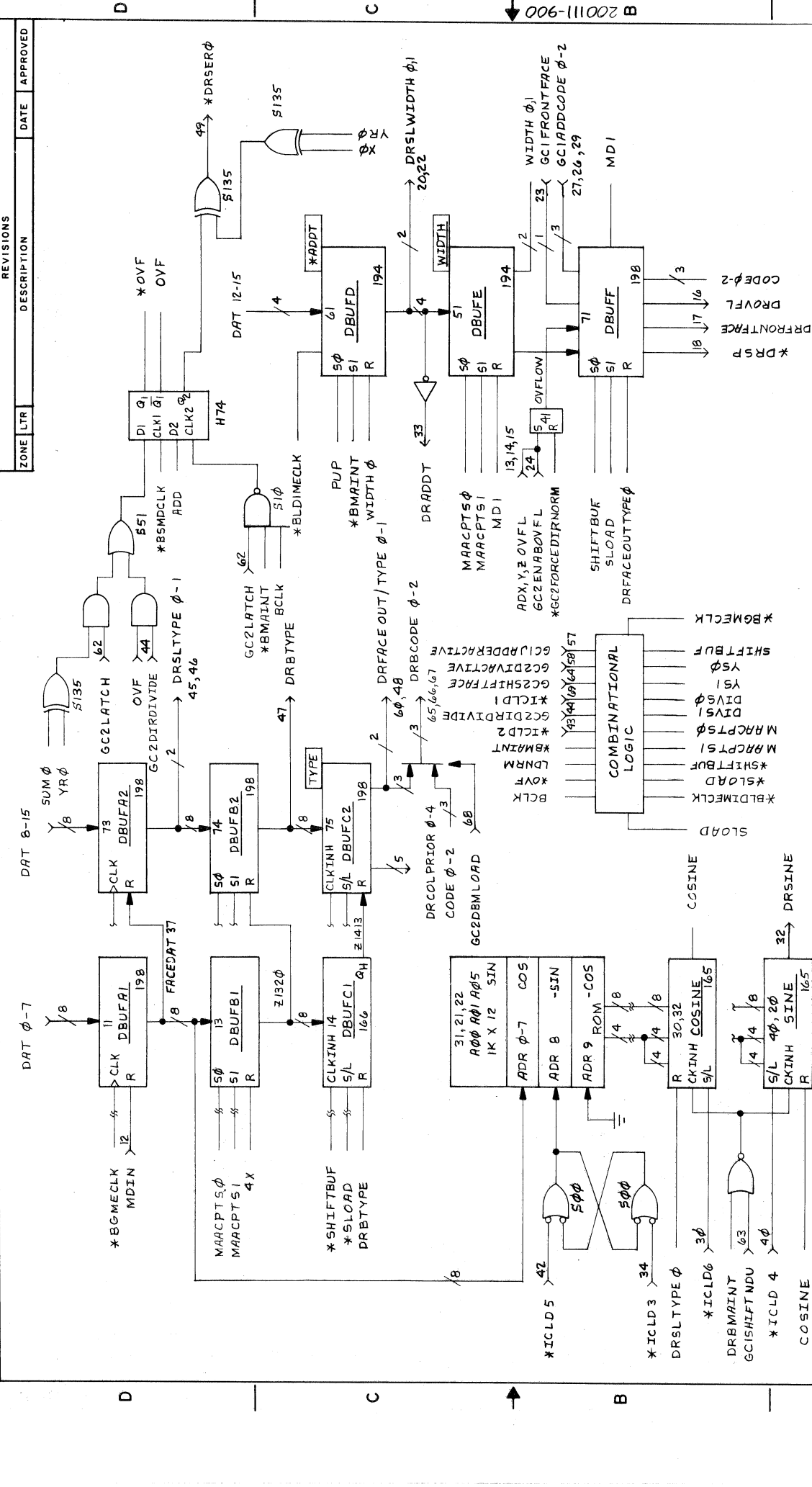
ZONE LTR	DESCRIPTION	DATE	APPROVED



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
A DDER (BLOCK DIAGRAM)	
SIZE CODE IDENT NO	REV
C 53938	200110-900 NC
SCALE	1 OF 2

CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
DRAWN <i>K.L.D.</i>	TOLERANCES ON
CHECKED <i>W.P.</i>	.XX ±
MECH	.XXX ±
ELEC	ANGLES ±
PROJ. ENG	✓
APPROVED	MATERIAL
	FINISH
	USED ON
	NEXT ASSY
	APPLICATION

- A SEL
- 0 = NORMAL
- 1 = SERIAL LOAD
- 2 = CURVE STRING
- 3 = MAINTENANCE
- ESEL
- 0 = STEERING
- 1 = FACE
- 2 = FACE
- 3 = FACE
- 4 = EDGE
- 5 = DEBAM LOAD 91
- 6 = DEBAM LOAD 91
- 7 = DEBAM LOAD 91



REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	

SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
DIRECTIONALITY BLOCK DIAGRAM	
CONTRACT NO.	5-2-77
DRAWN BY	Boyle
CHECKED BY	Langston
MECH	27 MAY 77
ELECTRICAL	6 June 77
PROJ. ENG	W. F. Allen
APPROVED	26 July 77
SIZE	CODE IDENT NO
C 53938	200111-900
SCALE	NONE
REV	NC
SHEET	1 OF 2

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON	
.XX ±	
.XXX ±	
ANGLES ±	
MATERIAL	
FINISH	
USED ON	
APPLICATION	

200111-900 B

ZONE	LTR	DESCRIPTION	DATE	APPROVED

Revisions

1

2

3

D

C

B

A

200111-900

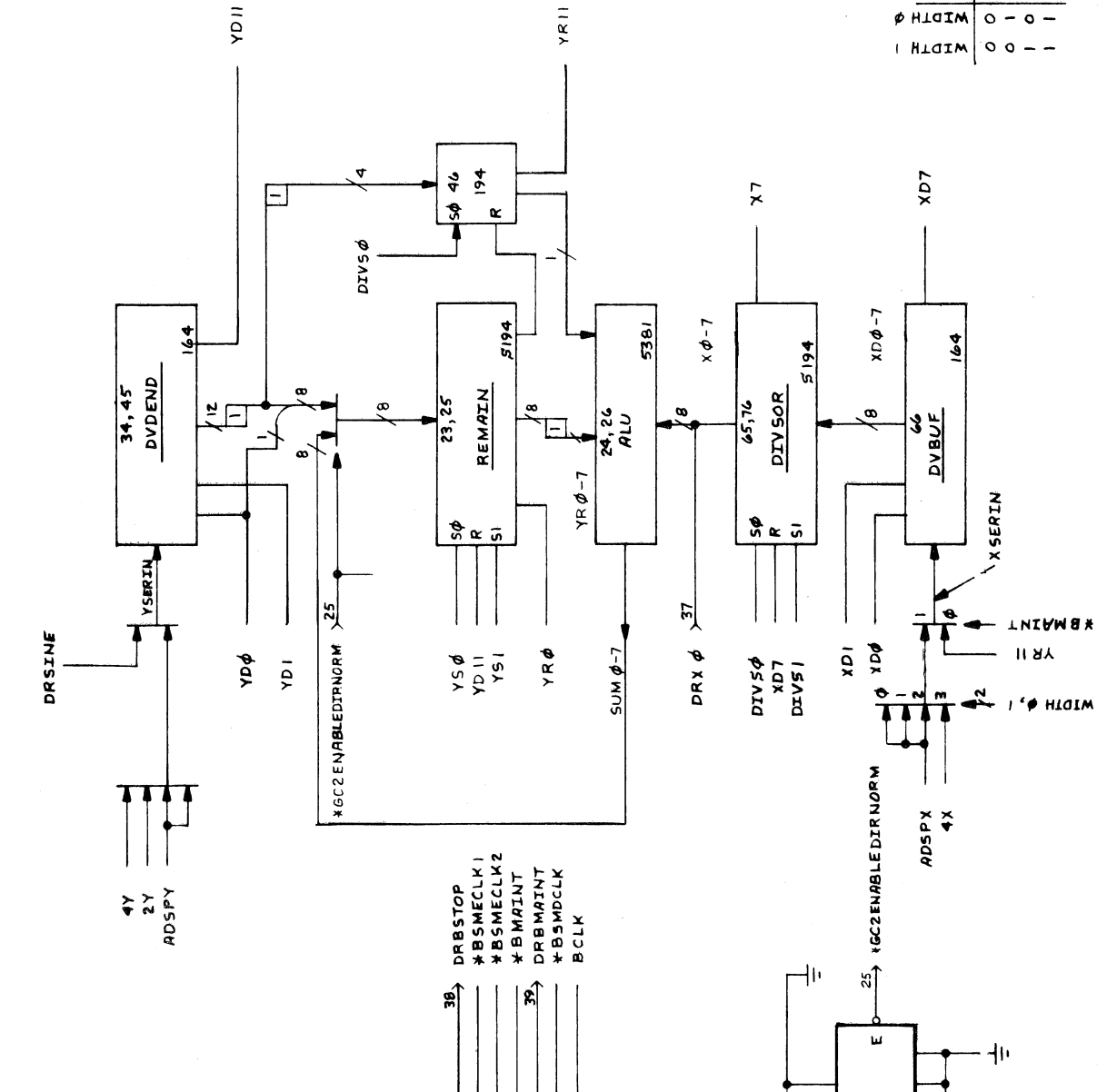
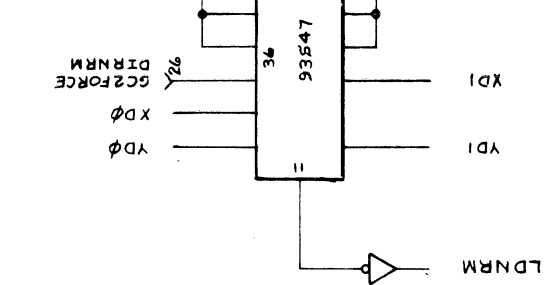
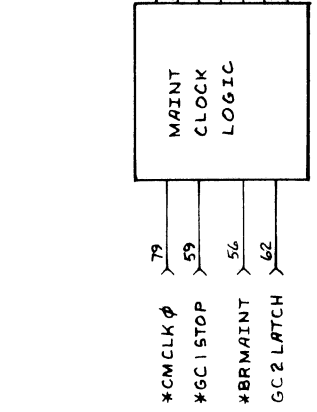
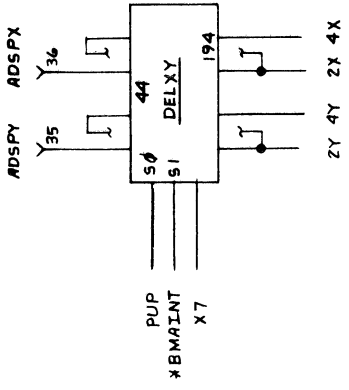
REV NC

53938

200111-900

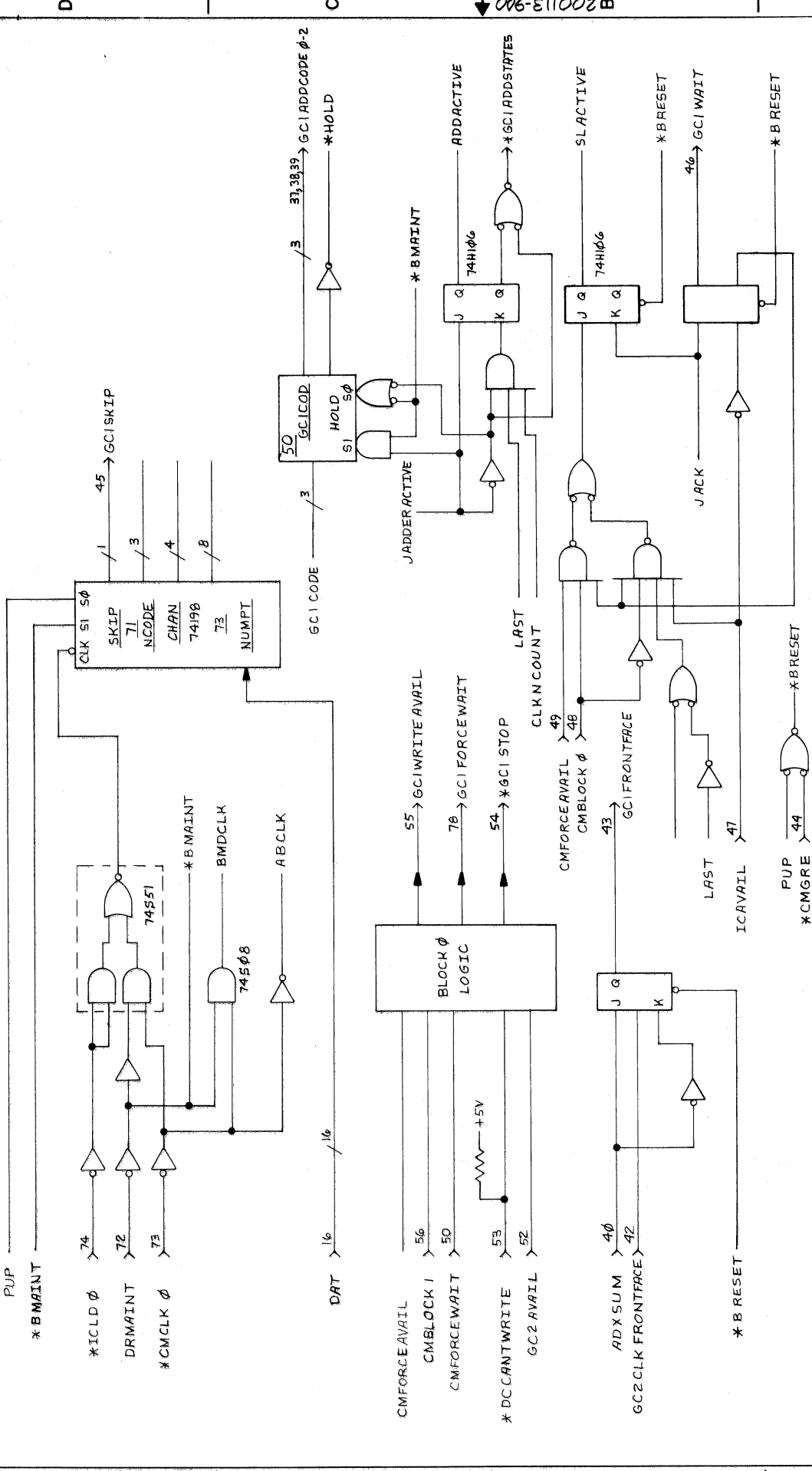
NC

SHEET 2 OF 2



WIDTH	WIDTH	FUNC
0	0	4Y/X = 14°
0	0	2Y/X = 27°
1	1	Y/X = 45°
1	1	Y/4X = 76°

ZONE	LTR	DESCRIPTION	DATE	APPROVED
A1		UPDATE W/NO DWG CHANGE	2-2-78	PS

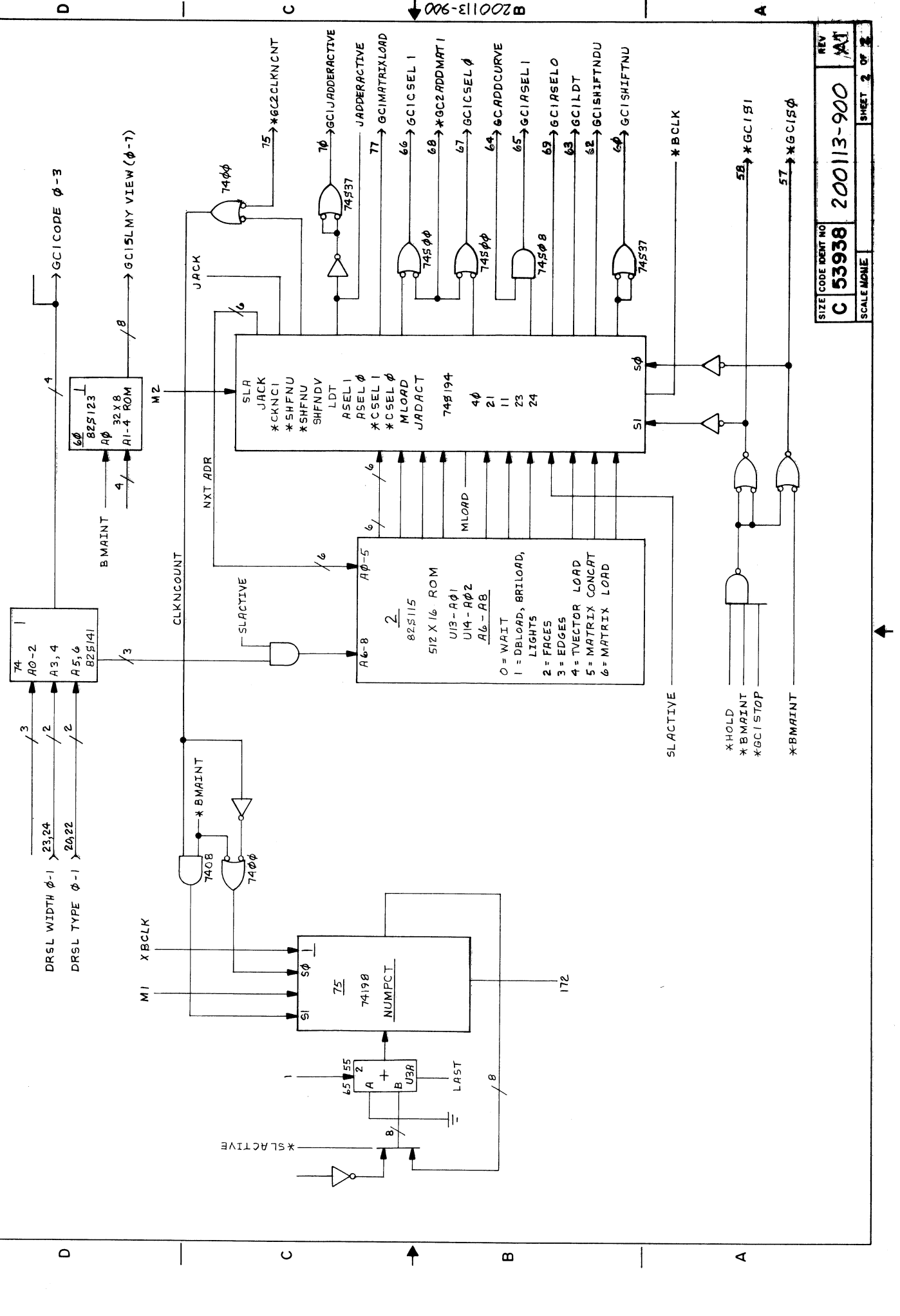
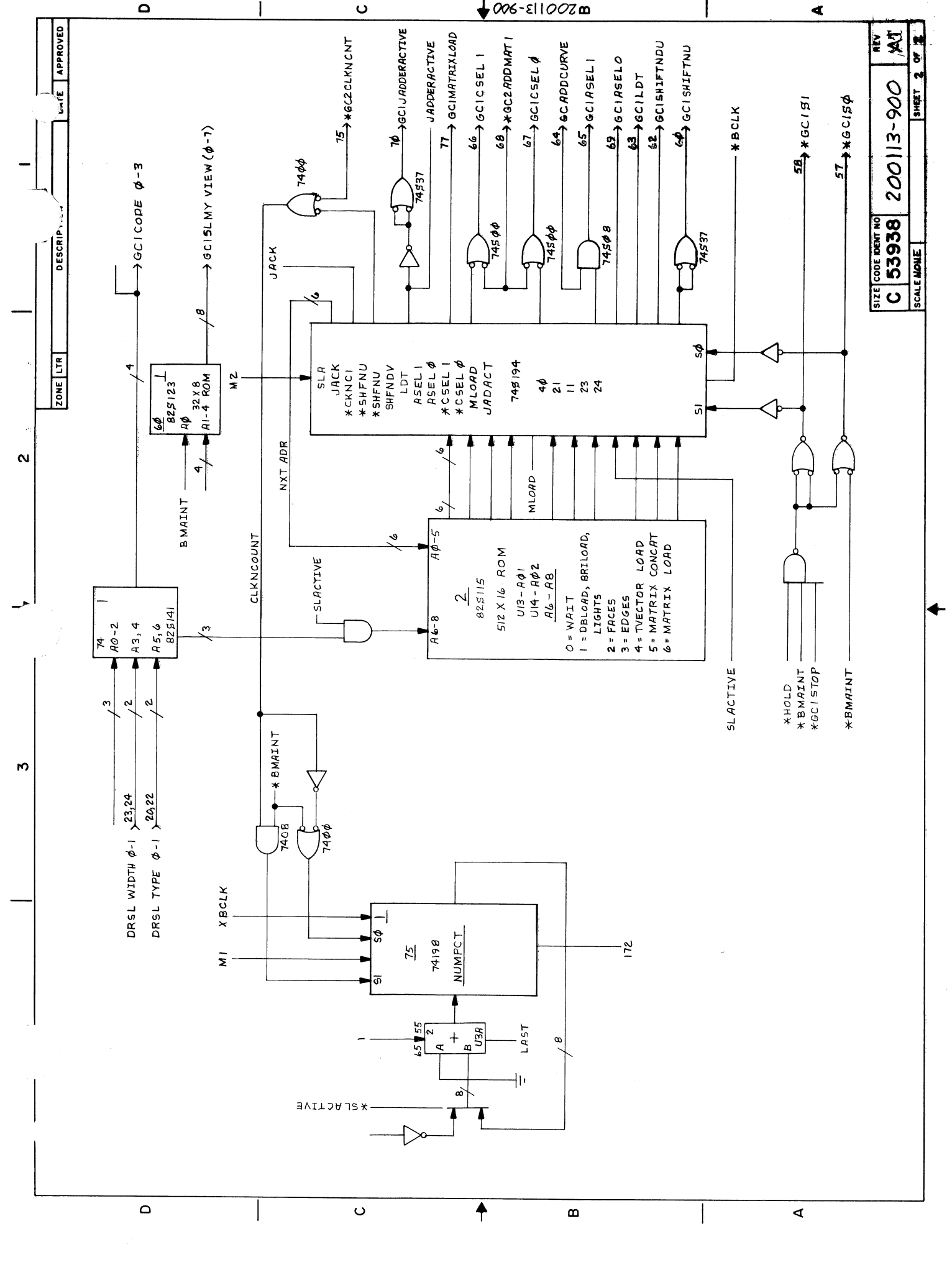


SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN & CHECKED BY 5-27-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON		MECH .XXX ±	
		ELEC .XXX ±	
		APPROVED	
		PROJECT ENG	
		MATERIAL	
		NEXT ASSY USED ON	
		APPLICATION	
GP CONTROL I		SCALE NONE	
SIZE CODE IDENT NO		REV	
C 53938		200113-900	
		A1	
		1 OF 2	

1
2
3
4

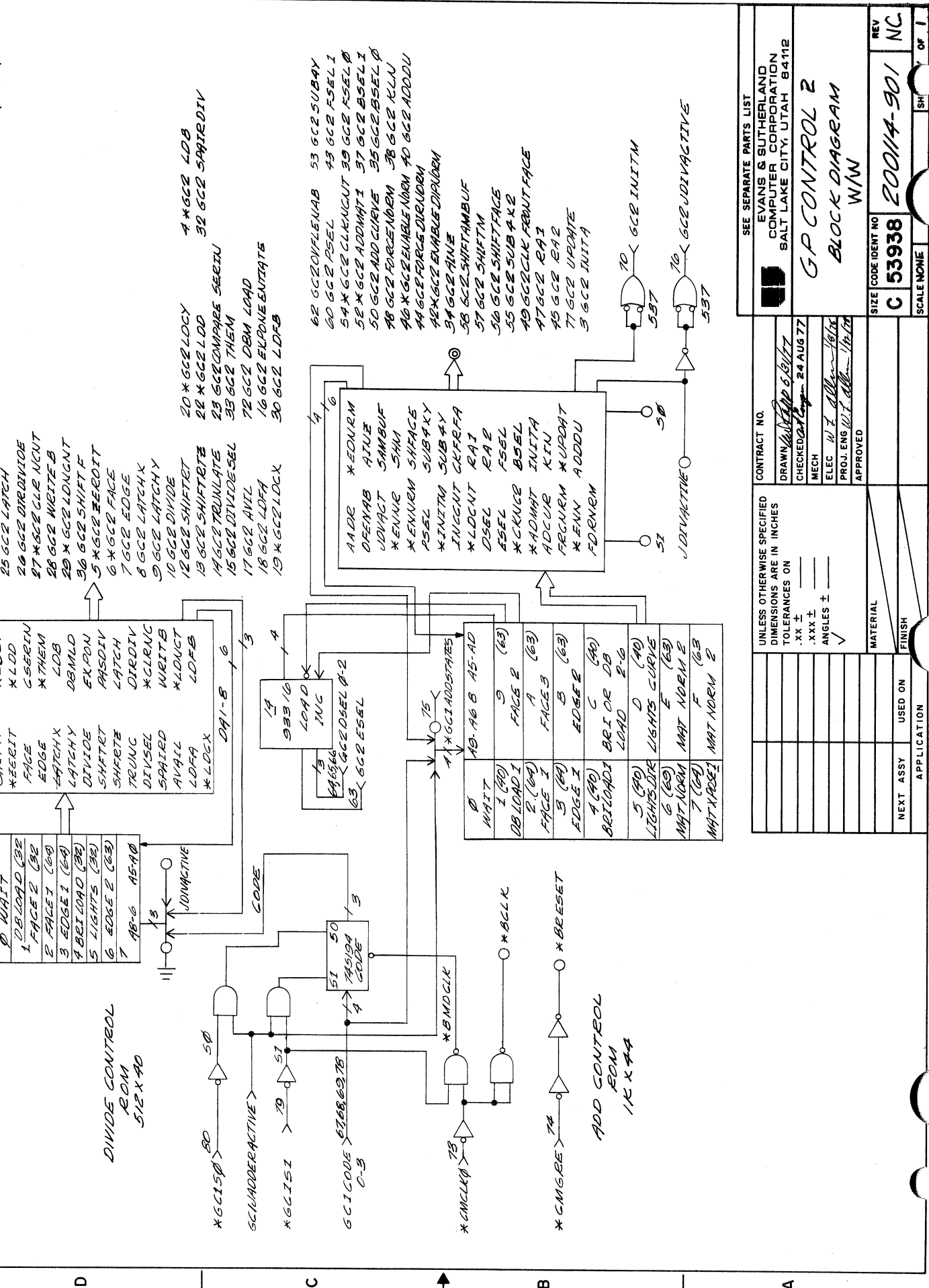
D
C
B
A

B 200113-900



ZONE	LTR	DESCRIPTION	DATE	APPROVED

ZONE	LTR	DESCRIPTION	DATE	APPROVED



- 24 GCR PASSON
- 25 GCR LATCH
- 26 GCR DIVIDE
- 27 GCR CLEAR
- 28 GCR WRITE
- 29 GCR LOAD
- 30 GCR SHIFT
- 31 GCR ZERO
- 32 GCR FACE
- 33 GCR EDGE
- 34 GCR LATCH
- 35 GCR DIVIDE
- 36 GCR SHIFTR
- 37 GCR TRUNCL
- 38 GCR LATCH
- 39 GCR DIVIDE
- 40 GCR SHIFTR
- 41 GCR TRUNCL
- 42 GCR LATCH
- 43 GCR DIVIDE
- 44 GCR SHIFTR
- 45 GCR TRUNCL
- 46 GCR LATCH
- 47 GCR DIVIDE
- 48 GCR SHIFTR
- 49 GCR TRUNCL
- 50 GCR LATCH
- 51 GCR DIVIDE
- 52 GCR SHIFTR
- 53 GCR TRUNCL
- 54 GCR LATCH
- 55 GCR DIVIDE
- 56 GCR SHIFTR
- 57 GCR TRUNCL
- 58 GCR LATCH
- 59 GCR DIVIDE
- 60 GCR SHIFTR
- 61 GCR TRUNCL
- 62 GCR LATCH
- 63 GCR DIVIDE
- 64 GCR SHIFTR
- 65 GCR TRUNCL
- 66 GCR LATCH
- 67 GCR DIVIDE
- 68 GCR SHIFTR
- 69 GCR TRUNCL
- 70 GCR LATCH
- 71 GCR DIVIDE
- 72 GCR SHIFTR
- 73 GCR TRUNCL
- 74 GCR LATCH
- 75 GCR DIVIDE
- 76 GCR SHIFTR
- 77 GCR TRUNCL
- 78 GCR LATCH
- 79 GCR DIVIDE
- 80 GCR SHIFTR
- 81 GCR TRUNCL
- 82 GCR LATCH
- 83 GCR DIVIDE
- 84 GCR SHIFTR
- 85 GCR TRUNCL
- 86 GCR LATCH
- 87 GCR DIVIDE
- 88 GCR SHIFTR
- 89 GCR TRUNCL
- 90 GCR LATCH
- 91 GCR DIVIDE
- 92 GCR SHIFTR
- 93 GCR TRUNCL
- 94 GCR LATCH
- 95 GCR DIVIDE
- 96 GCR SHIFTR
- 97 GCR TRUNCL
- 98 GCR LATCH
- 99 GCR DIVIDE
- 100 GCR SHIFTR

- 20 * GCR LDCY
- 21 * GCR LDD
- 22 * GCR COMPARE
- 23 * GCR THEM
- 24 * GCR DBM LOAD
- 25 * GCR EXPDNE ENTATE
- 26 * GCR LDFB
- 27 * GCR ENAB
- 28 * GCR FSEL
- 29 * GCR CLK
- 30 * GCR BSEL
- 31 * GCR ADD
- 32 * GCR CURVE
- 33 * GCR NORM
- 34 * GCR ADDU
- 35 * GCR ENAB
- 36 * GCR FSEL
- 37 * GCR CLK
- 38 * GCR BSEL
- 39 * GCR ADD
- 40 * GCR CURVE
- 41 * GCR NORM
- 42 * GCR ADDU
- 43 * GCR ENAB
- 44 * GCR FSEL
- 45 * GCR CLK
- 46 * GCR BSEL
- 47 * GCR ADD
- 48 * GCR CURVE
- 49 * GCR NORM
- 50 * GCR ADDU
- 51 * GCR ENAB
- 52 * GCR FSEL
- 53 * GCR CLK
- 54 * GCR BSEL
- 55 * GCR ADD
- 56 * GCR CURVE
- 57 * GCR NORM
- 58 * GCR ADDU
- 59 * GCR ENAB
- 60 * GCR FSEL
- 61 * GCR CLK
- 62 * GCR BSEL
- 63 * GCR ADD
- 64 * GCR CURVE
- 65 * GCR NORM
- 66 * GCR ADDU
- 67 * GCR ENAB
- 68 * GCR FSEL
- 69 * GCR CLK
- 70 * GCR BSEL
- 71 * GCR ADD
- 72 * GCR CURVE
- 73 * GCR NORM
- 74 * GCR ADDU
- 75 * GCR ENAB
- 76 * GCR FSEL
- 77 * GCR CLK
- 78 * GCR BSEL
- 79 * GCR ADD
- 80 * GCR CURVE
- 81 * GCR NORM
- 82 * GCR ADDU
- 83 * GCR ENAB
- 84 * GCR FSEL
- 85 * GCR CLK
- 86 * GCR BSEL
- 87 * GCR ADD
- 88 * GCR CURVE
- 89 * GCR NORM
- 90 * GCR ADDU
- 91 * GCR ENAB
- 92 * GCR FSEL
- 93 * GCR CLK
- 94 * GCR BSEL
- 95 * GCR ADD
- 96 * GCR CURVE
- 97 * GCR NORM
- 98 * GCR ADDU
- 99 * GCR ENAB
- 100 * GCR FSEL

SEE SEPARATE PARTS LIST
 EVANS & SUTHERLAND
 COMPUTER CORPORATION
 SALT LAKE CITY, UTAH 84112

CONTRACT NO.	DRAWN	CHECKED	MECH	ELEC	PROJ. ENG.	APPROVED

UNLESS OTHERWISE SPECIFIED	DIMENSIONS ARE IN INCHES
.XX ±	
.XXX ±	
ANGLES ±	

MATERIAL	FINISH	APPLICATION

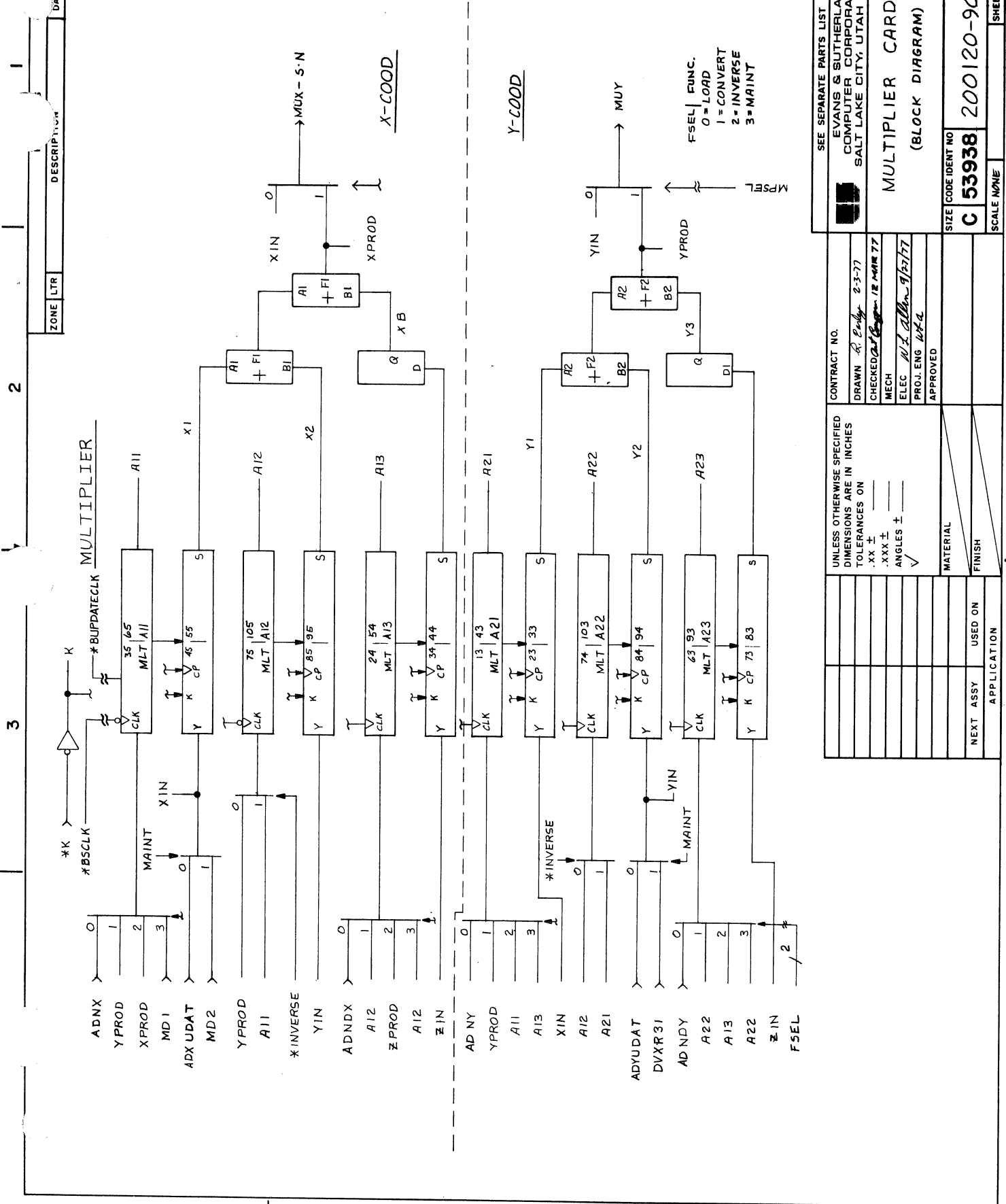
SIZE	CODE	IDENT NO	REV
C	53938	20014-901	NC

SCALE	NO	REV	OF

GP CONTROL 2	BLOCK DIAGRAM	W/W

20014-901

ZONE	LTR	DESCRIPTION	DATE	APPROVED
3	2	MULTIPLIER		



ADNX	YPROD	XPROD	MD1	ADXUDAT	MD2	YPROD	A11	*INVERSE	YIN	ADNDY	A22	A13	A22	XIN	FSEL
------	-------	-------	-----	---------	-----	-------	-----	----------	-----	-------	-----	-----	-----	-----	------

ADNX	A12	XPROD	A12	XIN	ADNDY	YPROD	A11	A13	XIN	A12	A21	ADVUDAT	DVXR31	ADNDY	A22	A13	A22	XIN	FSEL
------	-----	-------	-----	-----	-------	-------	-----	-----	-----	-----	-----	---------	--------	-------	-----	-----	-----	-----	------

SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
MULTIPLIER CARD (BLOCK DIAGRAM)	
SIZE CODE IDENT NO	REV
C 53938	NC
SCALE NAME	SHEET 1 OF 1

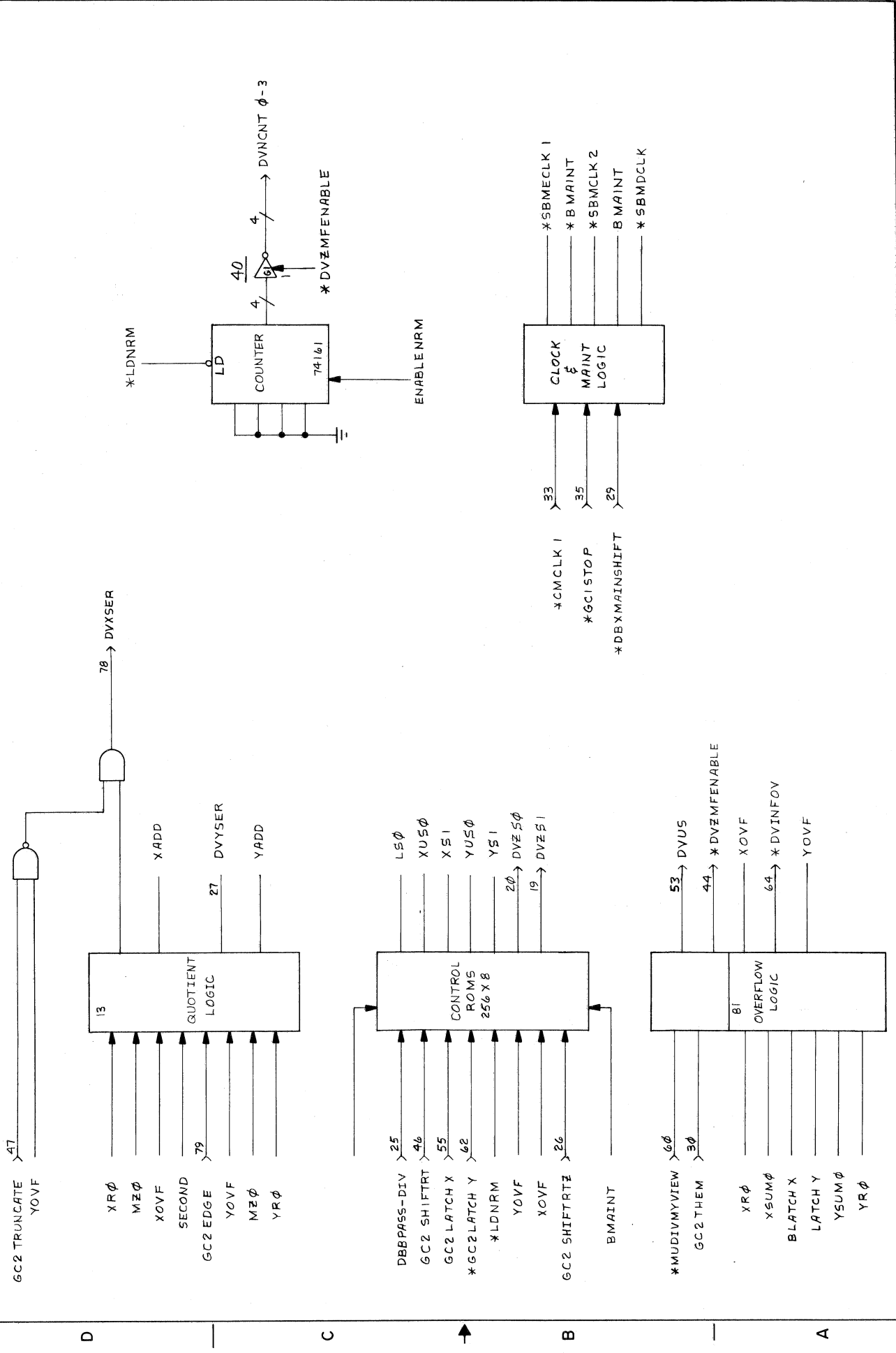
CONTRACT NO.	2-3-77
DRAWN	R. E. Eddy
CHECKED	W. J. Allen
MECH	12 MAR 77
ELEC	9/27/77
PROJ. ENG	W. J. A.
APPROVED	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON	
.XX ±	
.XXX ±	
ANGLES ±	
✓	
MATERIAL	
FINISH	
NEXT ASSY	USED ON
APPLICATION	

ADNX	YPROD	XPROD	MD1	ADXUDAT	MD2	YPROD	A11	*INVERSE	YIN	ADNDY	A22	A13	A22	XIN	FSEL
------	-------	-------	-----	---------	-----	-------	-----	----------	-----	-------	-----	-----	-----	-----	------

4 3 2 1

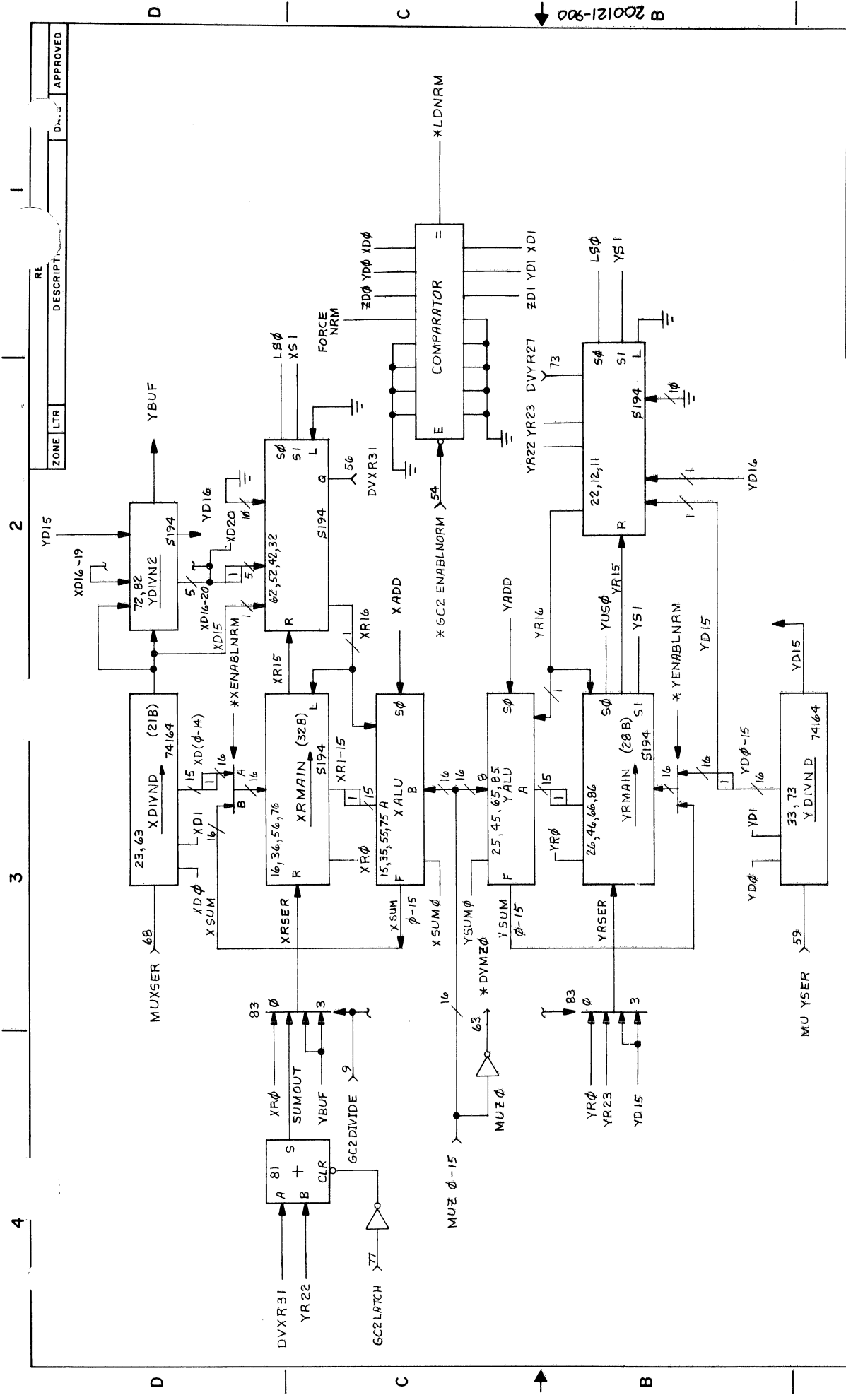
ZONE	LTR	DESCRIPTION	DATE	APPROVED



SIZE CODE IDENT NO	REV
C 53938	NC
SCALE NONE	SHE
200121-900	OF 2

200121-900 B

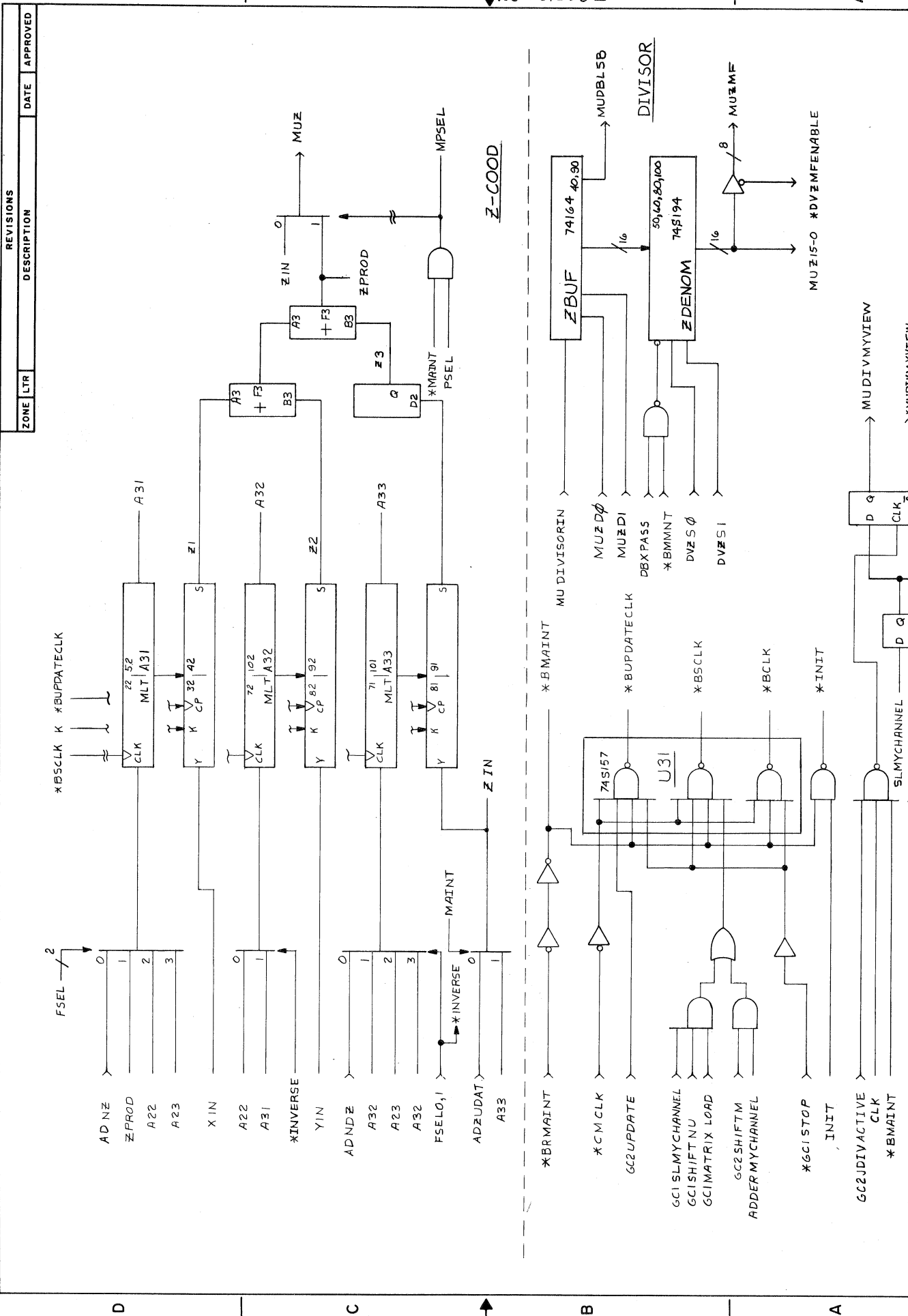
D C A



SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN <i>E. Enby</i> 4-21-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CHECKED <i>W. Enby</i> 16 MAY 77	
TOLERANCES ON		MECH	
.XX ±		ELEC <i>D. M. Moore</i> 6 June 77	
.XXX ±		APPROVED	
ANGLES ±		PROJECT <i>W. F. Enby</i> 16 MAY 77	
MATERIAL		SIZE CODE IDENT NO	
FINISH		C 53938	
200130-100 NOV/VIEW		200121-900	
NEXT ASSY USED ON		DIVIDER	
APPLICATION		BLOCK DIAGRAM	
		REV	
		NC	
		SHEET 1 OF 2	

4 3 2 1

200121-900 B



REVISIONS			DATE	APPROVED
ZONE	LTR	DESCRIPTION		

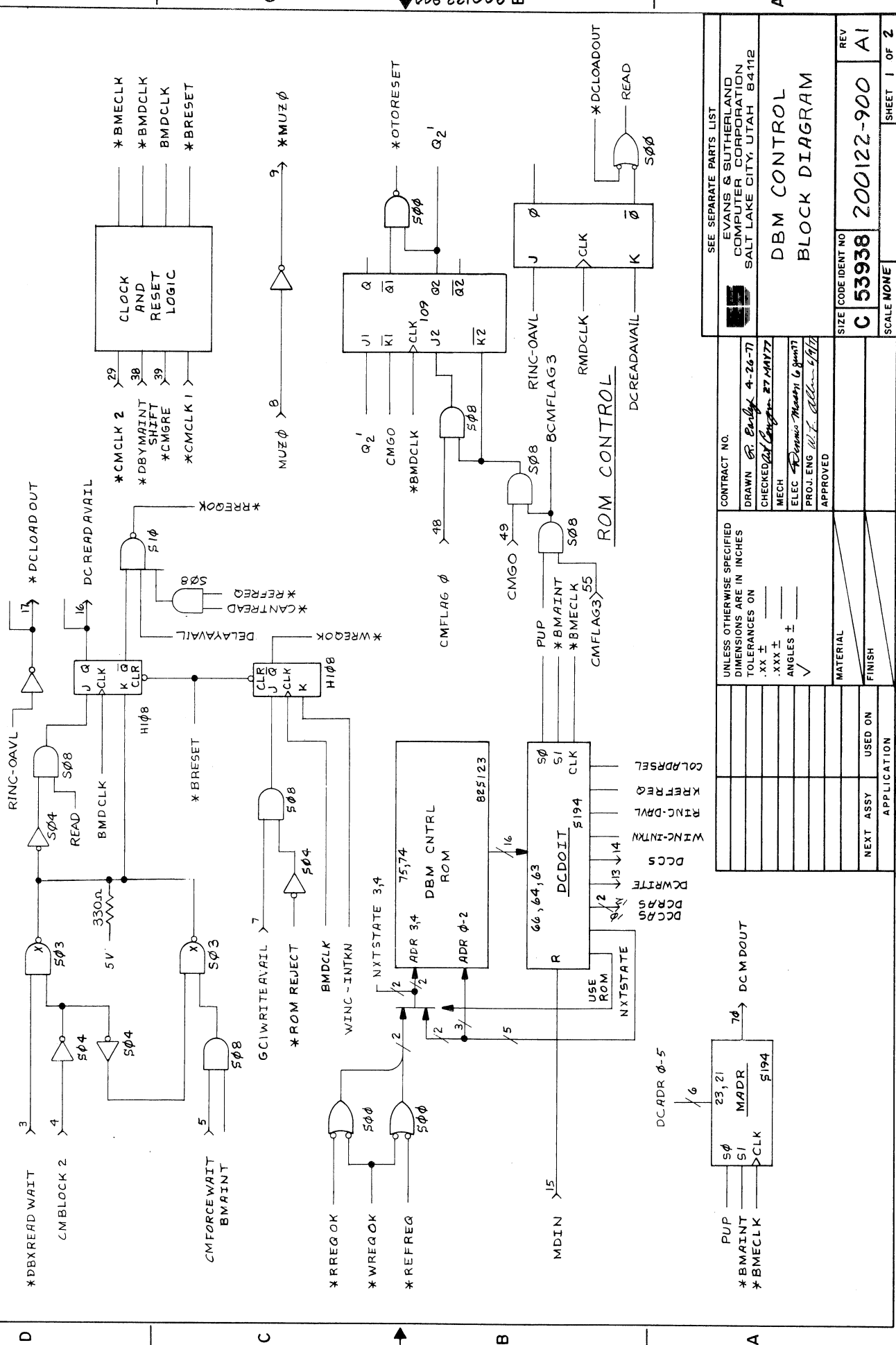
REV	NC
SIZE CODE IDENT NO	C 5397A
SCALE NO	200120-000
SHEET 2 OF 2	

Z-COOD

MUZ15-O *DVZMFMENABLE

MUZ15-O *DVZMFMENABLE

ZONE	DESCRIPTION	DATE	APPROVED
A1	REV UPDATE W/ NO DWG CHNG	1-12-77	[Signature]

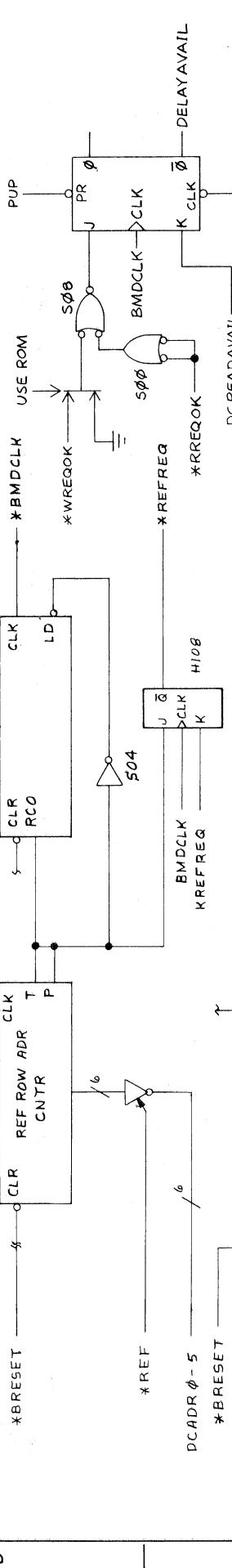


SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.		DRAWN R. Budy 4-26-77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CHECKED BY [Signature] 27 MAY 77	
TOLERANCES ON		MECH XXX ±	
ANGLES ±		ELEC [Signature] 6/2/77	
MATERIAL		PROJ. ENG. W. F. [Signature] 6/1/77	
FINISH		APPROVED	
APPLICATION		SIZE CODE IDENT NO	
NEXT ASSY		C 53938	
USED ON		REV	
APPLICATION		200122-900	
APPLICATION		REV	
APPLICATION		A1	
APPLICATION		SHEET 1 OF 2	

200122-900

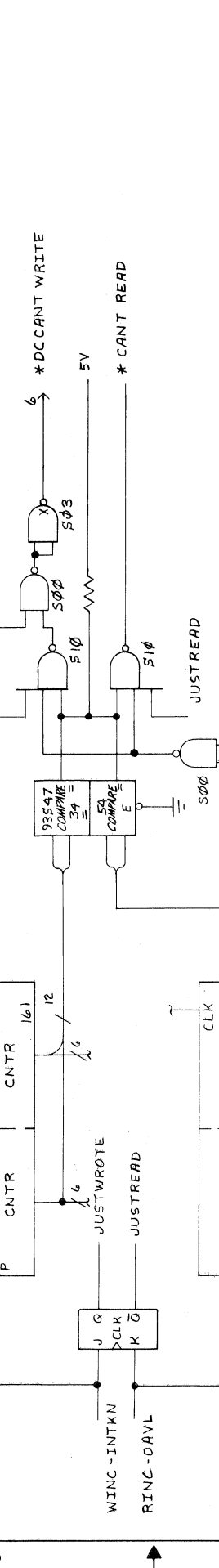
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D



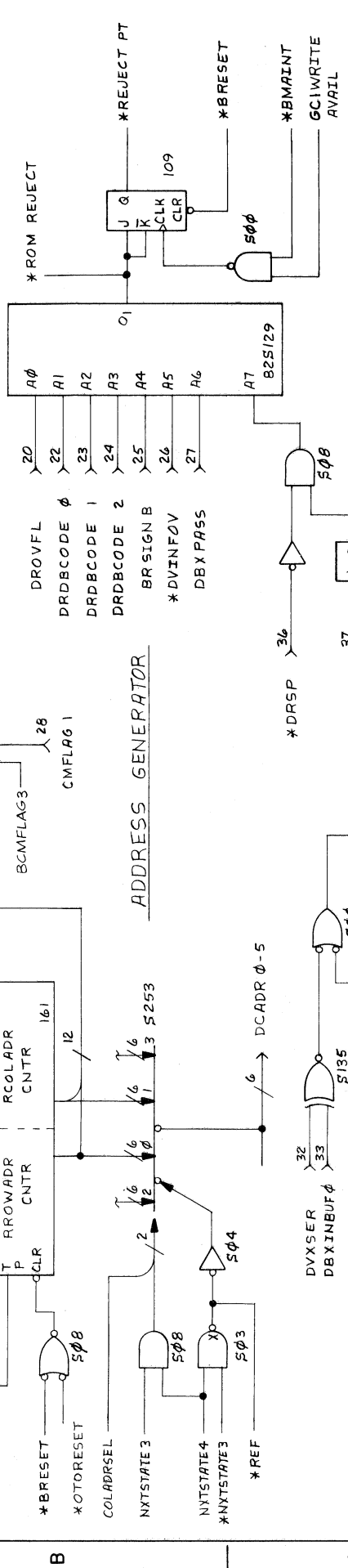
C

C



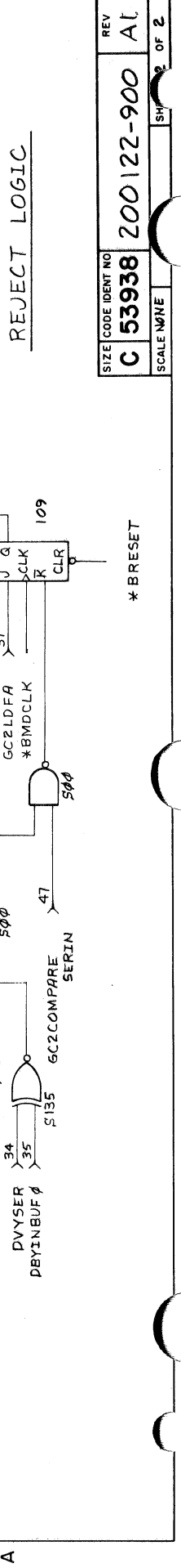
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B



A

A

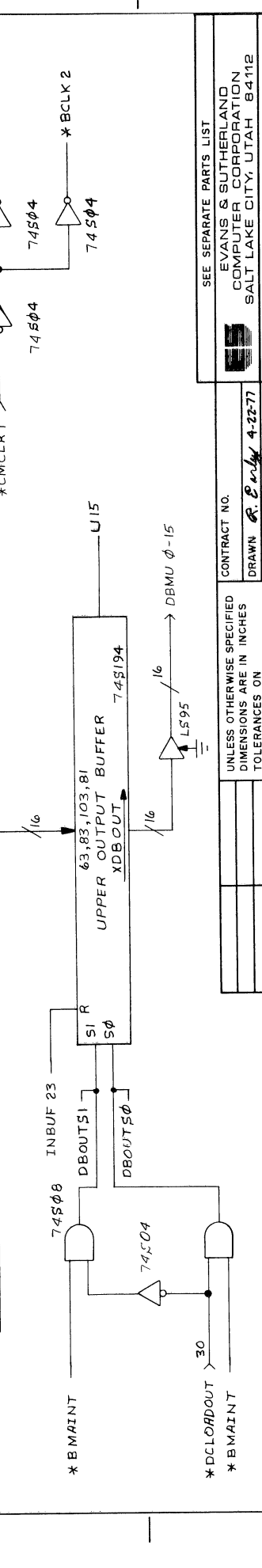
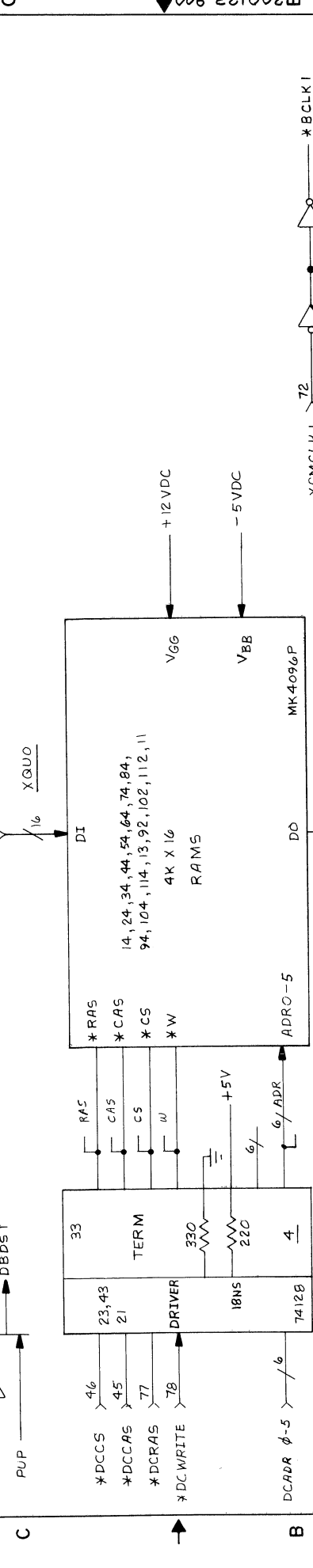
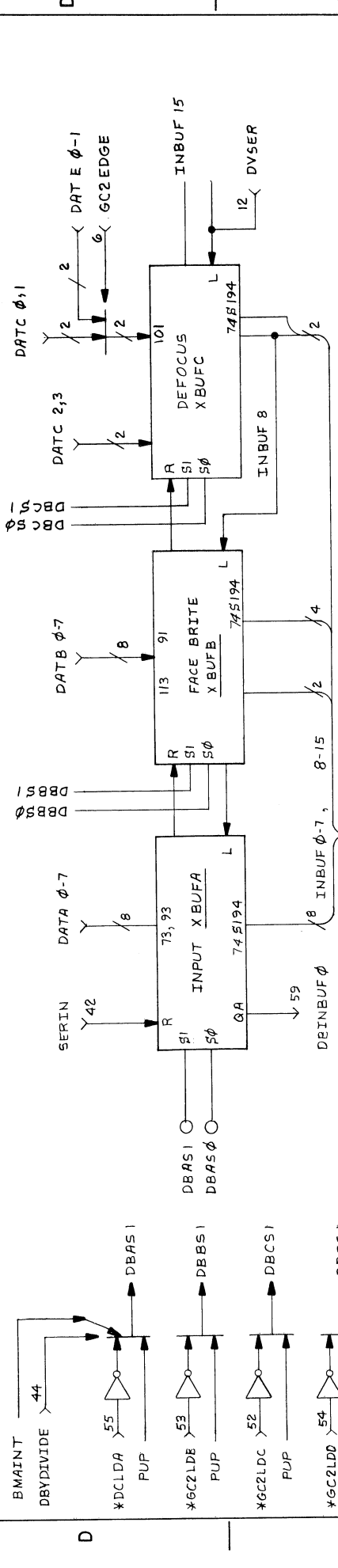


SIZE CODE IDENT NO	REV
C 53938	200122-900
SCALE NAME	AI
	OF 2

REJECT LOGIC

B 200122-900

ZONE	AI	REV	DATE	APPROVED
DESCRIPTION	REV UPDATE W/NO DWG CHNG JB 11-12-78			

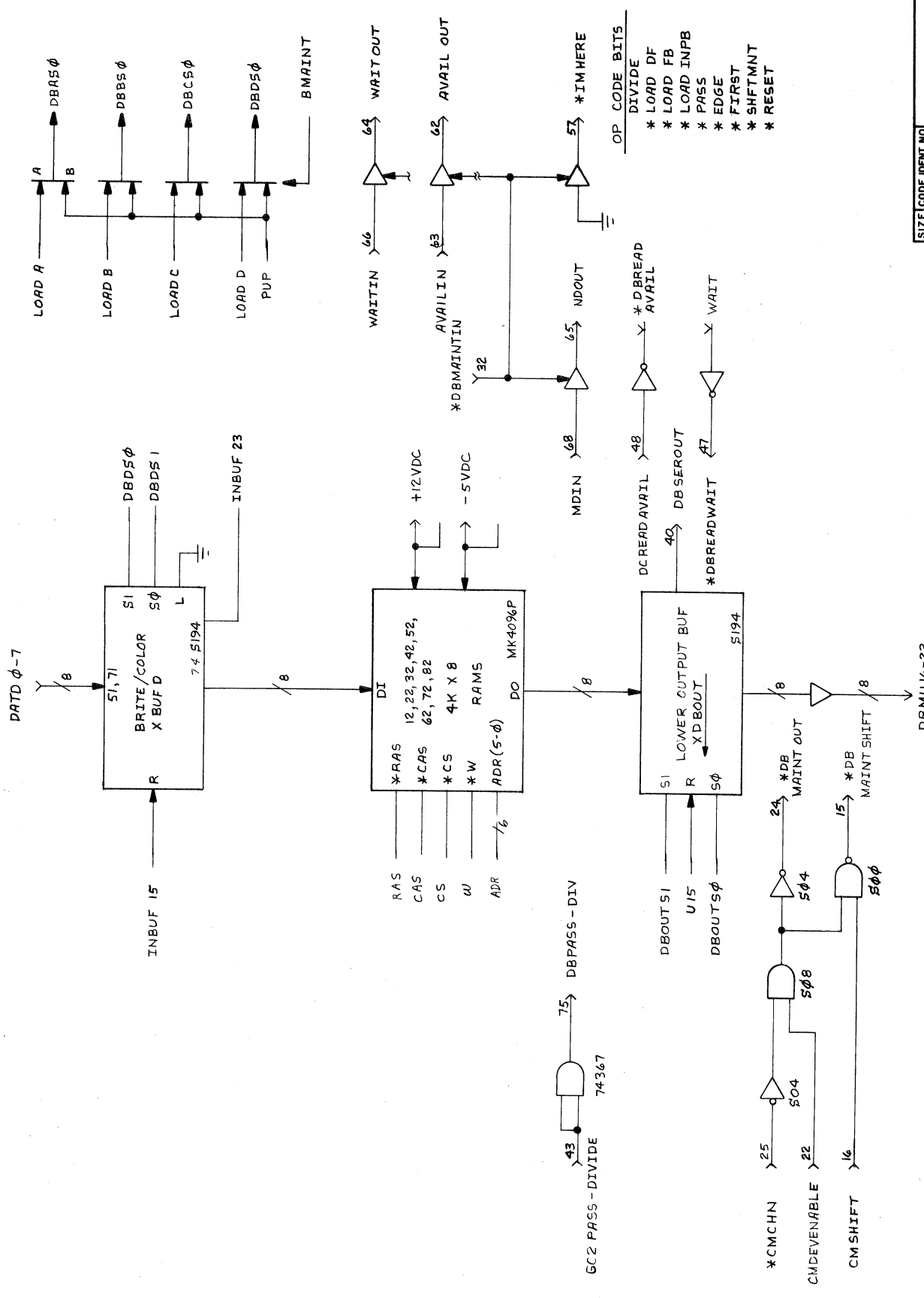


SEE SEPARATE PARTS LIST		CONTRACT NO.	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON		DRAWN	R. E. G. 4-22-77
.XX ±		CHECKED	W. H. G. 27 MAY 77
.XXX ±		MECH	
ANGLES ±		ELEC	R. M. G. 6/2/77
MATERIAL		PROJ. ENG	W. F. B. 4/19/77
FINISH		APPROVED	
NEXT ASSY		SIZE	CODE IDENT NO
APPLICATION		C 53938	200123-900
		SCALE	NONE
		REV	AI
		SHEET 1 OF 2	

**DISPLAY BUFFER
MEMORY
BLOCK DIAGRAM**

200123-900

REVISIONS		
ZONE LTR	DESCRIPTION	DATE APPROVED



SIZE	CODE IDENT NO	REV
C	53938	A1
SCALE	NAME	2 OF 2
	200123-900	

DBMU 16-23

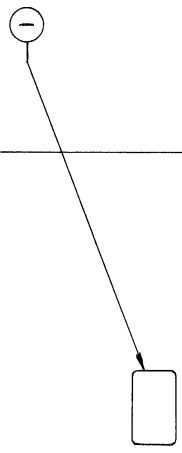
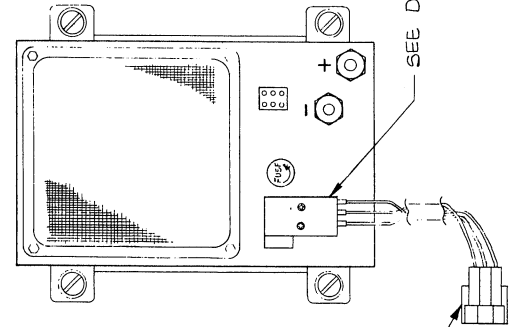
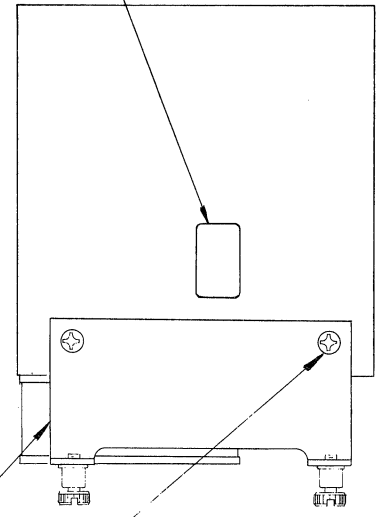
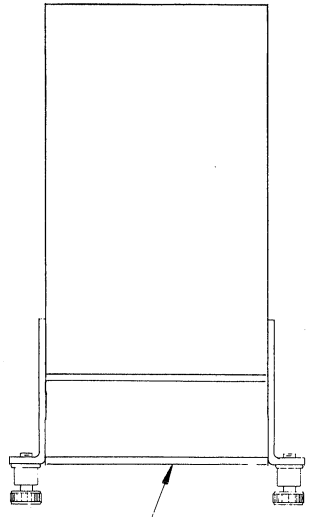
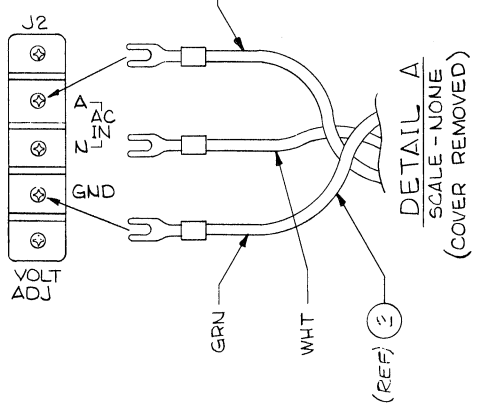
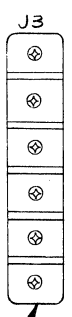
REV. 1

2

3

4

ZONE	LTR	DESCRIPTION	DATE	APPROVED
-	NC	ADDED DETAIL "A" INFORMATION, THIS DRAWING FORMALLY 200151-100-15-77	8-15-77	R Spolia



SEE SEPARATE PARTS LIST		CONTRACT NO.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		DRAWN ADS 6-15-77		.XX ±	
ASSY, POWER SUPPLY, 5V, 150A (NOVONVIEW SP)		CHECKED Jim Pino 8-15-77		.XXX ±	
SIZE CODE IDENT NO C 53938		MECH		ANGLES ±	
200151-100		ELEC R Spolia 15 AUG 77		✓	
SHEET 1 OF 1		PROJ. ENG		MATERIAL	
SCALE 1/2		APPROVED Jim Pino 8-15-77		SEE PARTS LIST	
				FINISH	
				200151-100 NOVONVIEW	
				NEXT ASSY USED ON	
				APPLICATION	

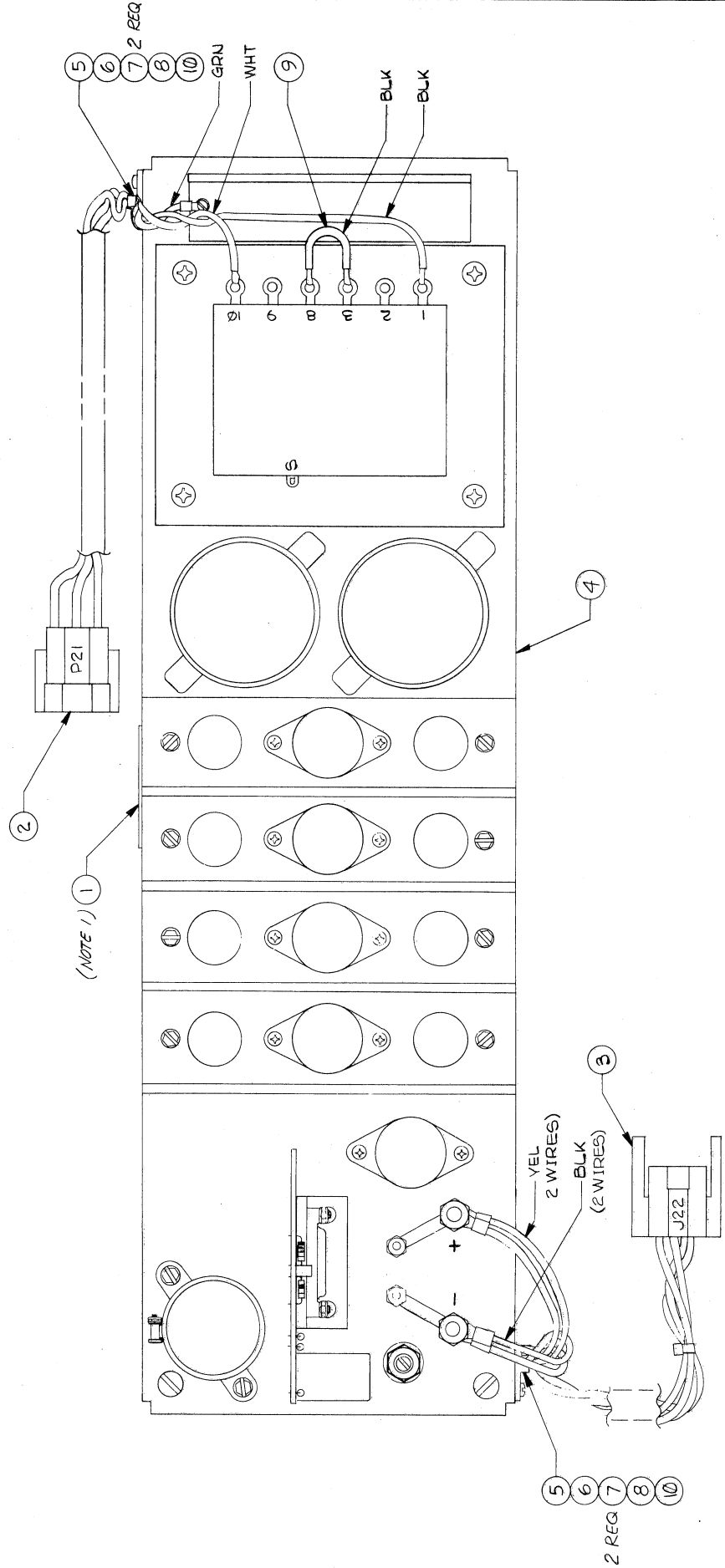
1. REMOVE TWO JUMPERS FROM JB

NOTES:

4 3 2 1

ZONE	LTR	DESCRIPTION	DATE	APPROVED
NC		FORMALLY RELEASED TO NC	8/17/77	DAL

D C B A



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
ASSY, POWER SUPPLY, 12V, 15A (NOVOVIEW 5P)	
SIZE CODE IDENT NO	REV
C 53938	NC
SCALE 1/16" = 1"	OF 1

CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
DRAWN <i>[Signature]</i> 8-16-77	TOLERANCES ON
CHECKED <i>[Signature]</i> 8/17/77	.XX ±
MECH	.XXX ±
ELEC	ANGLES ±
PROJ. ENG <i>[Signature]</i> 8/17/77	✓
APPROVED	
MATERIAL	SEE PARTS LIST
200154-100	NOVOVIEW
NEXT ASSY	USED ON
	APPLICATION

MARK ASSY NO., S/N, & ECO NO. USING BLACK OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIVALENT.

NOTES:

200152-100

ZONE	LTR	DESCRIPTION	DATE	APPROVED
A1	ADDED ITEM 12.		9/13/77	D. SUTHERLAND
				40877

D C B A

3 2 1

200153-100

SEE SEPARATE PARTS LIST

EVANS & SUTHERLAND
COMPUTER CORPORATION
SALT LAKE CITY, UTAH 84112

ASSY, POWER SUPPLY,
± 15 V, 2.8 A

SIZE CODE IDENT NO
C 53938

REV A1

SHEET 1 OF 7

SCALE 1/2

CONTRACT NO.
DRAWN D. CARROLL 8-16-77

CHECKED D. SUTHERLAND 8/17/77

MECH _____

ELEC _____

PROJ. ENG. APPROVED _____

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES

TOLERANCES ON .XX ± _____

.XXX ± _____

ANGLES ± _____

MATERIAL SEE PARTS LIST

FINISH

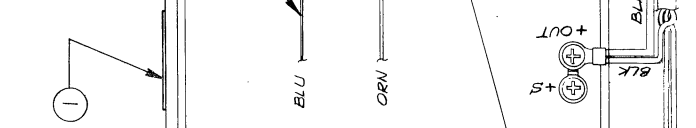
200153-100 NO VIEW

NEXT ASSY USED ON

APPLICATION

NOTE 1

(NOTE 2) 1



(11 PLACES)
COVER TERMINALS

7 (2 REQ'D)

5 6 7 (2 REQ'D) 8 11

BLU ORN BLK GRN

NOTE 1

(NOTE 2) 1

7 (2 REQ'D)

5 6 7 (2 REQ'D) 8 11

BLU ORN BLK GRN

7 (2 REQ'D)

5 6 7 (2 REQ'D) 8 11

BLU ORN BLK GRN

7 (2 REQ'D)

5 6 7 (2 REQ'D) 8 11

BLU ORN BLK GRN

7 (2 REQ'D)

5 6 7 (2 REQ'D) 8 11

BLU ORN BLK GRN

7 (2 REQ'D)

5 6 7 (2 REQ'D) 8 11

2. MARK ASSY NO., S/N & ECO NO. USING
BLACK OPAQUE INDUSTRIAL INK, PHILLIPS
PROCESS CO, NO. 35A OR EQUIV.

1. CONNECT EXISTING BLUE WIRES (2) AS SHOWN.

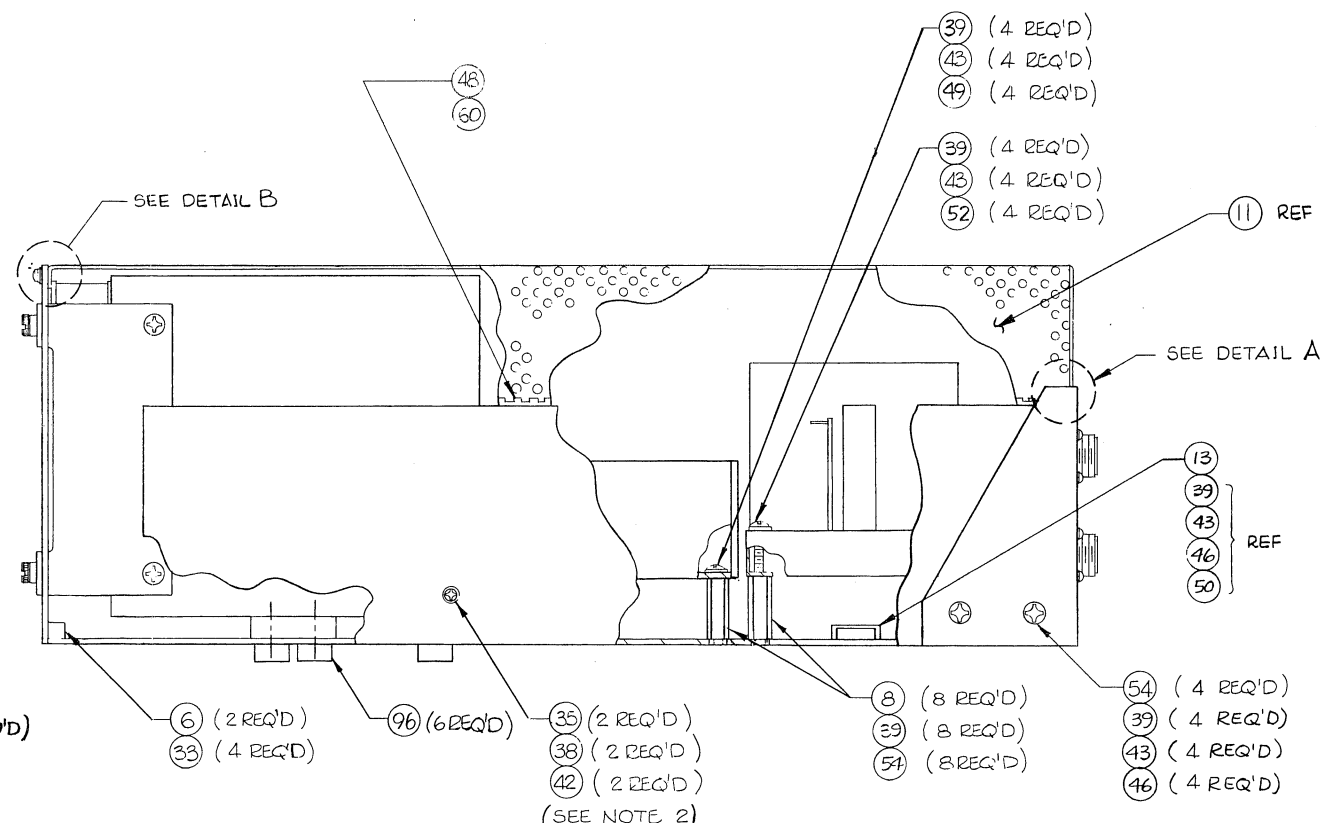
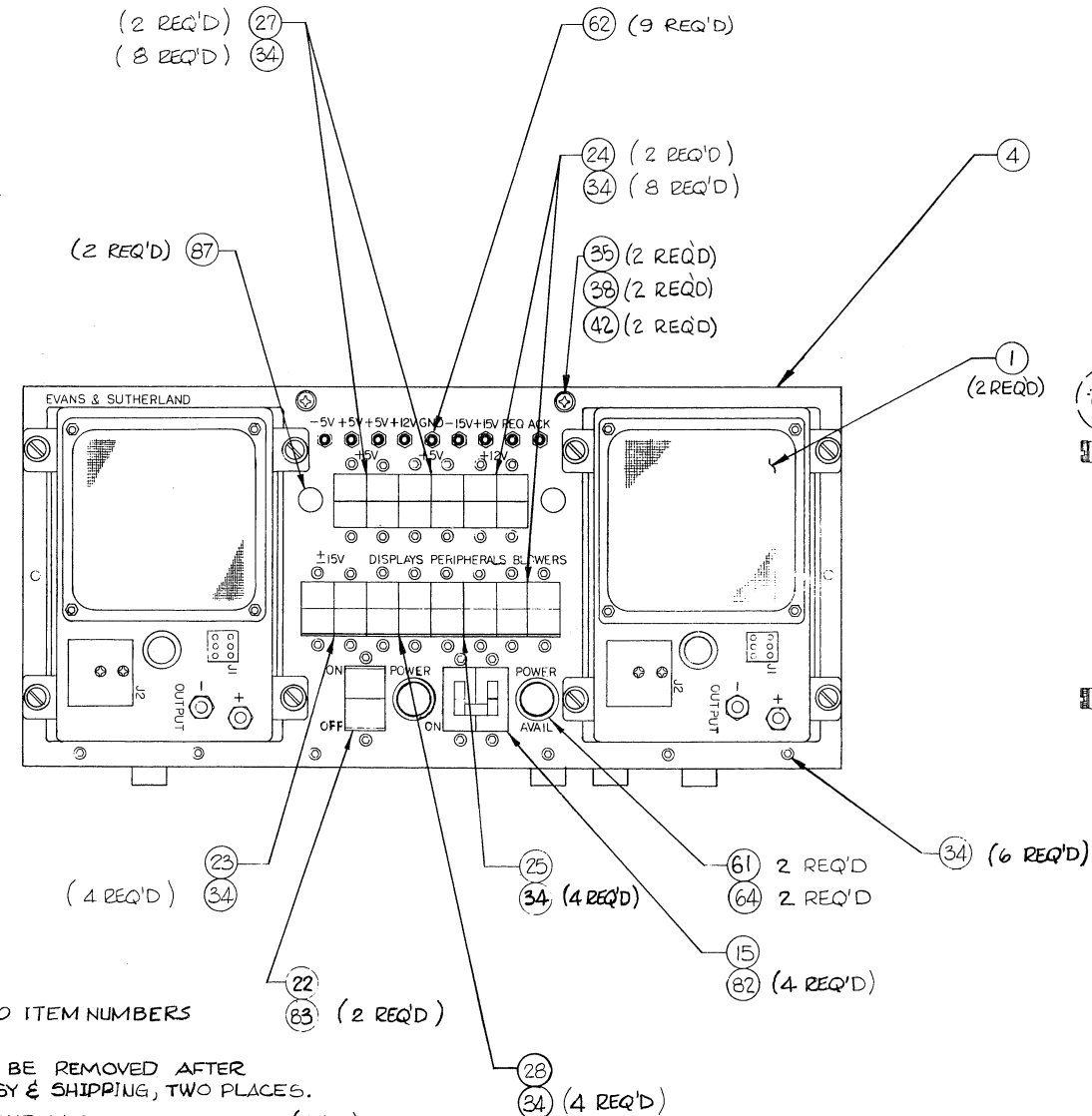
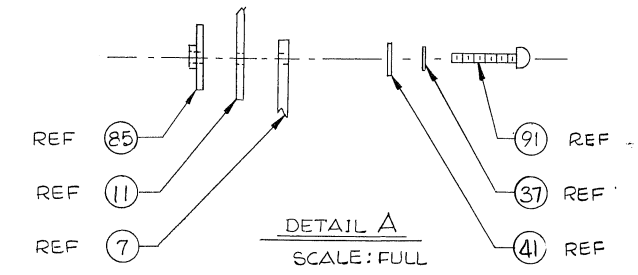
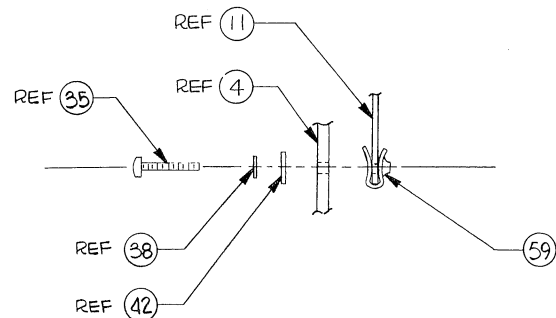
NOTES:



DAS/NO	REVISIONS				
	ZONE	LTR	DESCRIPTION	DATE	APPROVED
-	A1		EXTENSIVE PARTS LIST & DRAWING CHANGES	9-7-77	Shino 9/14/77
-	A2		EXTENSIVE CHANGES DC	11-3-77	Spain 11/17/77
P2, C7 P3, B8	A3		ITEM 55 WAS 38 FT. ADDED ITEM 56, 43 FT.	5-18-78	Spain
-104	A1		ITEM 55 WAS 38 FT ADDED ITEM 56, 43 FT	6-5-78	Spain

TABULATION TABLE

DRAWING NO.	ELAPSED TIME METER PART NO.	ITEM NO.	ECO
200154-100	801275-003	17	A3
200154-104	801275-002	17	A1



3. BALLOON CALL OUTS REFER TO ITEM NUMBERS ON THE PARTS LIST.
2. TEMPORARY HARDWARE TO BE REMOVED AFTER TESTING & BEFORE FINAL ASSY & SHIPPING, TWO PLACES.
1. REFERENCE DRAWINGS: SCHEMATIC, 200154-100, (SH 3). WIRING HARNESS, 200183-100. (65) PARTS LIST 200154-100.
- UNLESS OTHERWISE SPECIFIED:
- NOTES:

200154-100, -104
SEE SEPARATE PARTS LIST

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON .XXX ± .XXX ± ANGLES ± MATERIAL SEE PARTS LIST FINISH		CONTRACT NO. DRAWN J. M. PINO 6/10/77 CHECKED Jim Pino 31 AUG 77 MECH Jim Pino 31 AUG 77 ELEC Ron Speier 31 AUG 77 PROJ. ENG. Hubert Black 9/11/77 DESIG. AUTO. APPROVED Jim Pino 12-6-77	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112 TITLE ASSY, PRIMARY POWER CONTROL (NO VIEW SP) DWG 200154-100/104 REV SEE TABLE SHEET 1 OF 3
--	--	--	---

200154-TAB



8

7

6

5

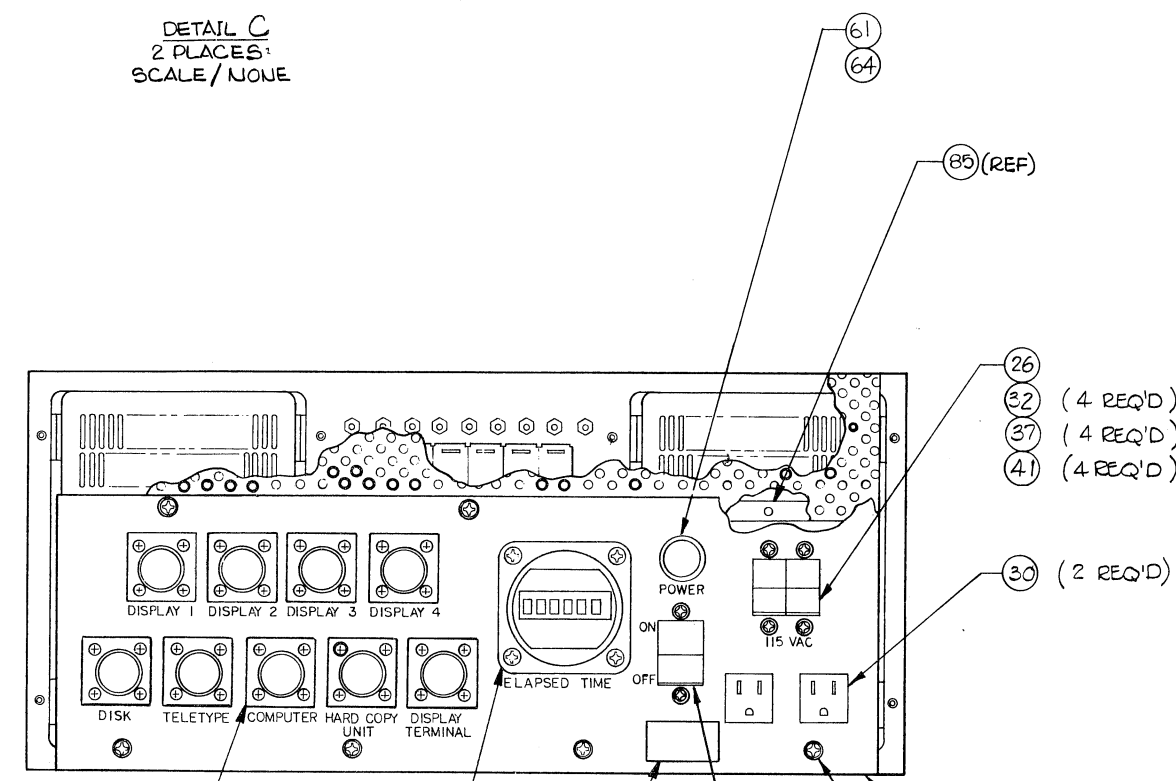
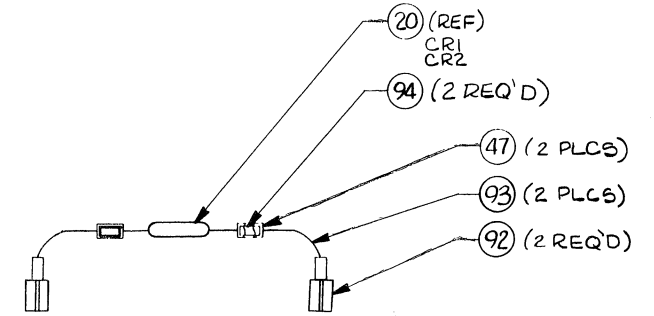
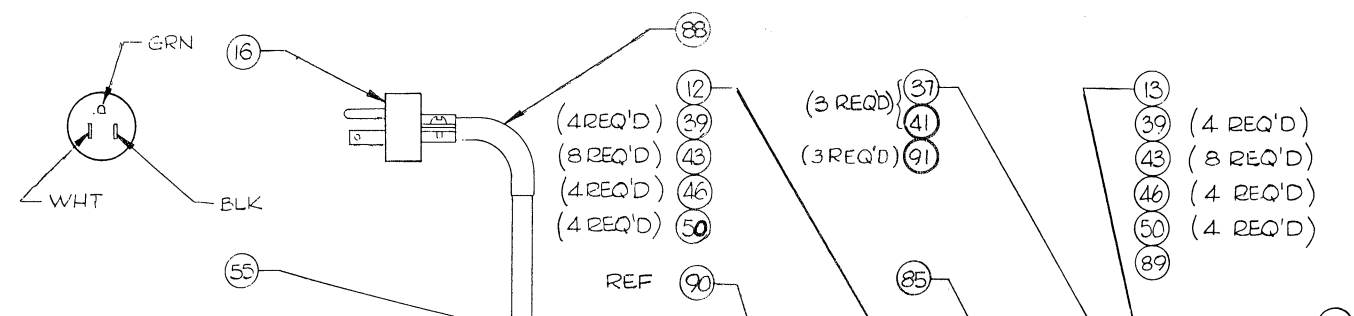
4

3

2

1

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED



D
C
B
A

D
C
B
A

200154-TAB

- (4 REQ'D) 39
- (6 REQ'D) 43
- (4 REQ'D) 46
- (3 REQ'D) 50
- (5 REQ'D) 89

(2 REQ'D) SEE DETAIL C

- (4 REQ'D) 39
- (8 REQ'D) 43
- (4 REQ'D) 46
- (4 REQ'D) 49

- (4 REQ'D) 39
- (8 REQ'D) 43
- (4 REQ'D) 46
- (4 REQ'D) 50
- (14 REQ'D) 89

- (4 REQ'D) 39
- (8 REQ'D) 43
- (4 REQ'D) 46
- (4 REQ'D) 50
- (14 REQ'D) 89

(2 PLCS) 60, 63

(2 REQ'D) 9, (4 REQ'D) 33

(20 REQ'D) 78 LOCATE APPROX. AS SHOWN.

- (4 REQ'D) 39
- (4 REQ'D) 45
- (4 REQ'D) 49

(2 REQ'D) 90 LOCATE APPROXIMATELY AS SHOWN ON ITEM 11

- (2 REQ'D) 29
- (2 REQ'D) 39
- (4 REQ'D) 43
- (2 REQ'D) 46
- (2 REQ'D) 53

- (2 REQ'D) 21
- (2 REQ'D) 37
- (4 REQ'D) 41
- (2 REQ'D) 45
- (2 REQ'D) 84
- (2 REQ'D) 91

- (9 REQ'D) 18
- (36 REQ'D) 31
- (36 REQ'D) 36
- (72 REQ'D) 40
- (36 REQ'D) 44

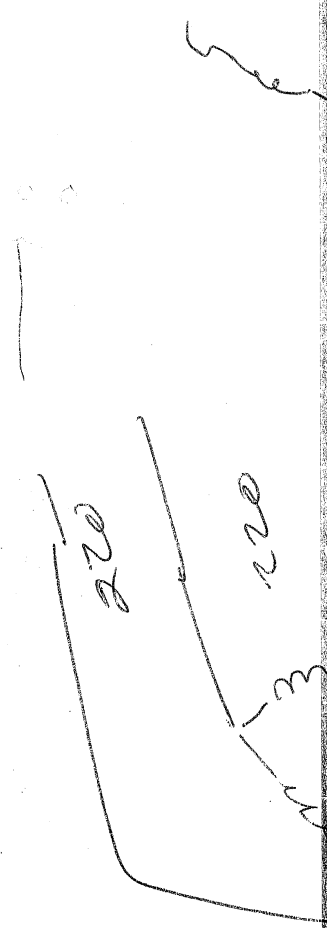
- (4 REQ'D) 17
- (4 REQ'D) 37
- (8 REQ'D) 41
- (4 REQ'D) 45
- (4 REQ'D) 91

- (2 REQ'D) 22
- (2 REQ'D) 37
- (2 REQ'D) 41
- (2 REQ'D) 99

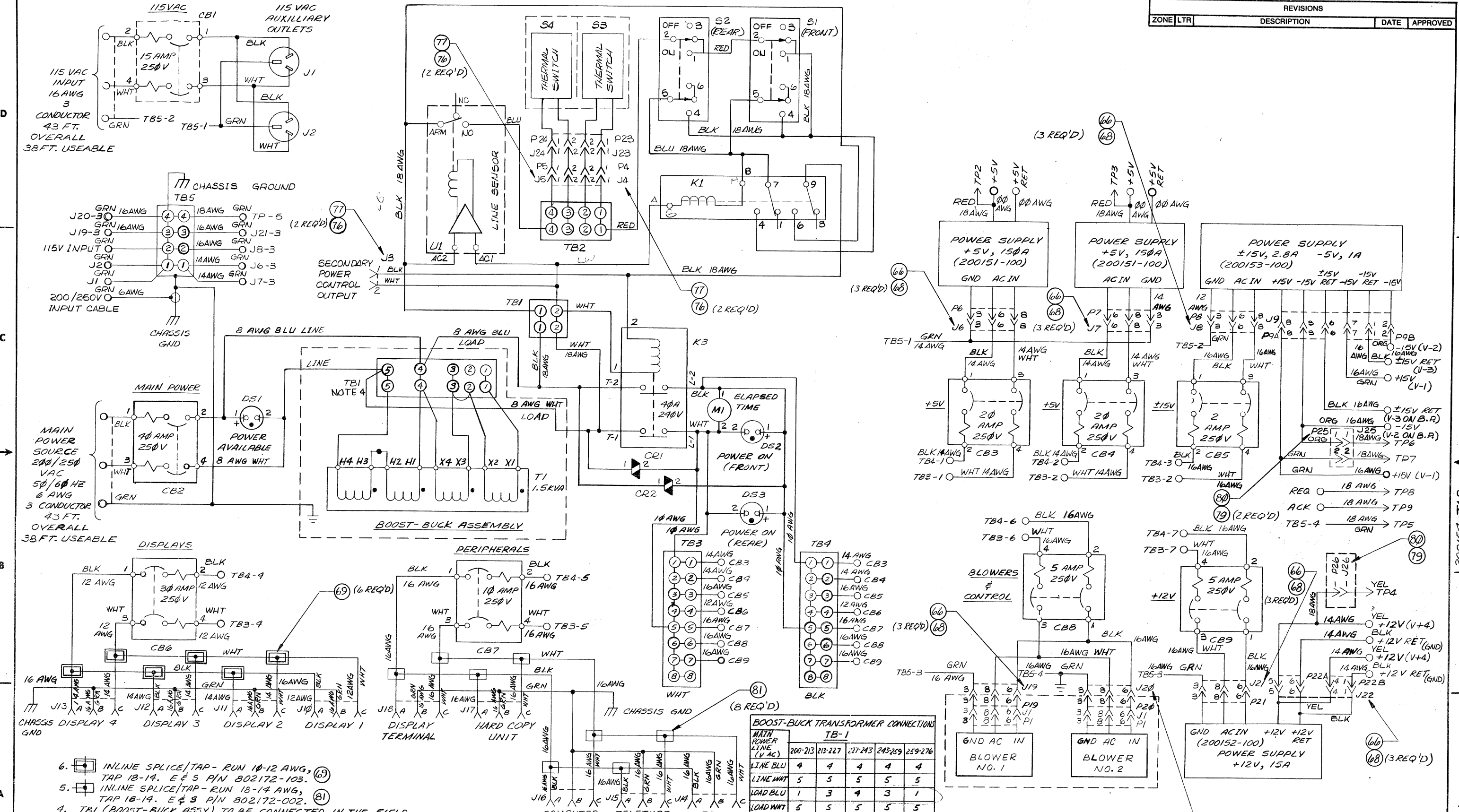
- (4 REQ'D) 39
- (8 REQ'D) 43
- (4 REQ'D) 46
- (4 REQ'D) 49

200154-100, -104

D	DWG	200154-100/104	REV SEE TABLE
	SCALE 1/2		



REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED



- NOTES:
1. WIRE SIZE TO BE 14 AWG. UNLESS OTHERWISE SPECIFIED:
 2. CR1, CR2, M1, DS1, DS2, DS3 PIGTAIL LEADS TO BE 18 AWG WIRE.
 3. ALL CHASSIS GROUNDS MUST ASSURE ELECTRICAL CONTACT.
 4. TB1 (BOOST-BUCK ASSY) TO BE CONNECTED IN THE FIELD TO PROVIDE 230V SYSTEM POWER ACCORDING TO TABLE I.
 5. INLINE SPLICE/TAP- RUN 13-14 AWG, TAP 16-14. E & S P/N 802172-002. (81)
 6. INLINE SPLICE/TAP- RUN 10-12 AWG, TAP 18-14. E & S P/N 802172-103. (65)

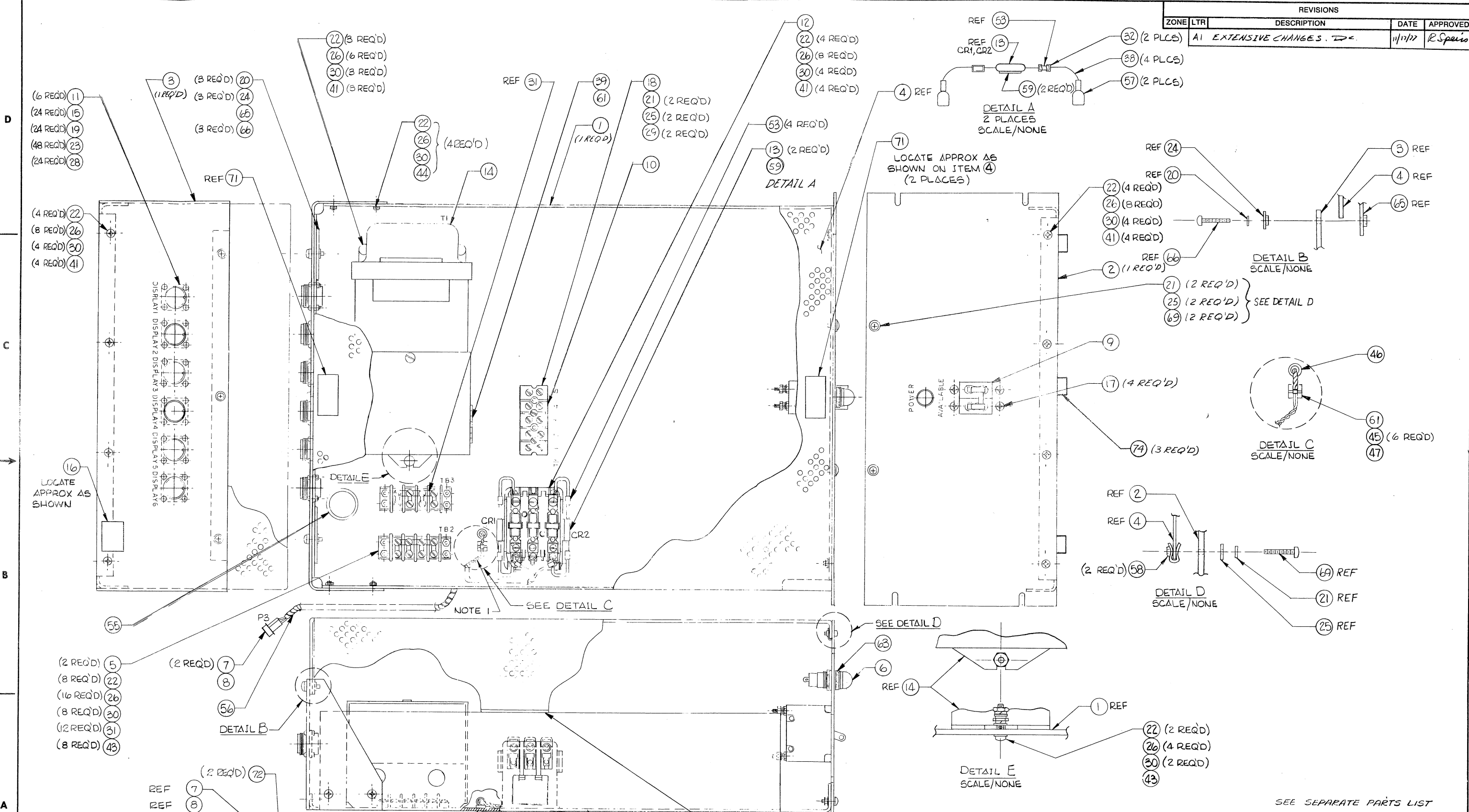
7. WIRE, 12 AWG. ITEM 97 BLK
 ITEM 98 WHT
 WIRE 14 AWG. ITEM 71 WHT.
 ITEM 72 BLK.
 WIRE, 16 AWG. ITEM 73 GRN.
 ITEM 74 WHT
 ITEM 75 BLK

BOOST-BUCK TRANSFORMER CONNECTIONS

MAIN POWER LINE (V AC)	TB-1				
	200-213	213-227	227-243	243-259	259-276
LINE BLU	4	4	4	4	4
LINE WHT	5	5	5	5	5
LOAD BLU	1	3	4	3	1
LOAD WHT	5	5	5	5	5
H4	5	5	5	4	4
H1	4	4	4	5	5
X4	4	4	4	4	4
X3	3	3	3	3	3
X2	2	2	2	2	2
X1	1	1	1	1	1

SCHEMATIC DIAGRAM

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
A1		EXTENSIVE CHANGES. DR	11/17/77	R. Spies



1. TWIST WIRE LOOSELY TOGETHER FROM ITEM 12 TO CHASSIS, ITEM 1.

NOTES:

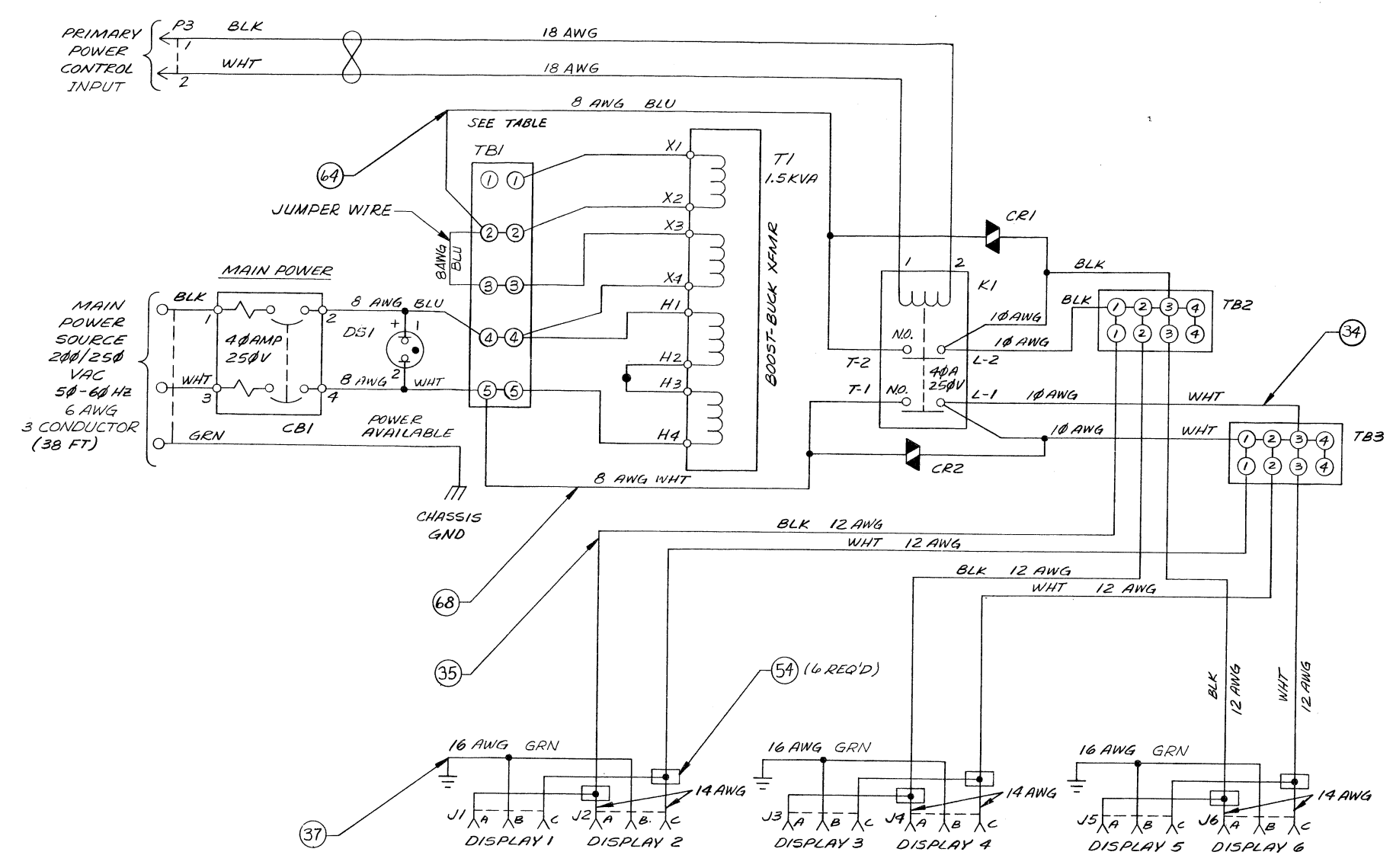
UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON: .XX ± .XXX ± ANGLES ± MATERIAL FINISH		CONTRACT NO. DRAWN <i>W. D. Rupp 9/3/77</i> CHECKED <i>W. D. Rupp 3 NOV 77</i> MECH <i>W. D. Rupp 3 NOV 77</i> ELEC PROJ. ENG. <i>W. D. Rupp 15 Nov 77</i> DESIG. AUTO. APPROVED	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112 TITLE ASSY SECONDARY POWER CONTROL (NOVOVIEW SP) DWG 200154-101 REV A1
200102-100 NEXT ASSY	NovoVIEW USED ON	SCALE 1/2	SHEET 1 OF 3

200154-101





REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED



BOOST BUCK TRANSFORMER CONNECTIONS
TBI

MAIN POWER LINE (VOLTS AC)	200-213	213-227	227-243	243-259	259-276
LINE BLU	4	4	4	4	4
LINE WHT	5	5	5	5	5
LOAD BLU	1	3	4	3	1
LOAD WHT	5	5	5	5	5
H4	5	5	5	4	4
H1	4	4	4	5	5
X4	4	4	4	4	4
X3	3	3	3	3	3
X2	2	2	2	2	2
X1	1	1	1	1	1

- NOTES:
1. WIRE SIZE TO BE 14 AWG.
 2. CR1, CR2, DSI PIGTAIL LEADS TO BE 18 AWG WIRE.
 3. ALL CHASSIS GROUNDS MUST ASSURE ELECT. CONTACT.
 4. TBI TO BE CONNECTED IN THE FIELD TO PROVIDE 230V SYSTEM POWER.
 5. INLINE SPLICE/TAP-RUN 10-12 AWG, TAP 14-18 AWG. E&S P/N 802172-103.

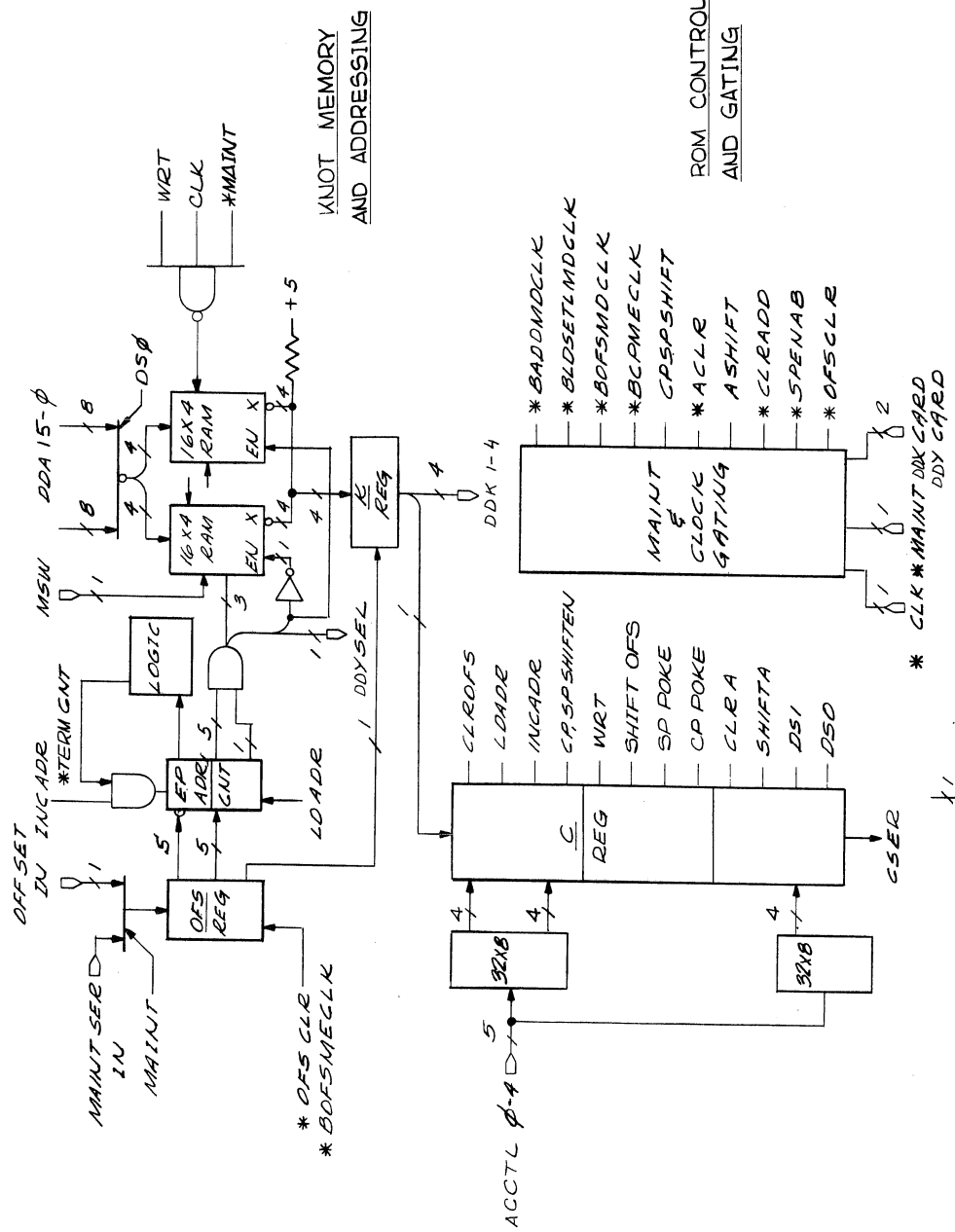
SCHMATIC

SIZE	CODE IDENT NO	REV
D	53938 200154-101	A1
SCALE NONE DO NOT SCALE DWG		SHEET 3 OF 3

200154-301



ZONE	DATE	APPROVED
A2	1-4-78	P. [Signature]
DESCR. W/ NO DWG. CHNG. [Signature]		
REV UPDATE		



SEE SEPARATE PARTS LIST		REV
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		A2
DISPLAY DATA (BLOCK DIAGRAM)		
SIZE CODE IDENT NO	REV	
C 53938	200155-900	A2
SCALE	NONE	SHEET 1 OF 2

CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
DRAWN AND RAPP 11/8/77	TOLERANCES ON
CHECKED [Signature] 5-4-77	.XX ±
MECH	.XXX ±
ELEC [Signature] 2-2-77	ANGLES ±
PROJ. ENGR [Signature] 2 JULY 77	✓
APPROVED	MATERIAL
	FINISH
	NEXT ASSY
	USED ON
	APPLICATION

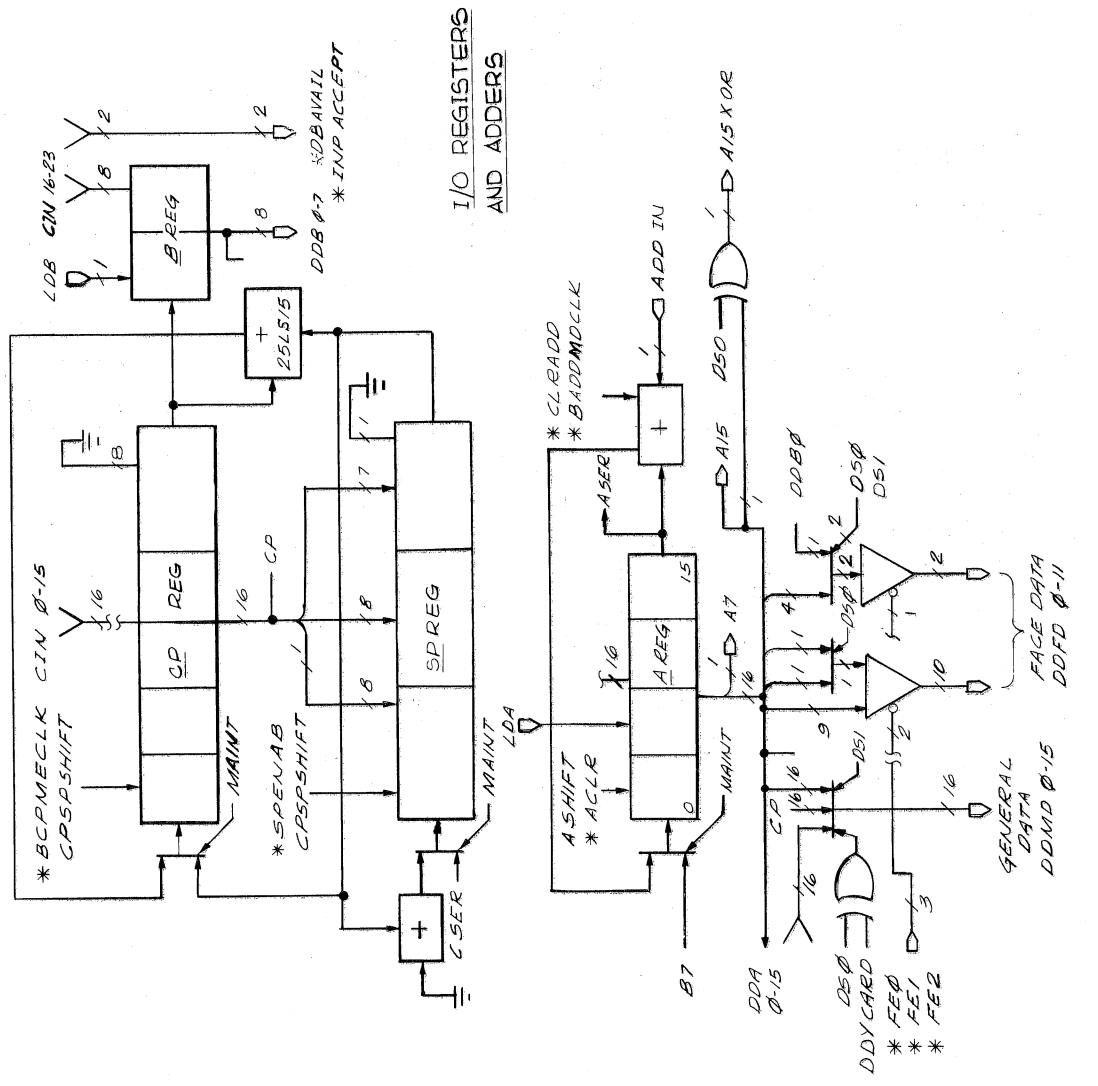
* RESET

* CLK * MAINT DDX CARD
DDY CARD

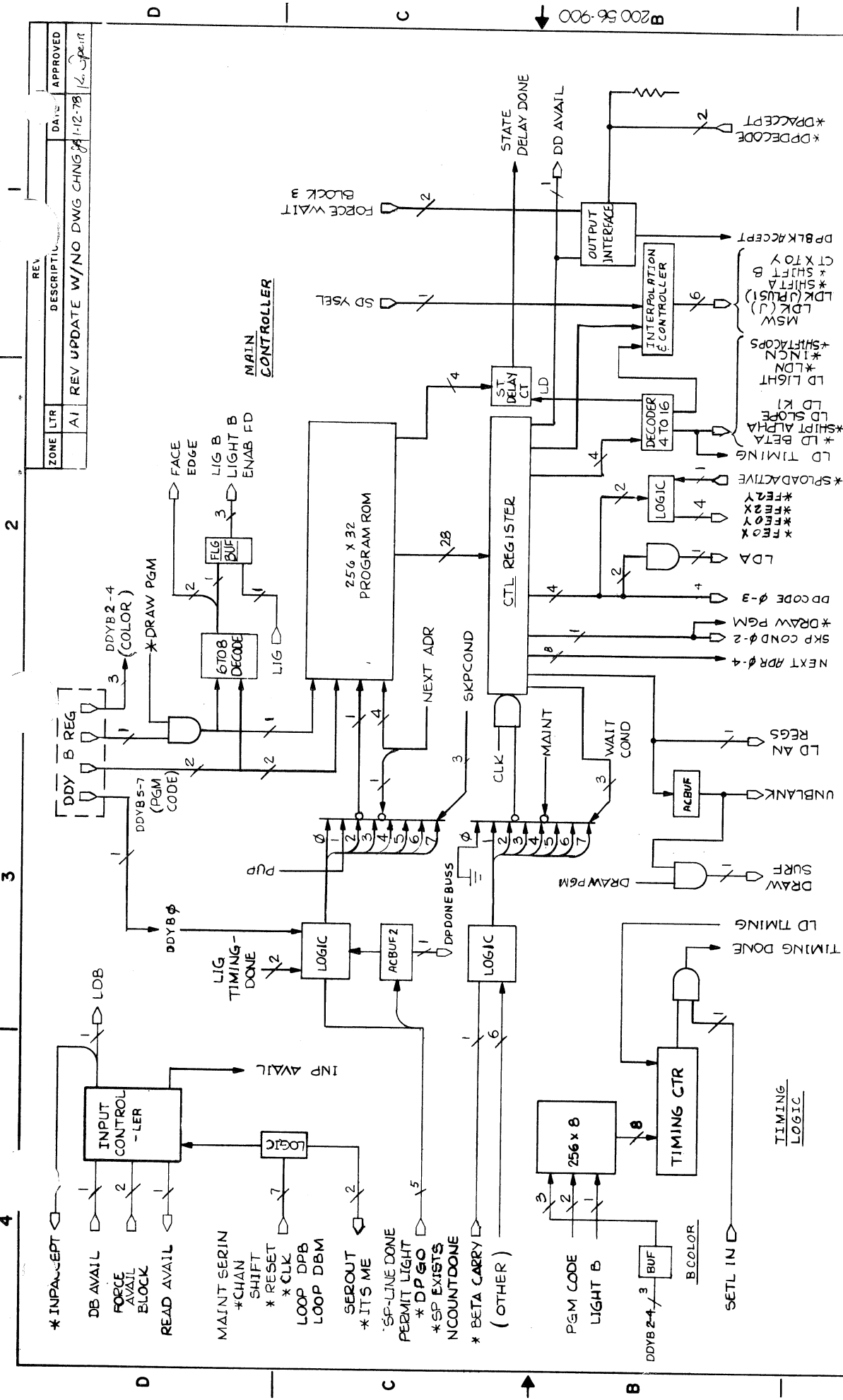
ROM CONTROL
AND GATING

KNOT MEMORY
AND ADDRESSING

ZONE	LTR	REVISIONS	
		DESCRIPTION	DATE APPROVED



SIZE CODE IDENT NO	REV
C 53938	A2
SCALE	2 OF 2



SEE SEPARATE PARTS LIST EVANIS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		SIZE CODE IDENT NO C 53938	PROJECT NO 200156 - 900	REV AI
CONTRACT NO. 7-20-77		CHECKED <i>[Signature]</i> 30 AUG 77		
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .XXX ± ANGLES ±		MATERIAL FINISH		
MECH ELEC PROJ. ENG APPROVED	APPLICATION USED ON NEXT ASSY			
ANALOG CONTROL BLOCK DIAGRAM				
SCALE NONE		SHEET 1 OF 1		

20056-900

* INP ACCEPT
 DB AVAIL
 FORCE AVAIL
 BLOCK
 READ AVAIL
 MAINT SERIN
 *CHAN SHIFT
 * RESET
 LOOP DPB
 LOOP DBM
 SEROUT
 * ITS ME
 * SP-LINE DONE
 PERMIT LIGHT
 * DP GO
 * SP EXISTS
 N COUNTDONE
 * BETA CARRY
 (OTHER)

DDYB 2-4
 (COLOR)
 * DRAW PGM
 DDYB 5-7
 (PGM CODE)
 DDYB φ
 LIG TIMING-DONE
 LIG B
 LIGHT B
 ENAB FD
 FACE EDGE
 DDYB 2-4
 * DRAW PGM
 DDYB 5-7
 (PGM CODE)

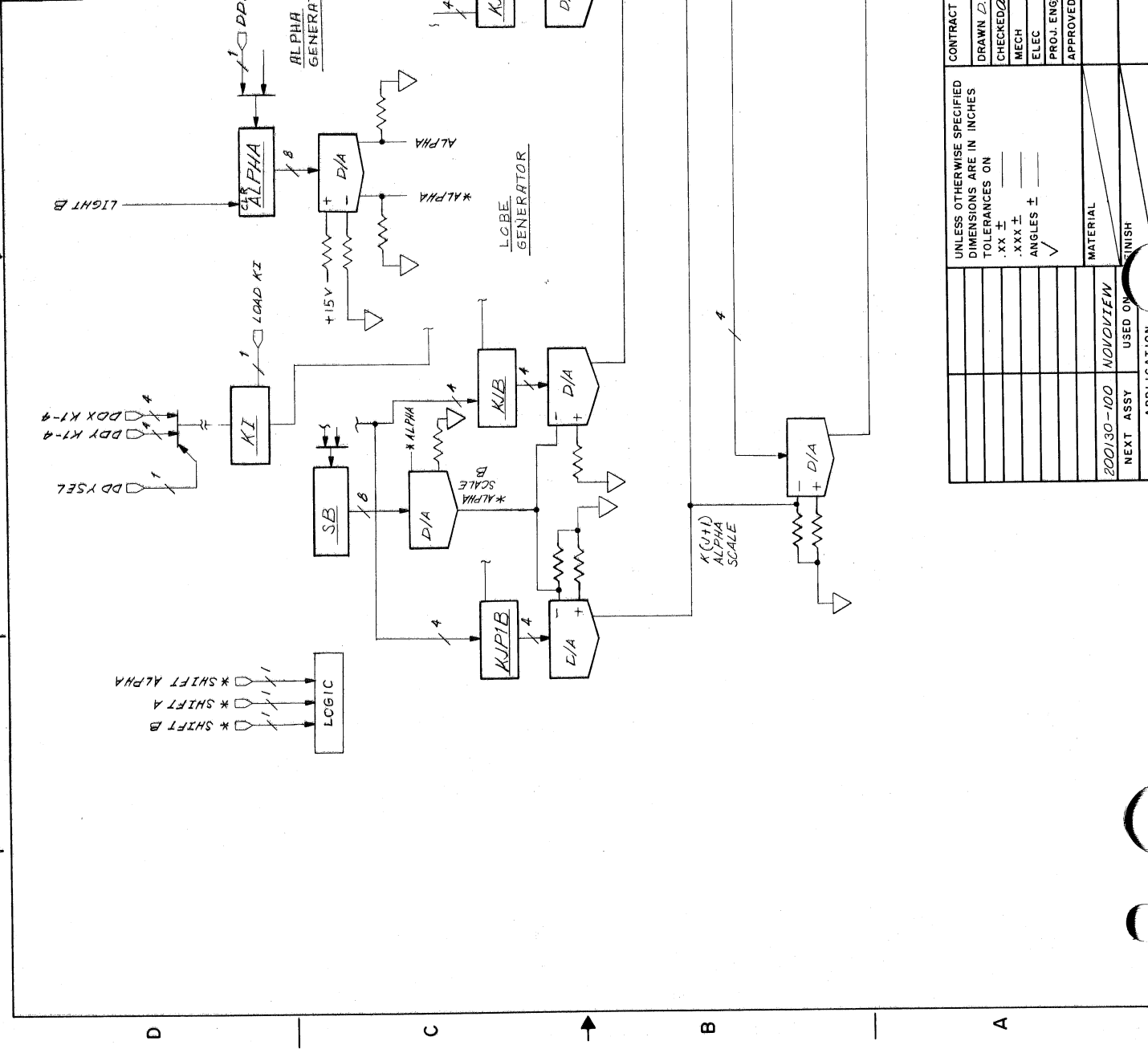
DDYB φ
 LIG TIMING-DONE
 LIG B
 LIGHT B
 ENAB FD
 FACE EDGE
 DDYB 2-4
 * DRAW PGM
 DDYB 5-7
 (PGM CODE)

DDYB 2-4
 * DRAW PGM
 DDYB 5-7
 (PGM CODE)

200157-900

1 2 3 4

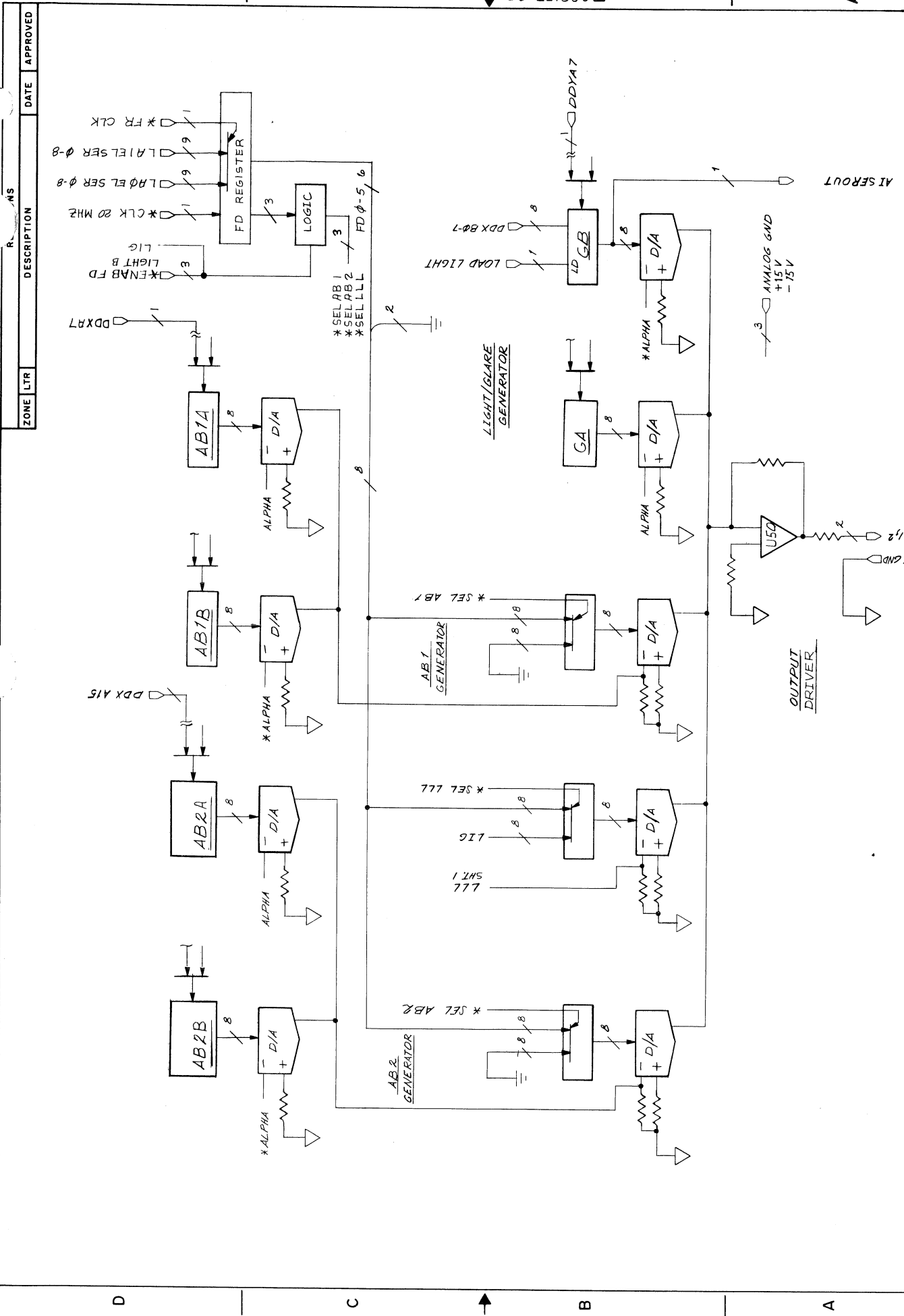
REVISIONS		DATE	APPROVED
ZONE	DESCRIPTION		
A1	UPDATED REV LEVEL. NO DWG. CHANGE.	1/14/77	MB 10/11/77
A2	REV. UPDATE ONLY, NO DWG. CHANGE	6-19-78	RS 10/15



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
ANALOG INTENSITY P.C.	
SIZE CODE IDENT NO	REV
C 53938	200157-900 A2
SCALE	1 OF 2

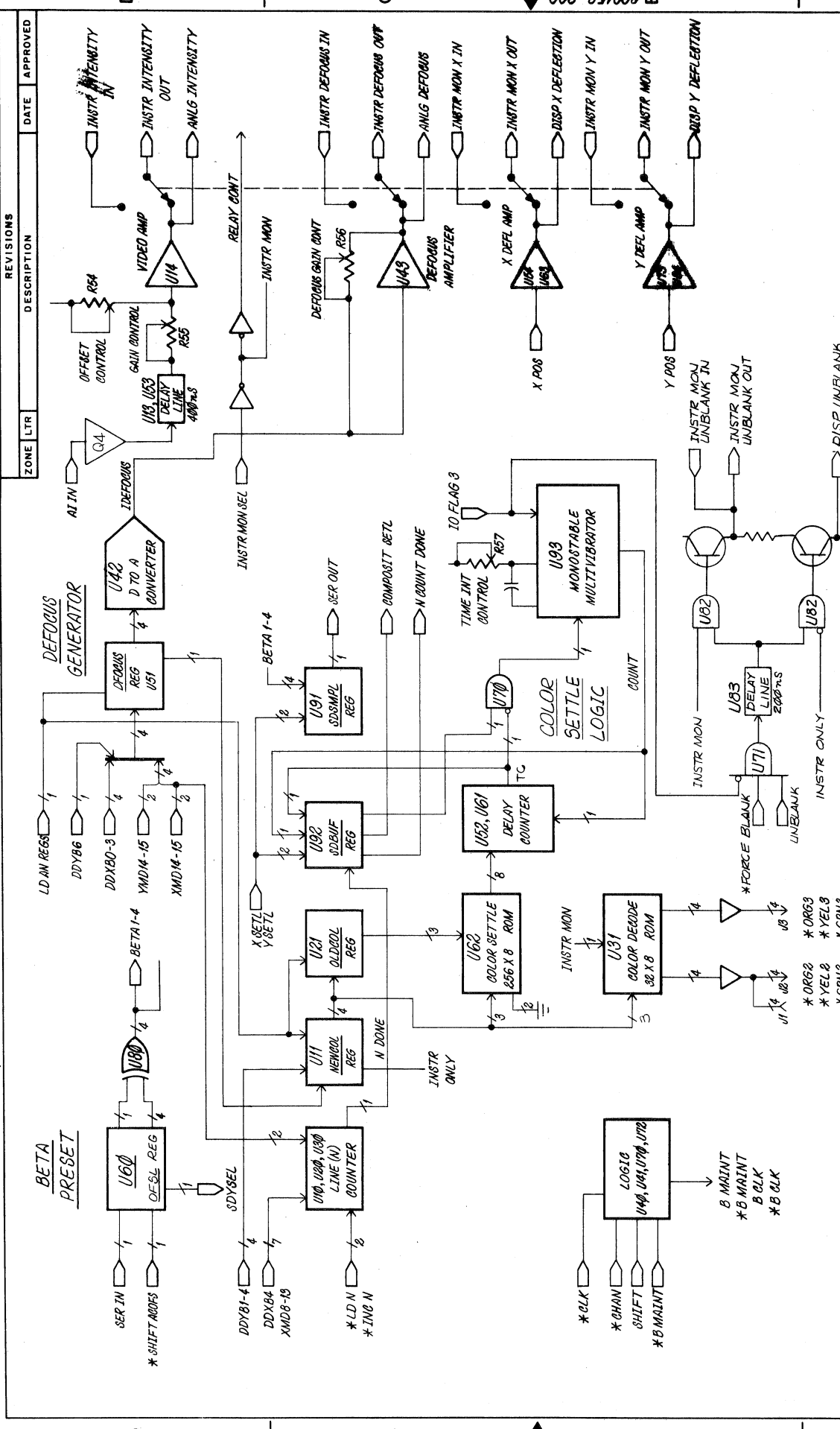
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	
TOLERANCES ON	
XX ±	
.XXX ±	
ANGLES ±	
	✓
MATERIAL	
200130-100	NOV/VIEW
NEXT ASSY	USED ON
APPLICATION	

CONTRACT NO.	DATE
DRAWN D. CARROLL 8-4-77	
CHECKED <i>[Signature]</i> 14-SEP-77	
MECH	
ELEC	
PROJ. ENG. <i>[Signature]</i> 21-SEP-77	
APPROVED	



2 3 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

1 2 3 4



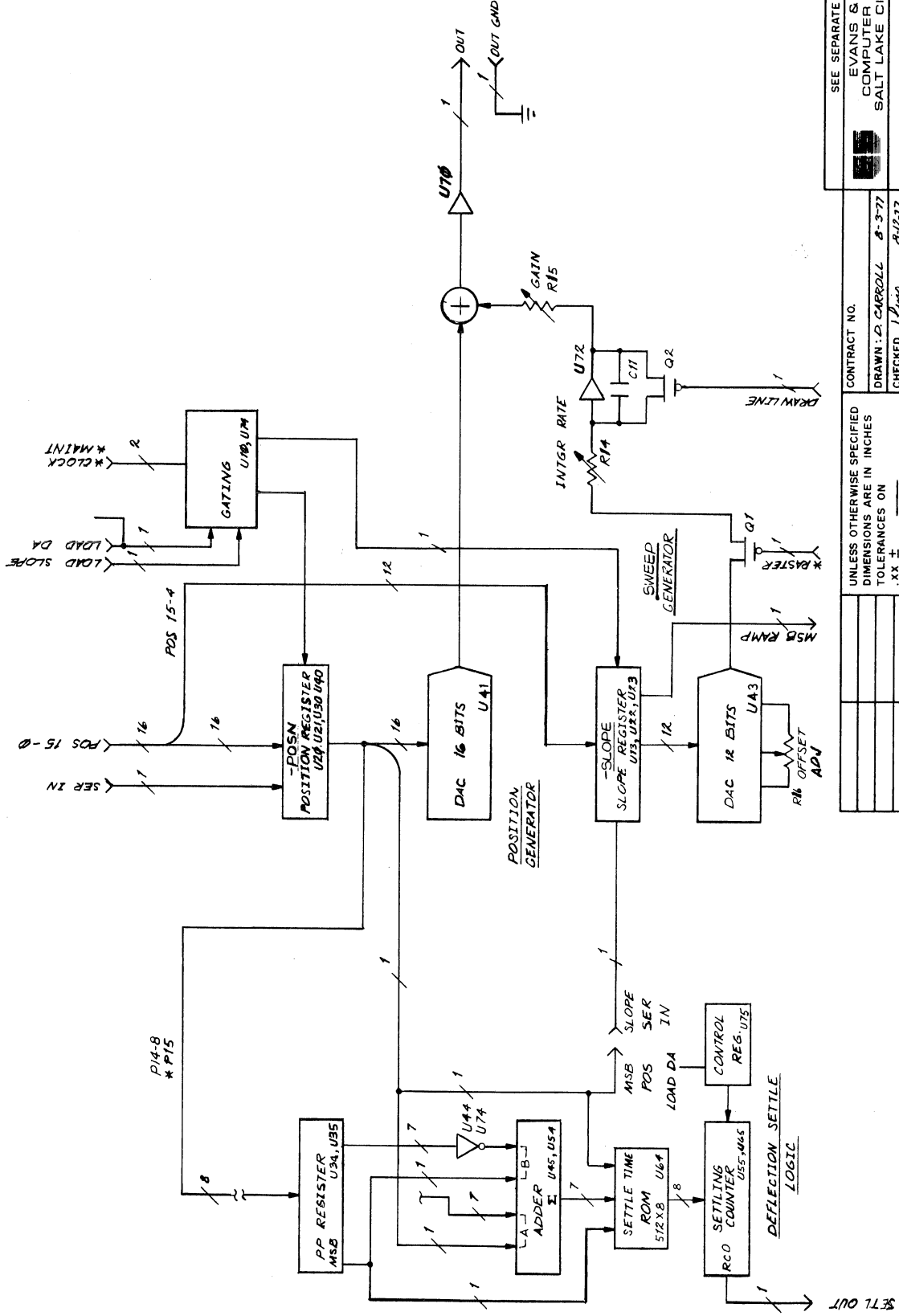
REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	

SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.	
DRAWN	
CHECKED	
MECH	
ELEC	
PROJ. ENG	
APPROVED	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .XXX ± ANGLES ±	
MATERIAL	
FINISH	
APPLICATION	
USED ON	
NEXT ASSY	
SIZE CODE IDENT NO	C 53938
REV	A4
SCALE	NONE
SHEET	OF 1

800158-900 B

D C B A

ZONE		DESCRIPTION		DATE	APPROVED
A2	LTR	REV UPDATE, NO DWG CHNG	8/5	12-27-77	<i>R. S. Evans</i>
A3	LTR	REV UPDATE, NO DWG CHNG	2/8	6-28-78	<i>R. S. Evans</i>



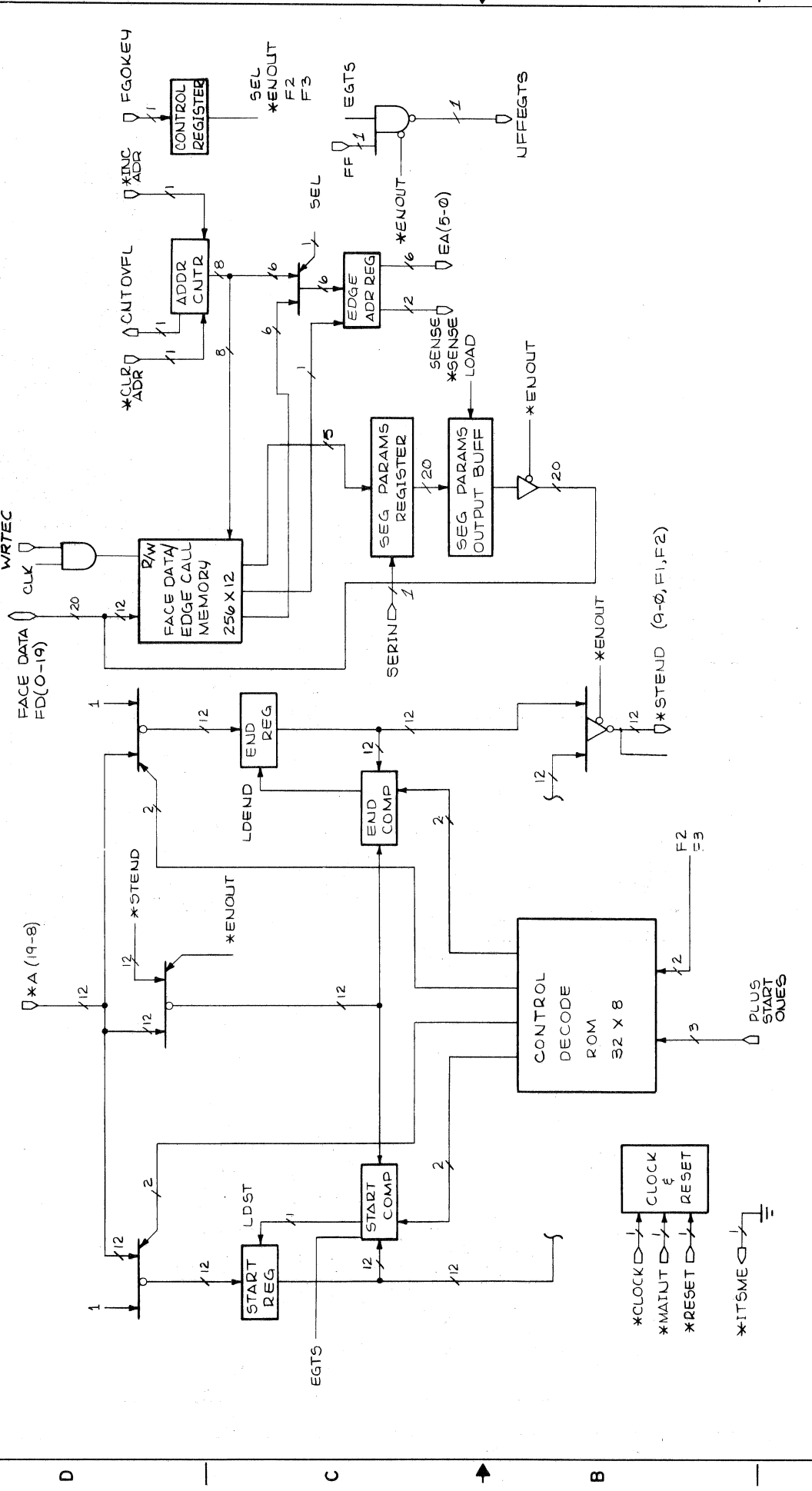
SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO.	DRAWN BY	CHECKED BY	DATE
	D. CARROLL	J. BERRY	8-3-77
			8-12-77
MECH	ELECT	PROJ ENG	APPROVED
			8/12/77
X-Y D/A CONVERTER BLOCK DIAGRAM			
SIZE CODE IDENT NO	REV	SHEET 1 OF 1	
C 53938	200159-900	A3	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON	
.XX ±	ANGLES ±
.XXX ±	✓
MATERIAL	
200159-100 NOVONVIEW	
NEXT ASSY USED ON	APPLICATION
FINISH	

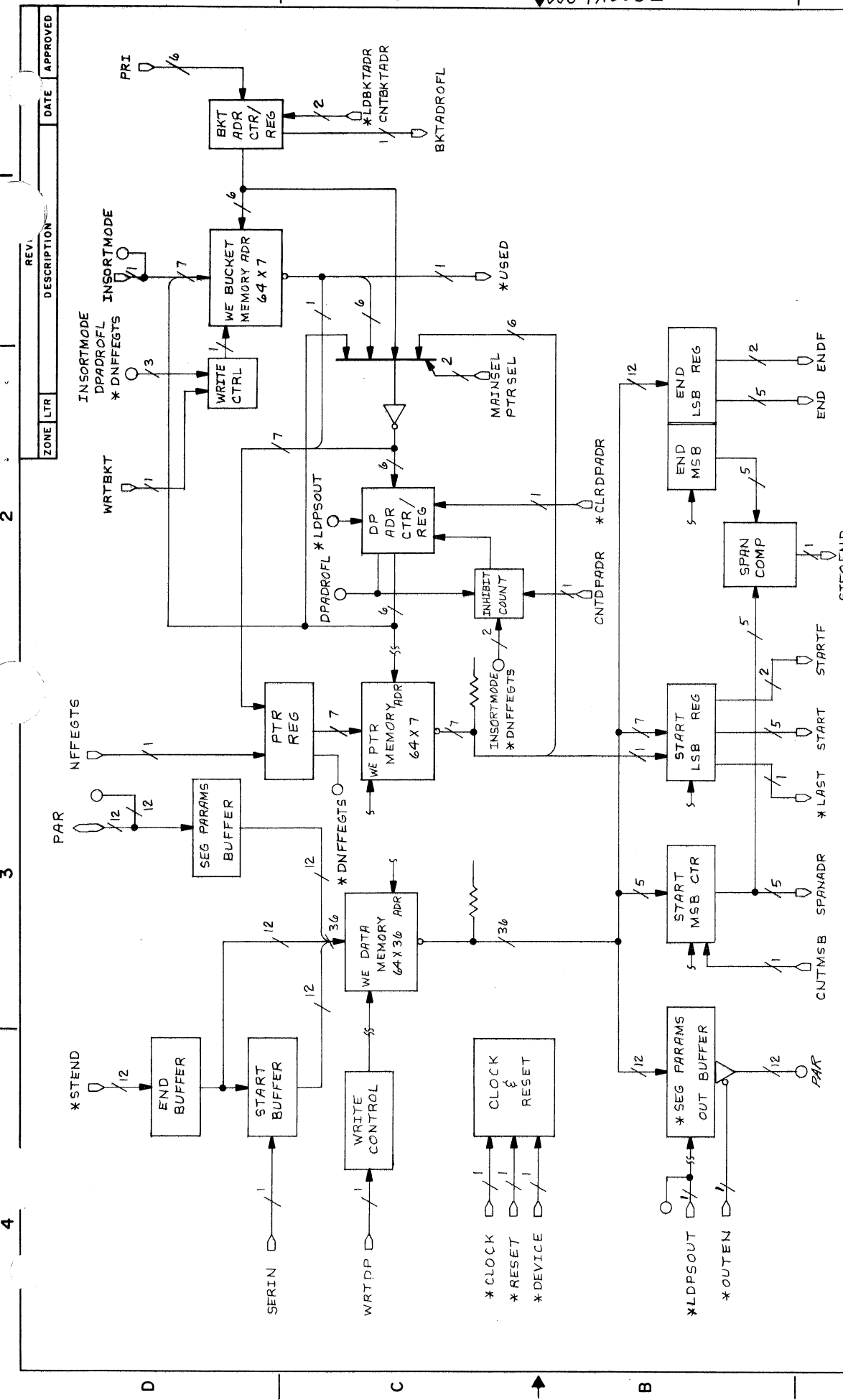
200159-900

B200160-900

REVISIONS		
ZONE	LTR	DESCRIPTION
A1		REVISION UPDATE W/AIO DWG CHNG ADS
		DATE APPROVED
		9-29-77 RSP/RS



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
FACE GENERATOR (BLOCK DIAGRAM)	
CONTRACT NO.	2-2-77
DRAWN R MELLUS	12 APR 77
CHECKED BY	
MECH	
ELEC	
PROJ. ENG.	
APPROVED	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	
TOLERANCES ON	
.XX ±	
.XXX ±	
ANGLES ±	
MATERIAL	
FINISH	
APPLICATION	
NEXT ASSY	
USED ON	
SIZE CODE IDENT NO	C 53938
REV	200160-900
REV	A1
SCALE	1:1
SHEET	OF 1



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
PRIORITY SORTER BLOCK DIAGRAM	
SIZE CODE IDENT NO C 53938	REV NC
SCALE NONE	SHEET 1 OF 1

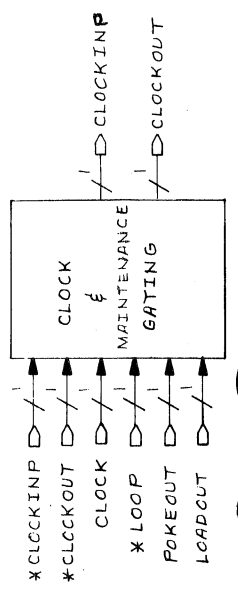
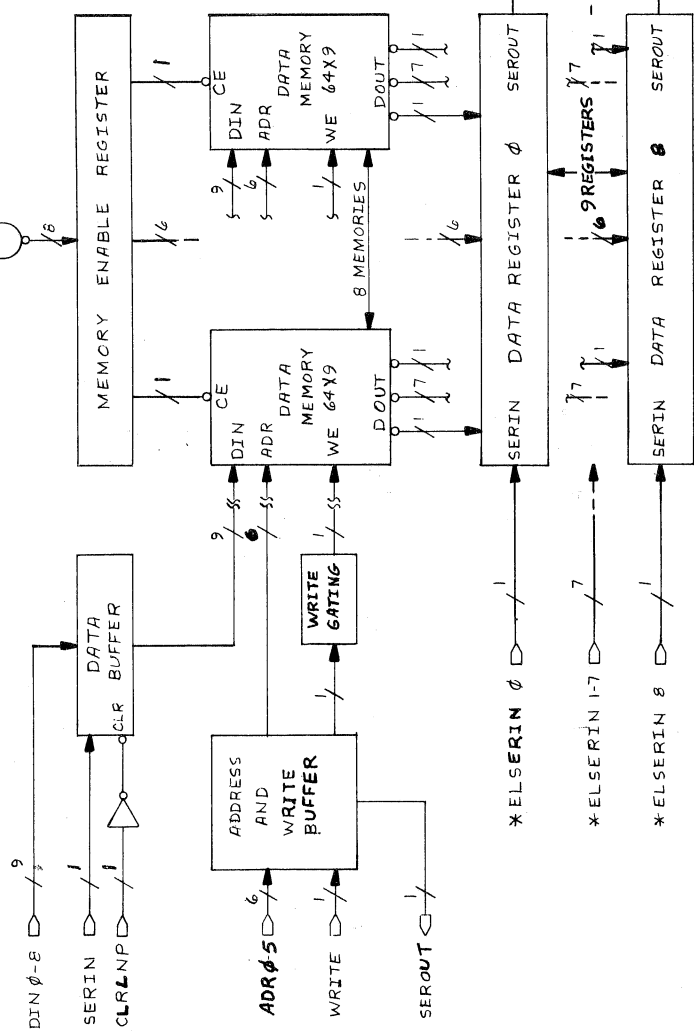
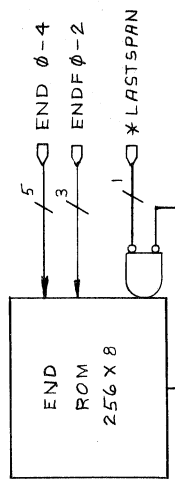
CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON
DRAWN <i>R. Early</i> 8-8-77	.XX ±
CHECKED <i>AT</i> 9 SEP 77	XXX ±
MECH	ANGLES ±
ELEC <i>John Wilson</i> 23 NOV 77	✓
PROJ. ENG. <i>John Wilson</i> 23 NOV 77	
APPROVED	MATERIAL
	FINISH
	USED ON
	APPLICATION
	NEXT ASSY

REVISIONS		
ZONE	LTR	DESCRIPTION
AI		REV UPDATE W/NO DMG CHNG MM
		DATE APPROVED
		4-578
		MT

2

3

4

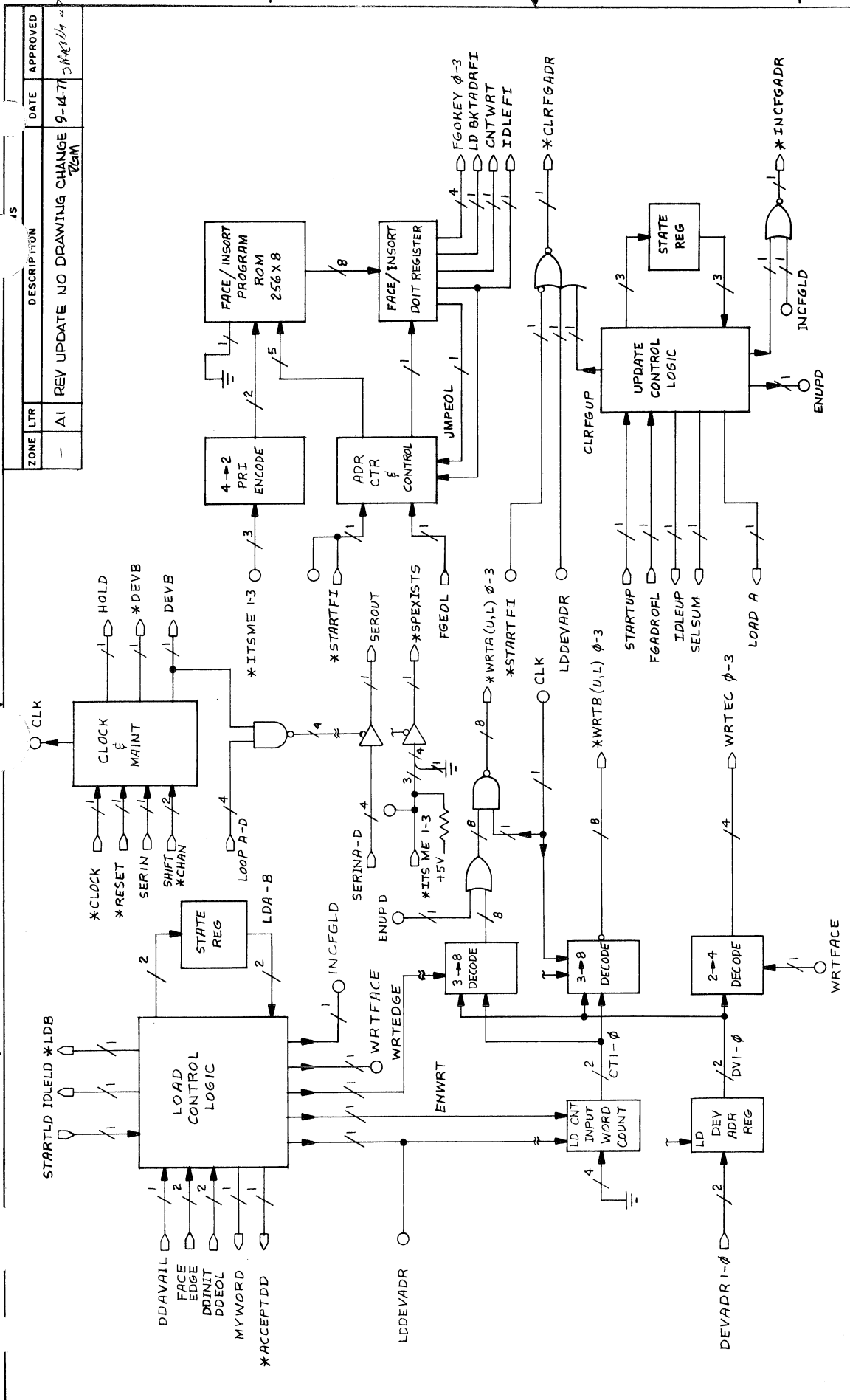


SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
LINE ASSEMBLER BLOCK DIAGRAM	
SIZE CODE IDENT NO	REV
C 53938	200162-900 AI
SCALE NONE	ET 1 OF 1

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON	
.XX ±	MECH
.XXX ±	ELEC
ANGLES ±	PROJ. ENG
MATERIAL	APPROVED
NEXT ASSY USED ON	
FINISH	
APPLICATION	

CONTRACT NO.	8-5-77
DRAWN	R. P. Gentry
CHECKED	Carl R. Gentry
MECH	7 SEP 77
ELEC	John M. 20 SEP 77
PROJ. ENG	

200163-900

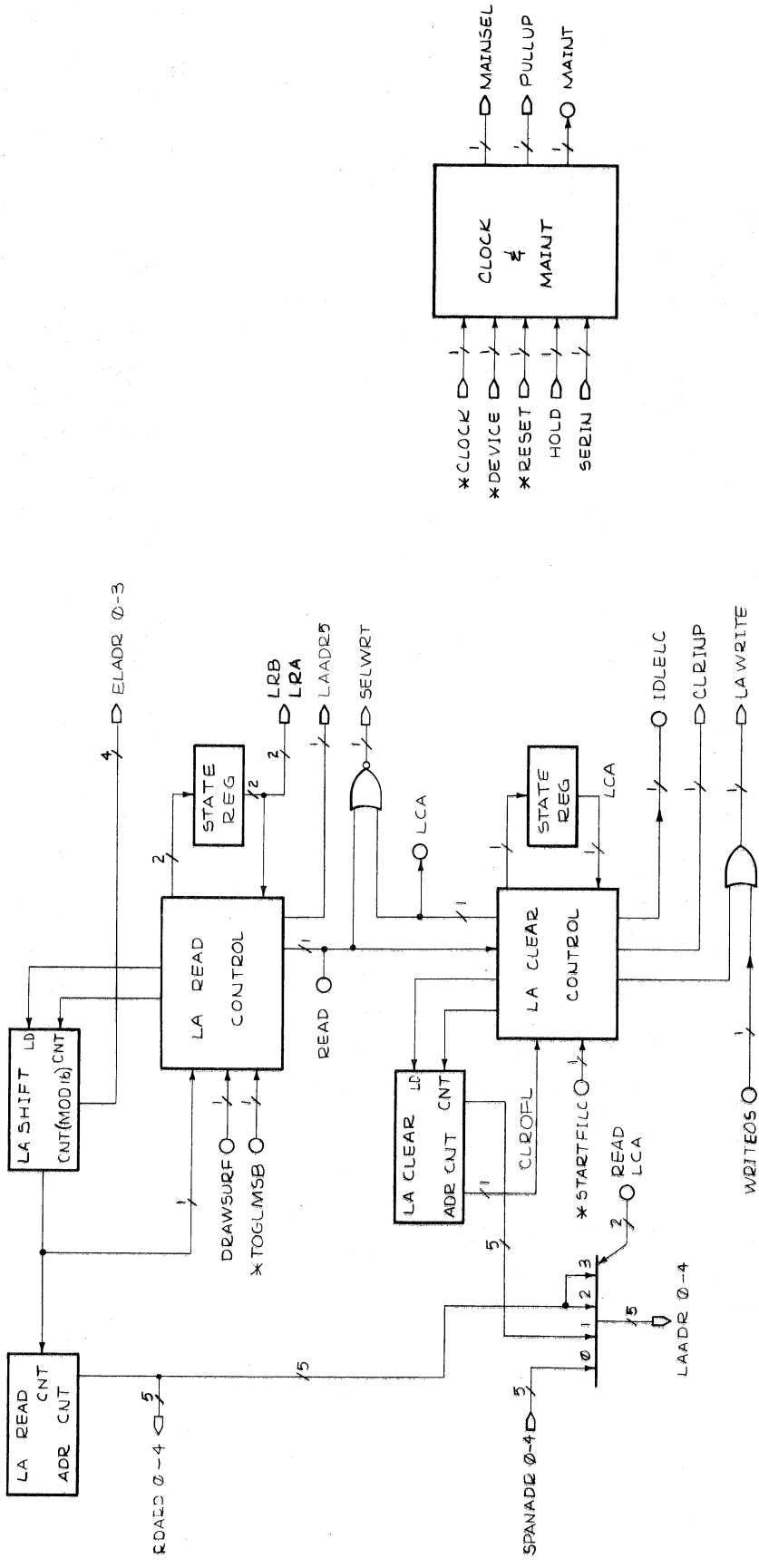


SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
E/F CONTROL (BLOCK DIAGRAM)	
SIZE CODE IDENT NO	REV
C 53938	A1
SCALE NAME	SHEET 1 OF 1

CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON
DRAWN R. EARLEY 1-31-77	.XX ±
CHECKED <i>[Signature]</i> 3 FEB 77	.XXX ±
MECH	ANGLES ±
ELEC <i>[Signature]</i> 17 APR 77	
PROJ. ENG <i>[Signature]</i> 17 APR 77	
APPROVED	MATERIAL
	NOVIEW II
	USED ON
	NEXT ASSY
	APPLICATION

ZONE	DESCRIPTION	DATE	APPROVED
-	AI REV UPDATE NO DRAWING CHANGE	9-14-77	<i>[Signature]</i>

ZONE	LTR	DESCRIPTION	DATE	APPROVED



SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
S/L CONTROL BLOCK DIAGRAM	
CONTRACT NO.	1-19-71
DRAWN	R. MELLUS
CHECKED	3 FEB 77
MECH	
ELEC	14 MAR 77
PROJ. ENG	17 MAR 77
APPROVED	
SIZE	C 53938
CODE IDENT NO.	200164-900
SCALE	NONE
REV	NC
SHEET	OF 2

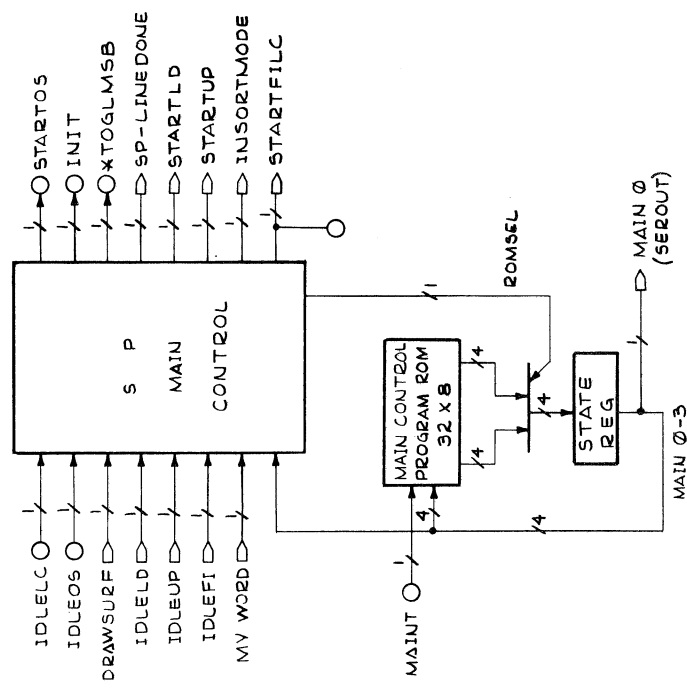
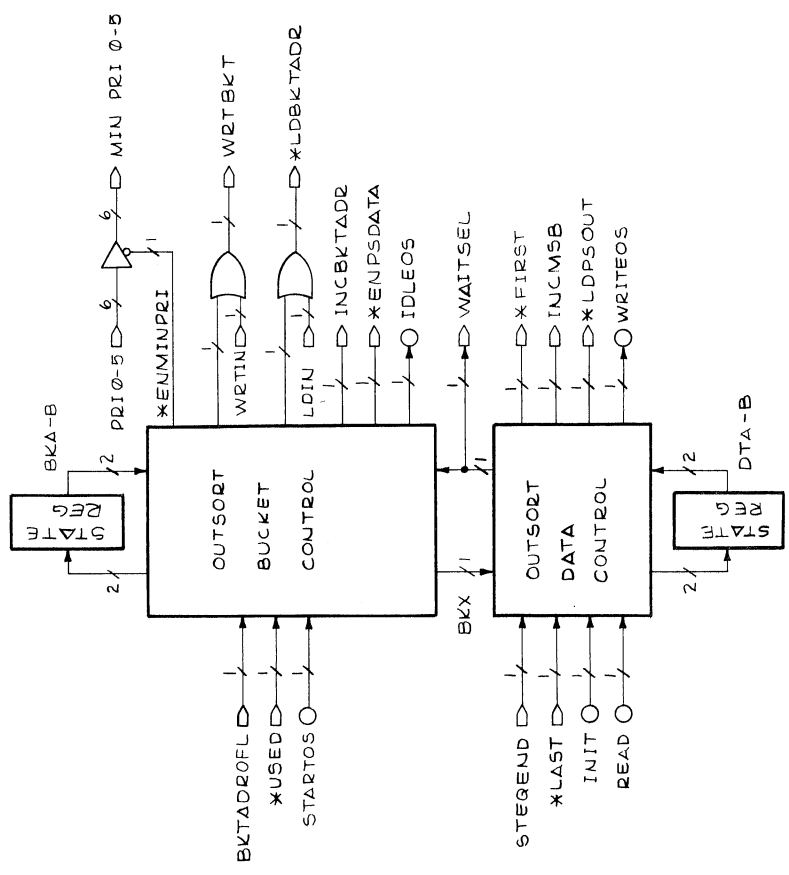
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	TOLERANCES ON	ANGLES ±	MATERIAL
.XX ±			
.XXX ±			
ANGLES ±			
✓			
NEXT ASSY	USED ON	APPLICATION	FINISH

B 200164-900

ZONE	LTR	REV	DESCRIPTION	DATE	APPROVED

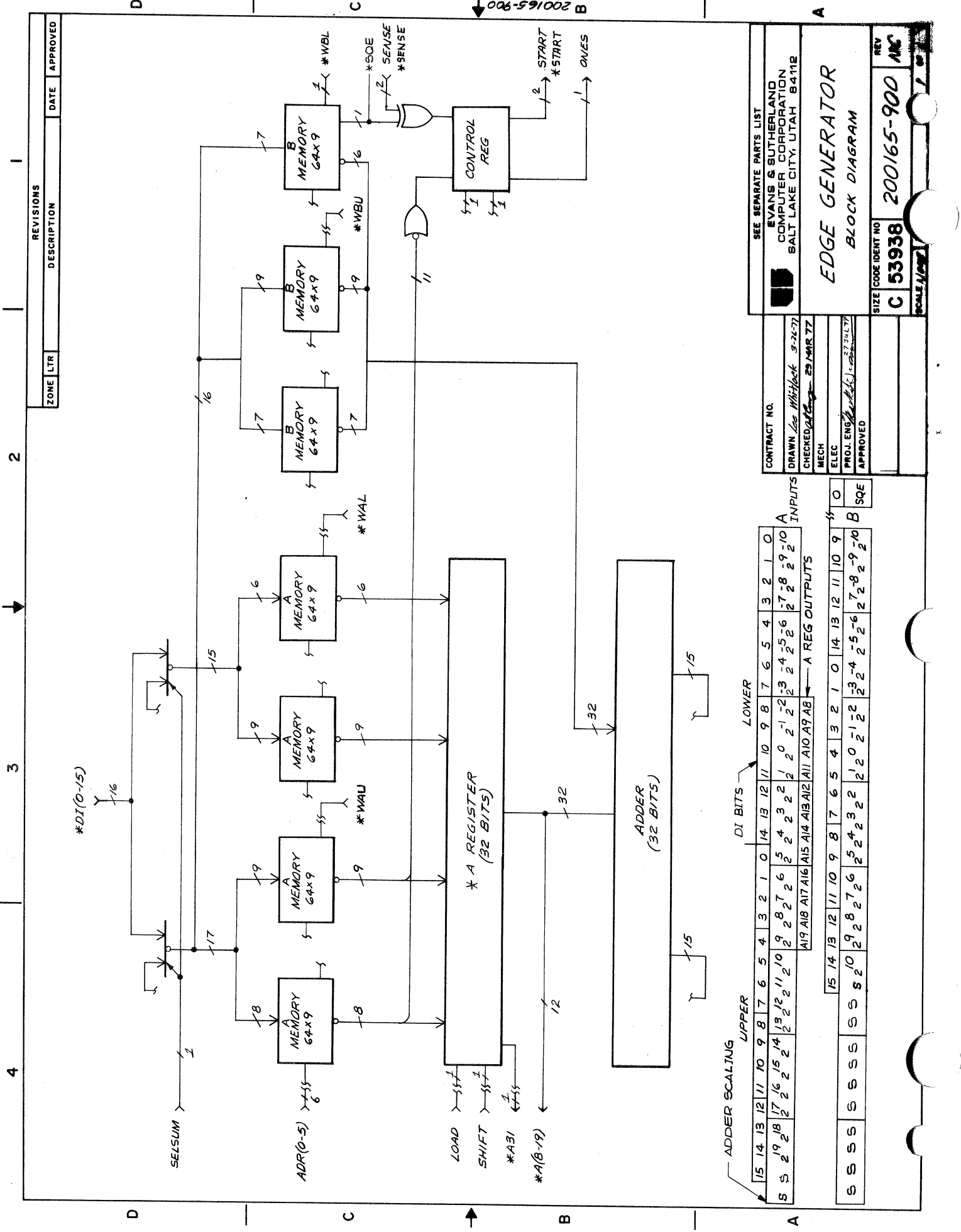
D C B A

D C B A



200164-900

SIZE CODE IDENT NO	REV
C 53938	NC
SCALE NONE	SHEET 2 OF 2



ZONE	LTR	REVISIONS	DESCRIPTION	DATE	APPROVED
4					
3					
2					
1					

SEE SEPARATE PARTS LIST
 EVANS & SUTHERLAND
 COMPUTER CORPORATION
 SALT LAKE CITY, UTAH 84118

CONTRACT NO. _____
 DRAWN *Lee Whitlock* 3-24-77
 CHECKED *[Signature]* 29 MAR 77
 MECH _____
 ELEC _____
 PROJ. ENGR. *[Signature]* 1-27-77
 APPROVED _____

EDGE GENERATOR
 BLOCK DIAGRAM

SIZE CODE IDENT NO. **C 53938** REV **MC**
 SCALE 1/8" = 1"

ADDER SCALING

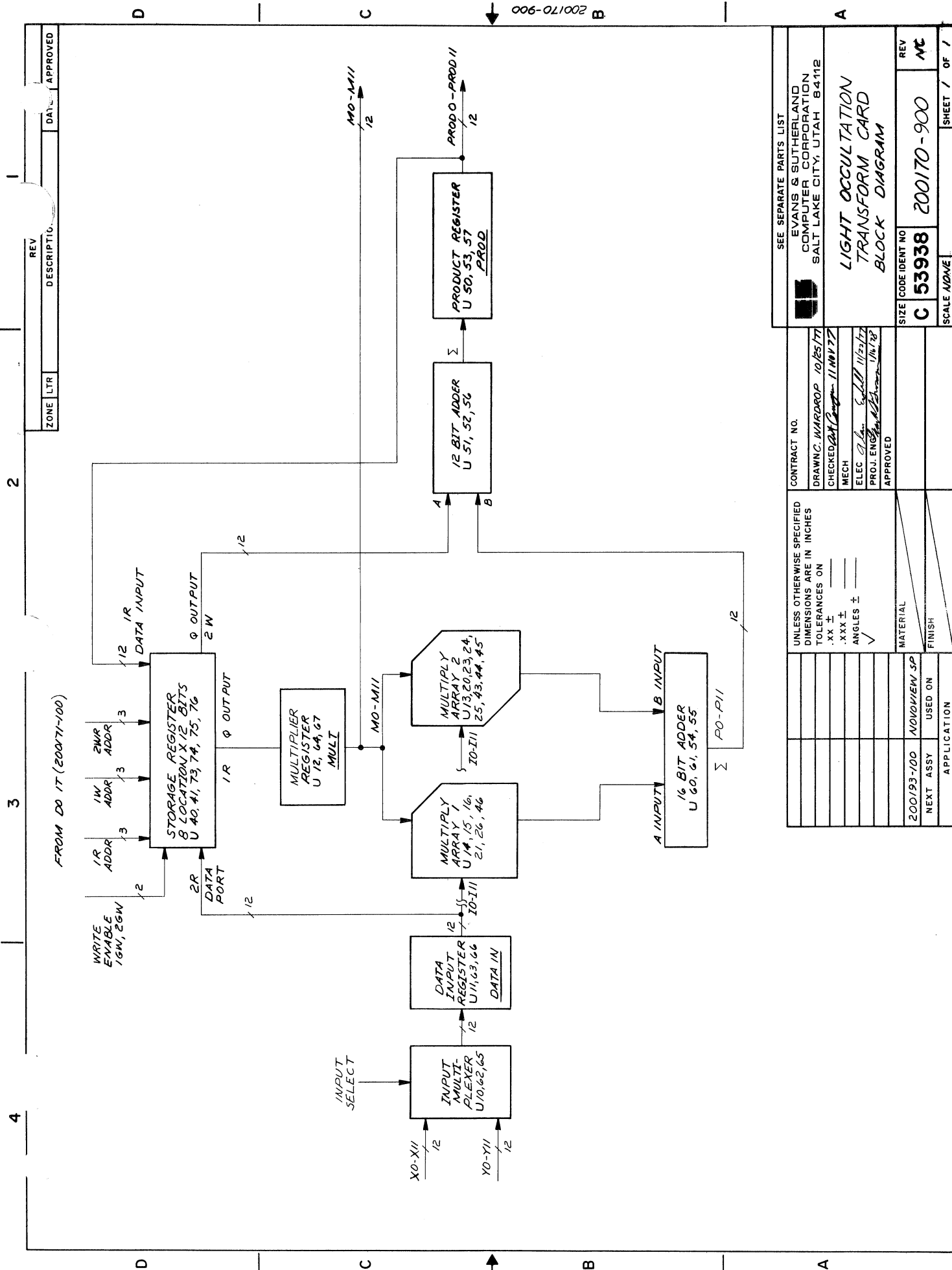
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
S	S	S	S	S	S	S	S	S	S	S	S	S	S	S	S	2	2	2	2	2	2	2	2	2	2	2	2	2	2		
17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	13	12	11	10	9	8	7	6	5	4	3	2	1	0
2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	

DI BITS LOWER

DI BITS UPPER

A REG OUTPUTS

INPUTS A B SQE



200170-900

ZONE	LTR	REV	DATE	APPROVED

FROM DO 17 (200171-100)

WRITE ENABLE 16W, 26W
 1R ADDR 3
 2WR ADDR 3
 1R ADDR 3
 DATA INPUT 12 1R
 STORAGE REGISTER 8 LOCATIONS X 12 BITS U 40, 41, 73, 74, 75, 76
 DATA PORT 2R 12
 1R 12
 2W 12
 MULTIPLIER REGISTER U 12, 64, 67
 MULTIPLY ARRAY 1 U 14, 15, 16, 21, 26, 46
 MULTIPLY ARRAY 2 U 13, 20, 23, 24, 25, 43, 44, 45
 DATA INPUT REGISTER U 11, 63, 66
 DATA IN 12
 INPUT MULTI-PLEXER U 10, 62, 65
 INPUT SELECT 12
 MO-M111 12
 12 BIT ADDER U 51, 52, 56
 A 12
 B 12
 PRODUCT REGISTER U 50, 53, 57
 PROD 12
 MO-M111 12
 16 BIT ADDER U 60, 61, 54, 55
 A INPUT 12
 B INPUT 12
 FO-P111 12

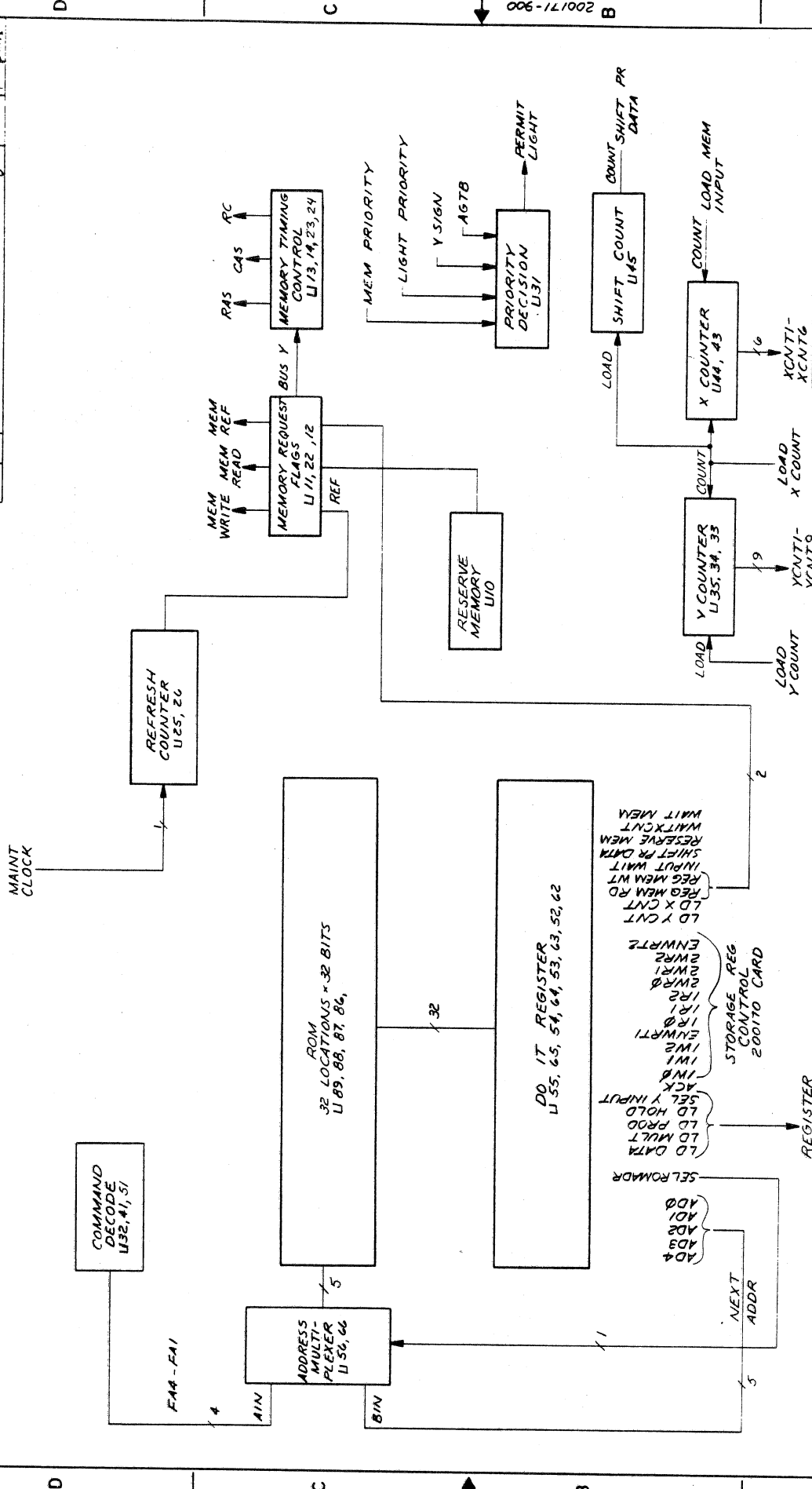
SEE SEPARATE PARTS LIST
 EVANS & SUTHERLAND
 COMPUTER CORPORATION
 SALT LAKE CITY, UTAH 84112

CONTRACT NO.
 DRAWING: WARDROP 10/25/77
 CHECKED: [Signature] 11/11/77
 MECH
 ELEC [Signature] 11/23/77
 PROJ. ENG. [Signature] 11/17/77
 APPROVED

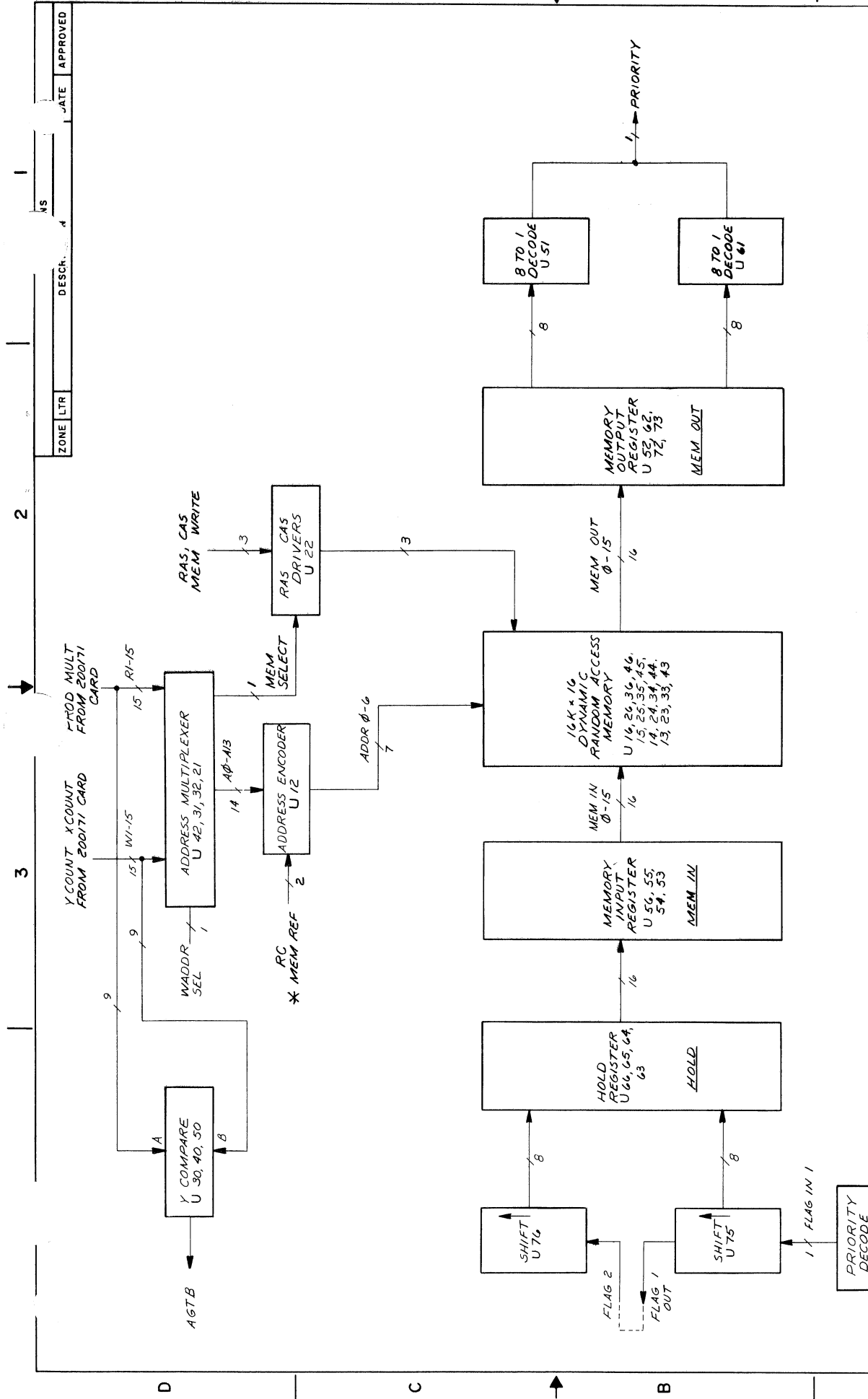
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
 TOLERANCES ON
 .XX ±
 .XXX ±
 ANGLES ±
 MATERIAL
 FINISH

200193-100
 NEXT ASSY USED ON
 APPLICATION
 SIZE CODE IDENT NO
 C 53938
 200170-900
 SCALE NAME
 SHEET / OF /
 REV MC
 LIGHT OCCULTATION TRANSFORM CARD BLOCK DIAGRAM

REVISIONS			DATE	APPROVED
ZONE	LTR	DESCRIPTION		
A2		REV UPDATE W/NO DWG CHG	11/16/78	[Signature]
A3		REV UPDATE W/NO DWG CHG	3-14-78	[Signature]



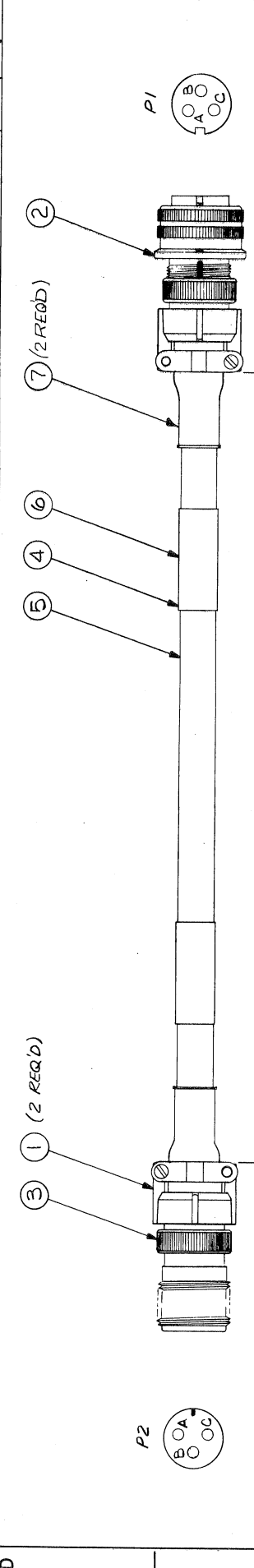
SEE SEPARATE PARTS LIST		CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	MATERIAL	APPLICATION
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		CW	XX ±	200193-100	NO/NO/VIEW SP
LIGHT OCCULTATION CONTROL BLOCK DIAGRAM		MECH	.XXX ±	USED ON	
BLOCK DIAGRAM		ELEC	ANGLES ±	FINISH	
BLOCK DIAGRAM		APPROVED			
SIZE CODE IDENT NO	REV	CHECKED			
C 53938	A3	22 MAY 77			
200171-900		200171-900			
SCALE/NAME	1 OF 1				



SEE SEPARATE PARTS LIST		CONTRACT NO.	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		DRAWN C. WARDROP 10/21/77	
LIGHT OCCULTATION MAP CARD BLOCK DIAGRAM		CHECKED BY <i>[Signature]</i> 11 NOV 77	
REV	NC	MECH	
SIZE CODE IDENT NO	C 53938	ELEC	
SCALE NONE	200172-900	PROJ. ENGINEER	
SHEET 1 OF 1		APPROVED	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			
TOLERANCES ON			
.XX ±			
.XXX ±			
ANGLES ±			
MATERIAL			
FINISH			
APPLICATION		MATERIAL	
200193-100		MOVIEVIEW SP	
NEXT ASSY		USED ON	

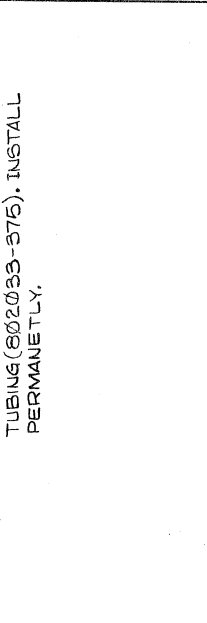
4 3 2 1

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
-	NC	ITEM 5 WAS 802081-016. THIS DRAWING IS FORMALLY RELEASED TO NC REV. STATUS. <i>PS</i>	8-12-77	<i>PS</i>
A1		ADDED ITEM 7 802174-008 2 <i>PS</i> CHANGE SCHEMATIC Hess, 10-5-78	10-2-78	<i>PS</i>



CABLE LENGTH SEE TABULATION TABLE

INSTALL KALOGRAPH TAPE ITEM 6, (802311-004) MARKED WITH PART NUMBER, REV. LEVEL AND SERIAL NUMBER. COVER WITH ITEM 4 CLEAR SHRINK TUBING (802033-375). INSTALL PERMANENTLY.



TABULATION TABLE

CABLE ASSY	CABLE LENGTH
200178 - 024	24FT. ±.50 IN.

SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTRACT NO.	
XX ±		DRAWN <i>PS</i> 8-1-77	
.XXX ±		CHECKED <i>PS</i> 2 AUG 77	
ANGLES ±		MECH <i>PS</i> 8/15/77	
✓		ELEC <i>PS</i> 15 AUG 77	
MATERIAL	SEE PARTS LIST	PROJ. ENG	
200104-100 NOVOVUEW		APPROVED <i>PS</i> 8-15-77	
NEXT ASSY	USED ON	SIZE CODE IDENT NO	
		C 53938	
APPLICATION	FINISH	SCALE	
		NOMIE	
		SHEET	
		OF 1	
CABLE ASSY, POWER EXTENSION		REV	
		A1	

B 200178-TAB

D

C

A

D

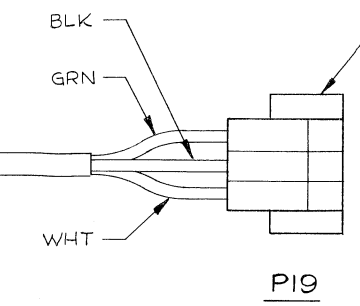
C

A

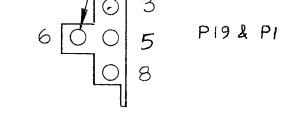
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
-	A2	ITEM NO. & NOTE CHANGES	9/11/77	J.Pino 9/11/77
	A3	ADDED AIR BAFFLES M/W	10-26-77	<i>[Signature]</i> 1 Nov 77



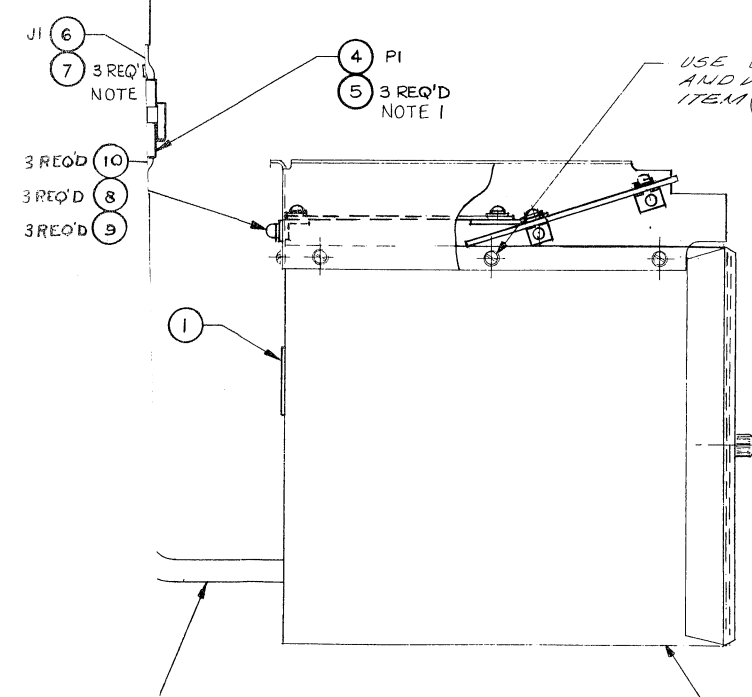
SCHMATIC
J1/PI/P19



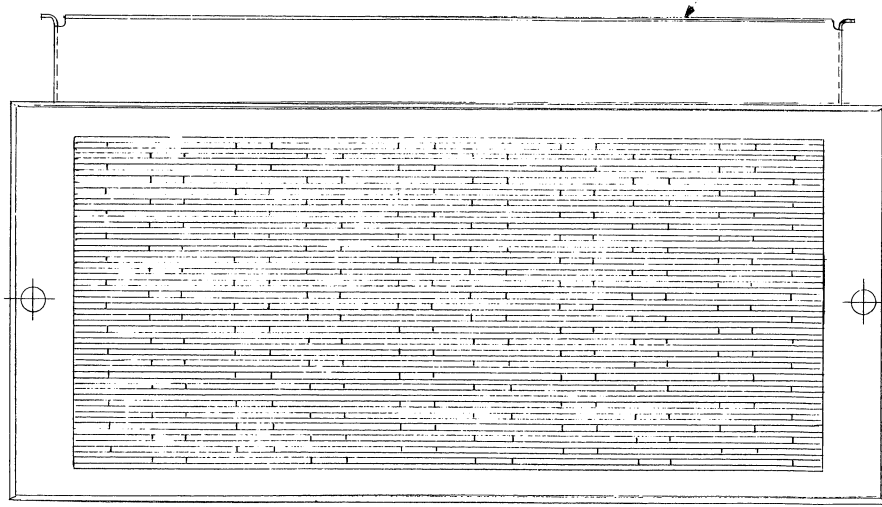
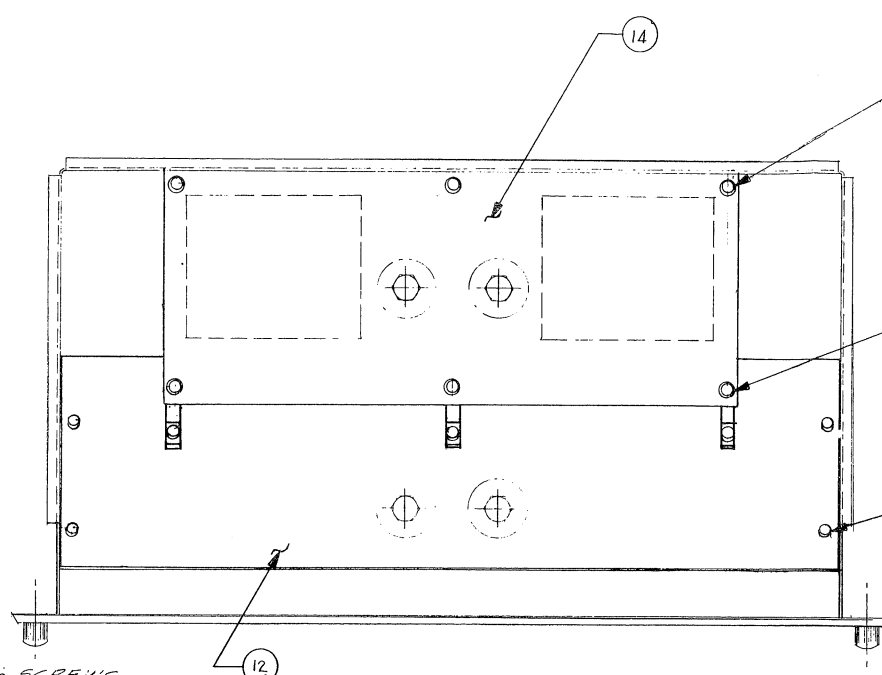
PI9



PI9 & PI



USE EXISTING LENGTH OF CABLE.



USE EXISTING SCREWS
AND WASHERS TO ASSY
ITEM (2) TO ITEM (3)

- (11) 3 REQ'D
- (10) 3 REQ'D
- (8) 3 REQ'D
- (9) 3 REQ'D
- (13) 3 REQ'D
- (10) 6 REQ'D
- (8) 6 REQ'D
- (9) 6 REQ'D
- (11) 4 REQ'D
- (10) 8 REQ'D
- (8) 8 REQ'D
- (9) 8 REQ'D

2. CUT FORD 6 IN. FROM BLOWER. INSTALL CONNECTORS ACCO SCHAEMTICS FOR PI AND J1.
1. DO INSTALL ITEM (5) & (7) INTO ITEM (4) & (6) AT CN 5.
- UNLESS OTHERWISE SPECIFIED:
- NOTES:

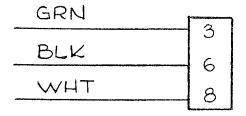
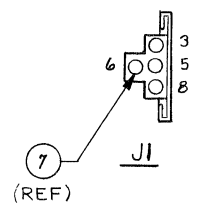
UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .50 IN. .XXX ± — ANGLES ± — ✓ MATERIAL SEE PARTS LIST FINISH		CONTRACT NO.	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112
200101-100	NOVOVIEW	DRAWN <i>[Signature]</i> 6/17/77	TITLE ASSY, BLOWER & SHROUD (NOVOVIEW SP)
NEXT ASSY	USED ON	CHECKED <i>[Signature]</i> 8/15/77	REV A3
APPLICATION		MECH <i>[Signature]</i> 8/15/77	DWG 200181-100
		ELEC <i>[Signature]</i> 8/15/77	SCALE 1/2
		PROJ. ENG.	SHEET 1 OF 1
		DESIG. AUTO.	
		APPROVED <i>[Signature]</i> 8-15-77	

SEE SEPARATE PARTS LIST

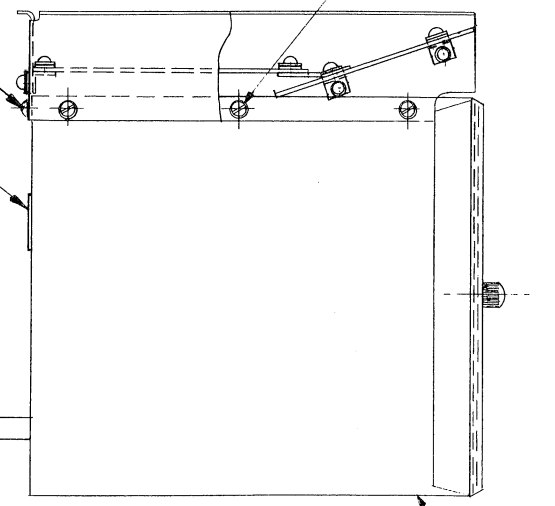
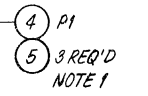
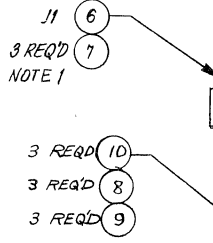
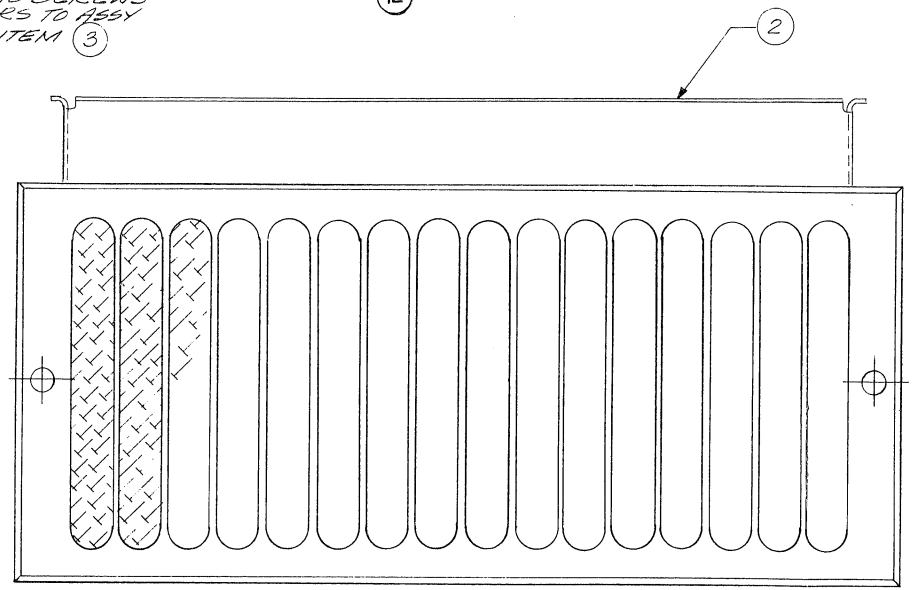
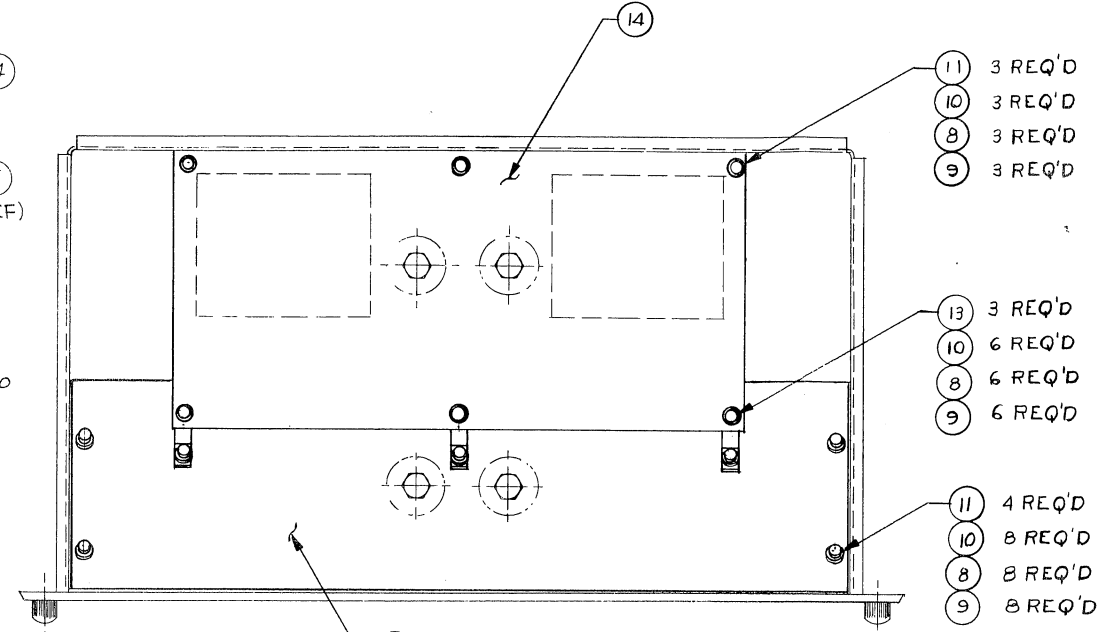
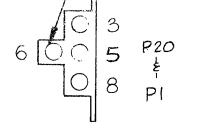
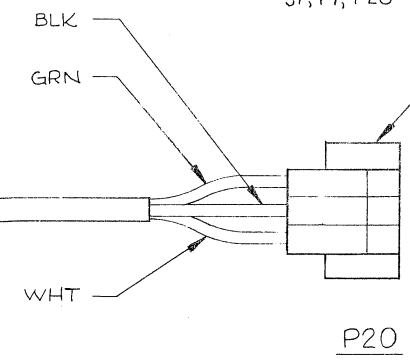
200181-100



REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A1	ITEM NO. AND DRAWING CHANGES	9-9-77	J.Pano 9/11/77
	A2	ADDED AIR BAFFLES MW	10-26-77	ll Alford 11/1/77



SCHMATIC
J1, P1, P20



USE EXISTING SCREWS AND WASHERS TO ASSY ITEM (2) TO ITEM (3)

USE EXISTING CABLE LENGTH.

2. CUT EXISTING CORD 6 IN. FROM BLOWER. INSTALL CONNECTORS ACCORDING TO SCHEMATICS FOR P1 AND J1.
 1. DO NOT INSTALL ITEM (5) & (7) INTO ITEM (4) & (6) AT LOCATION 5.
 UNLESS OTHERWISE SPECIFIED:
 NOTES:

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .50 IN. .XXX ± ANGLER ± ✓ MATERIAL SEE PARTS LIST FINISH		CONTRACT NO. DRAWN CHECKED MECH ELEC PROJ. ENG. DESIG. AUTO. APPROVED	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112 TITLE ASSY, BLOWER & SHROUD (NOVOVIEW 5P) D DWG 200181-101 REV A2 SCALE 1/2 SHEET 1 OF 1
200102-100 NEXT ASSY	NOVOVIEW USED ON	J.Pano 8-15-77	

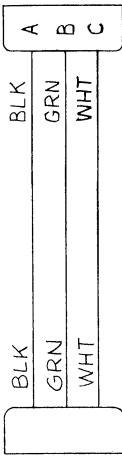
200181-101



2

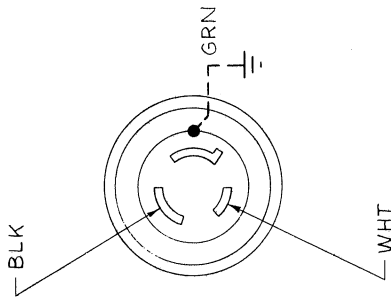
JN LEVEL FOR DASH NO.:

ZONE	-010	-025	-050	-075	-100	-125	-150	ZONE	DESCR.	DWG. RELEASE	DATE	APPROVED
									NC			
									AI		9-9-77	RS
									A2	REDRAWN, ADDED ITEM 6, NOTE 1 & ITEM NOS. 4, 5, 6	2-17-78	RS
									NC	DWG. RELEASE	7-6-79	RS
									RCI	NOTE 1 WAS MARK INFO ON ITEM 7 (ETC). COVER WITH ITEM 6 & REMOVED BALLOON 4.	7-6-79	RS



WIRING DIAGRAM

3 (REF)



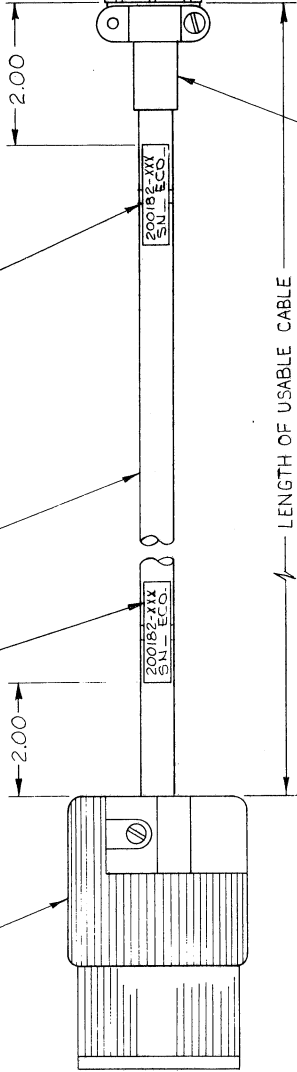
SEE TABLE I FOR APPROPRIATE DASH NO. (2 PLCS)

2 REQD NOTE 1

6

5

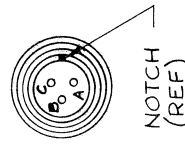
3



LENGTH OF USABLE CABLE SEE TABLE I

TABLE I

DRAWING DASH NO.	USABLE LENGTH OF CABLE
200182-010	10 FEET
200182-025	25 FEET
200182-050	50 FEET
200182-075	75 FEET
200182-100	100 FEET
200182-125	125 FEET
200182-150	150 FEET



3

2

200182-TAB

D

C

B

A

1. MARK APPROPRIATE PART NO., ECO LEVEL & S/N ON ITEM 6 USING OPAQUE INDUSTRIAL INK, PHILLIPS PROCESS CO. NO. 35A OR EQUIV.

NOTES:

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .05 .XXX ± ANGLES ±

MATERIAL SEE PARTS LIST

FINISH

USED ON

APPLICATION

CONTRACT NO.	DRAWN J. BAHLMANN 2-17-78
CHECKED	15-MAR-78
MECH	
ELEC	3-MAY-78
PROJ. ENG.	3-16-78
APPROVED	18 JUL 79

EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112

CABLE ASSEMBLY, POWER, FOR DSI-1910 DISPLAY

SIZE CODE IDENT NO C 53938

200182-TAB

DO NOT SCALE SHEET 1 OF 1

REV SEE REV BLOCK

LTR		ZONE		REVISIONS		DATE	APPROVED
-150	-125	-120	-075	-050			
A1	A1	A2	A1	A1	ADD CLAMPS & HARDWARE	11-9-77	<i>9/11/77</i>
A1	A1	A3	A1	A1	REVERSED CONNECTOR PINS P-5, P-6	12-15-77	<i>12/15/77</i>
					ADDED ITEMS 18, 19, & 20	12-5-78	<i>12-5-78</i>
				S.C.			

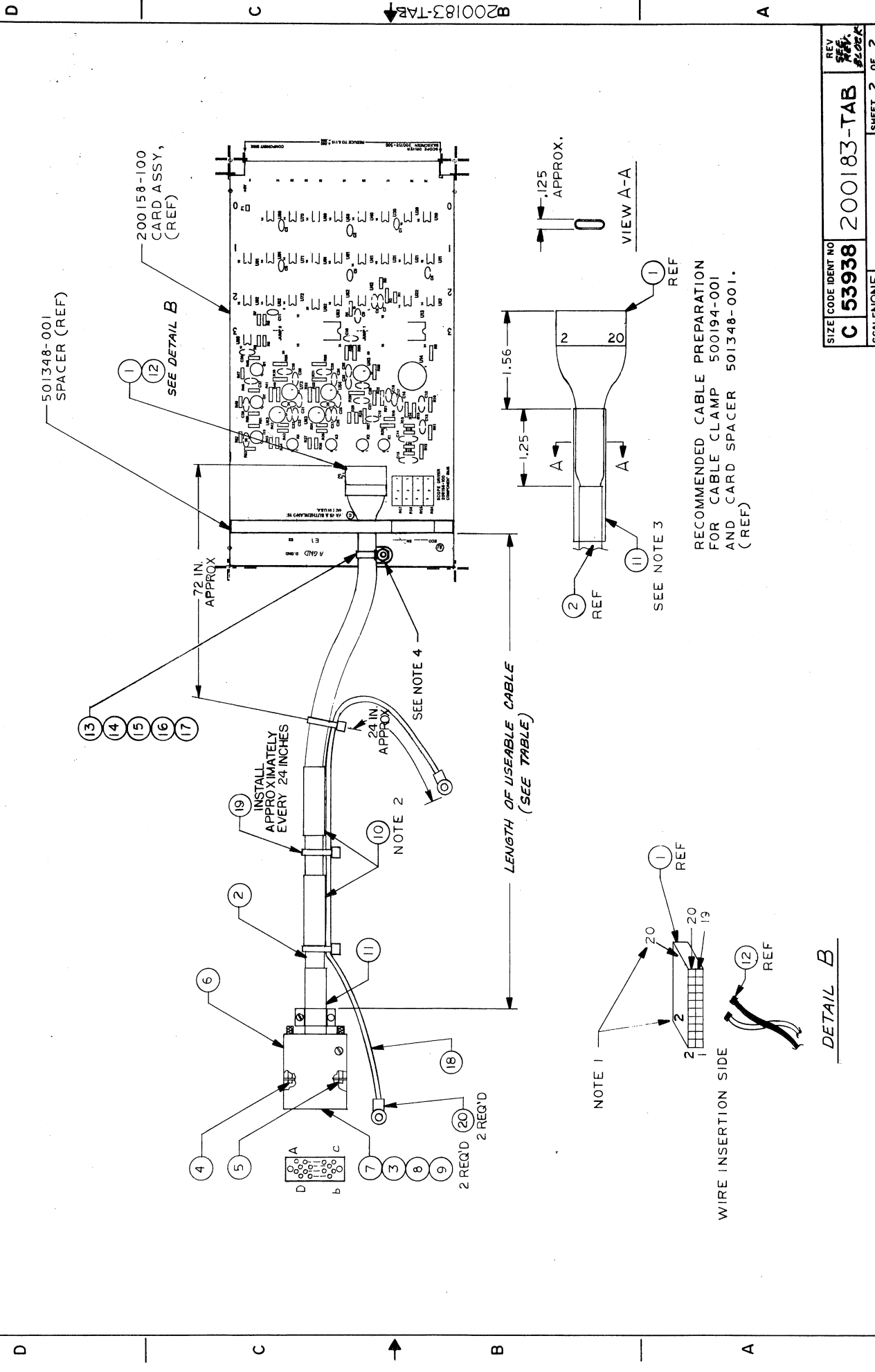
SIGNAL NAME	NO.1 CONNECTOR P/N NUMBER	WIRE COLOR OR CODE	P3 CONNECTOR PIN NUMBER SIGNAL GROUND
SPARE UNBLANK VIDEO ANA DEFOCUS	V S N T	COAX COAX COAX	11 12 7 6
XDEFL YDEFL	P R	COAX COAX	4 2
DIGITAL DEFOCUS GND	a b	RED BLK	9 10
ORN GND	-C D	ORN BLK	13 14
YEL GND	E F	YEL BLK	15 16
GRN GND	Y Z	GRN BLK	17 18
HORIZON GND	X W	BLU BLK	19 20

CABLE NO.	CABLE LENGTH
200183-050	50 FT 0 IN
200183-100	100 FT 0 IN
200183-125	125 FT 0 IN
200183-150	150 FT 0 IN
200183-075	75 FT 0 IN

- DO NOT TIGHTEN CLAMP AROUND THE CABLE AT THIS LEVEL OF ASSEMBLY. INSTALL HARDWARE AND ATTACH LOOSELY AROUND CABLE. CLAMP SHOULD BE FIRMLY ATTACHED TO 200158-100 CARD ONLY AT FINAL SYSTEM ASSEMBLY.
- FOLD BACK UNUSED COAX & WIRES, ADD SHRINK TUBING ITEM NO.10.
- INSTALL TWO PIECES OF HEAT SHRINK TUBING ON CABLE AS SHOWN, BUT DO NOT SHRINK AT TIME OF CABLE ASSEMBLY. AFTER FINAL FACTORY ACCEPTANCE OF SYSTEM, LABELS WITH PART NO. AND SERIAL NO. OF MATING ASSEMBLY SHALL BE INSERTED UNDER TUBING AND THE TUBING SHALL BE SHRUNK AT THAT TIME. USE 802311-004 TAPE PHOTO MARKING AND LABEL AS SHOWN.
- MARK PIN NO'S 2 AND 20 ON TOP OF THE CONNECTOR AS SHOWN, USING WHITE EPOXY INK.

NOTES:

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTRACT NO.	
TOLERANCES ON		DRAWN <i>R. Early</i> 7-1-77	
.XX ± .02		CHECKED <i>OK</i> 7-7-77	
.XXX ±		MECH <i>H. H. H.</i> 7/20/77	
ANGLES ±		ELEC <i>H. H. H.</i> 7/20/77	
✓		PROJ. ENG. <i>H. H. H.</i> 7-12-77	
		APPROVED <i>H. H. H.</i> 7/20/77	
MATERIAL SEE PARTS LIST		SEE SEPARATE PARTS LIST	
FINISH		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
200158-100 NOVO SP		CABLE ASSY, SIGNAL, FOR DSI-1910 DISPLAY,	
NEXT ASSY USED ON		SIZE CODE IDENT NO	
APPLICATION		C 53938 200183 -TAB	
		REV. <i>REV.</i> BLOCK	
		PAGE 1 OF 2	



REV. 1
REV. 2
REV. 3
REV. 4

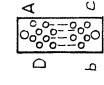
SIZE CODE IDENT NO **C 53938**

SCALE NONE

200183-TAB

SHEET 2 OF 2

RECOMMENDED CABLE PREPARATION FOR CABLE CLAMP 500194-001 AND CARD SPACER 501348-001. (REF)



2 REQ'D

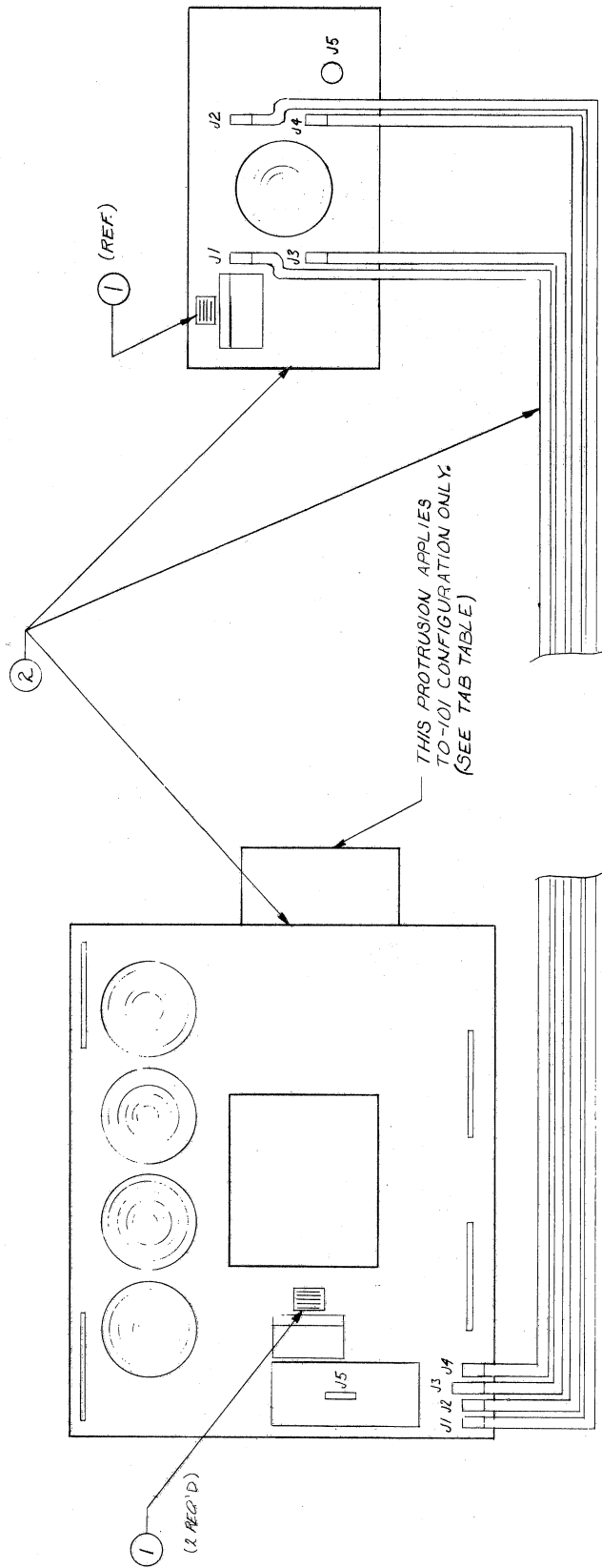
2 REQ'D

DETAIL B

ZONE	LTR	DESCRIPTION	DATE	APPROVED

TABULATION TABLE

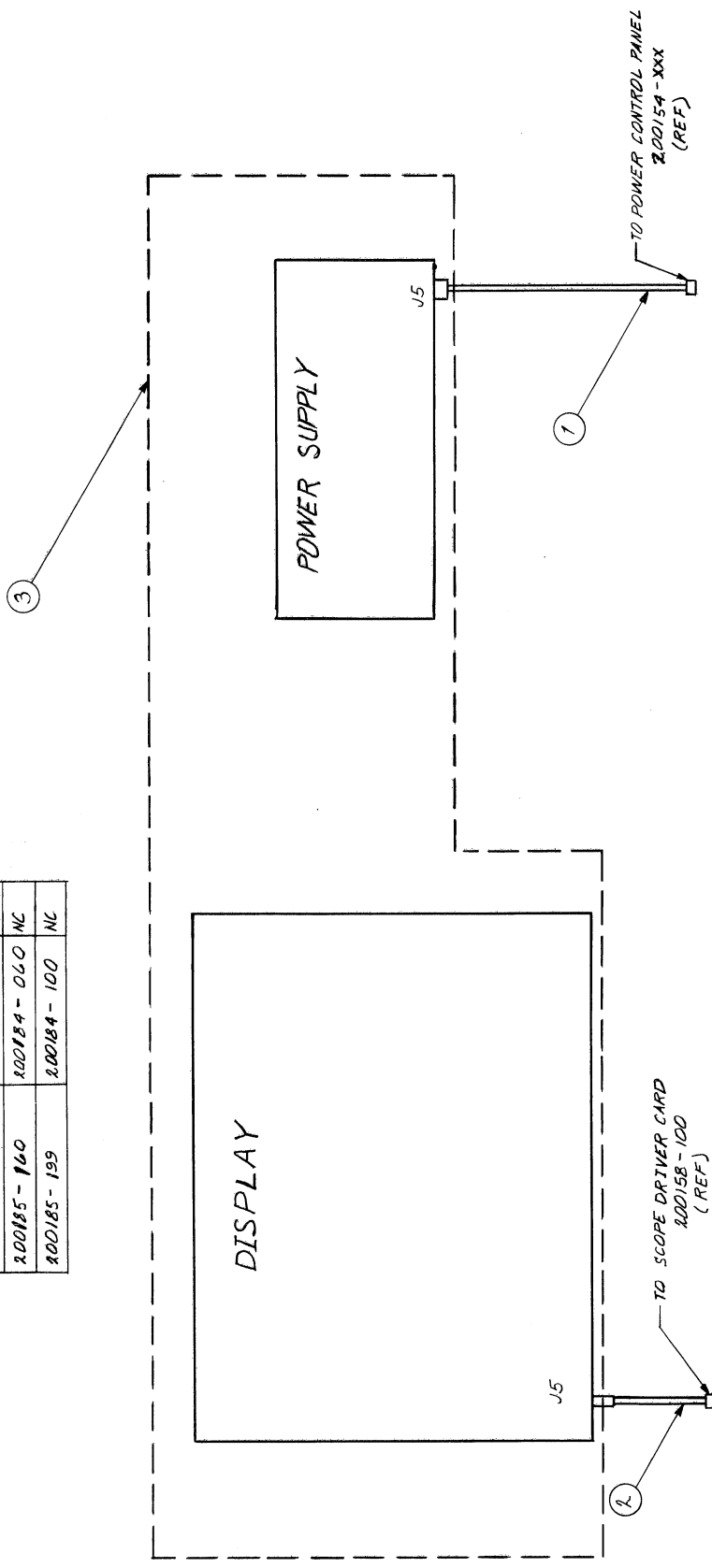
ASSY. NO.	ITEM 2	REV. LEVEL	INTERCONNECT CABLE LENGTH (FT)	VENDOR MODEL NO.	APPROVED
200184-012	801786-001	A1	12	DSI-1910	R.S. Davis 30 MAY 79
200184-030	801786-002	A1	30	DSI-1910	R.S. Davis 30 MAY 79
200184-060	801786-003	A1	60	DSI-1910	R.S. Davis 30 MAY 79
200184-100	801786-004	A1	100	DSI-1910	R.S. Davis 30 MAY 79
200184-101	801786-007	NC	100	DSI-1930	R.S. Davis 30 MAY 79
200184-150	801786-005	NC	150	DSI-1910	R.S. Davis 30 MAY 79



CONTRACT NO. DRAWN BY CARROLL B-1877 CHECKED BY [Signature] 23 AUG 77 MECH [Signature] ELEC [Signature] 01 SEP 77 PROJ. ENG [Signature] APPROVED [Signature] 15 JAN 78		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON XX ± .XXX ± ANGLES ± ✓		ASSY, DISPLAY, WITH INTERCONNECT CABLES	
MATERIAL SEE PARTS LIST		SIZE CODE IDENT NO C 53938	
NEXT ASSY USED ON APPLICATION		REV SEE TABLE	
FINISH		200184-TAB	
NOVDVIEW		SCALE SET 1 OF 1	

TABULATION TABLE

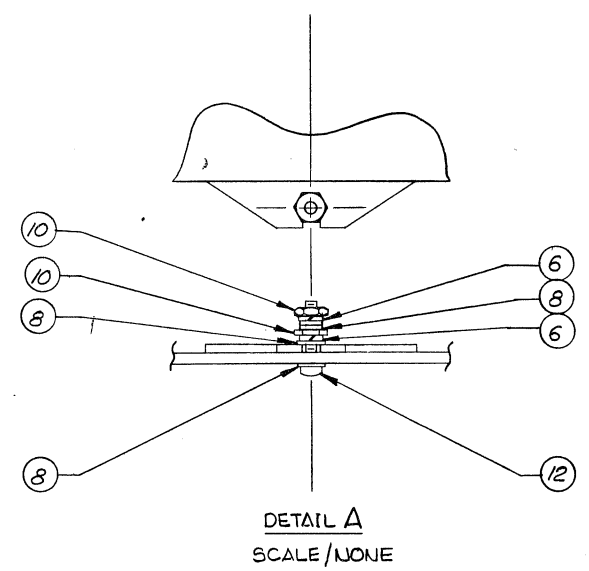
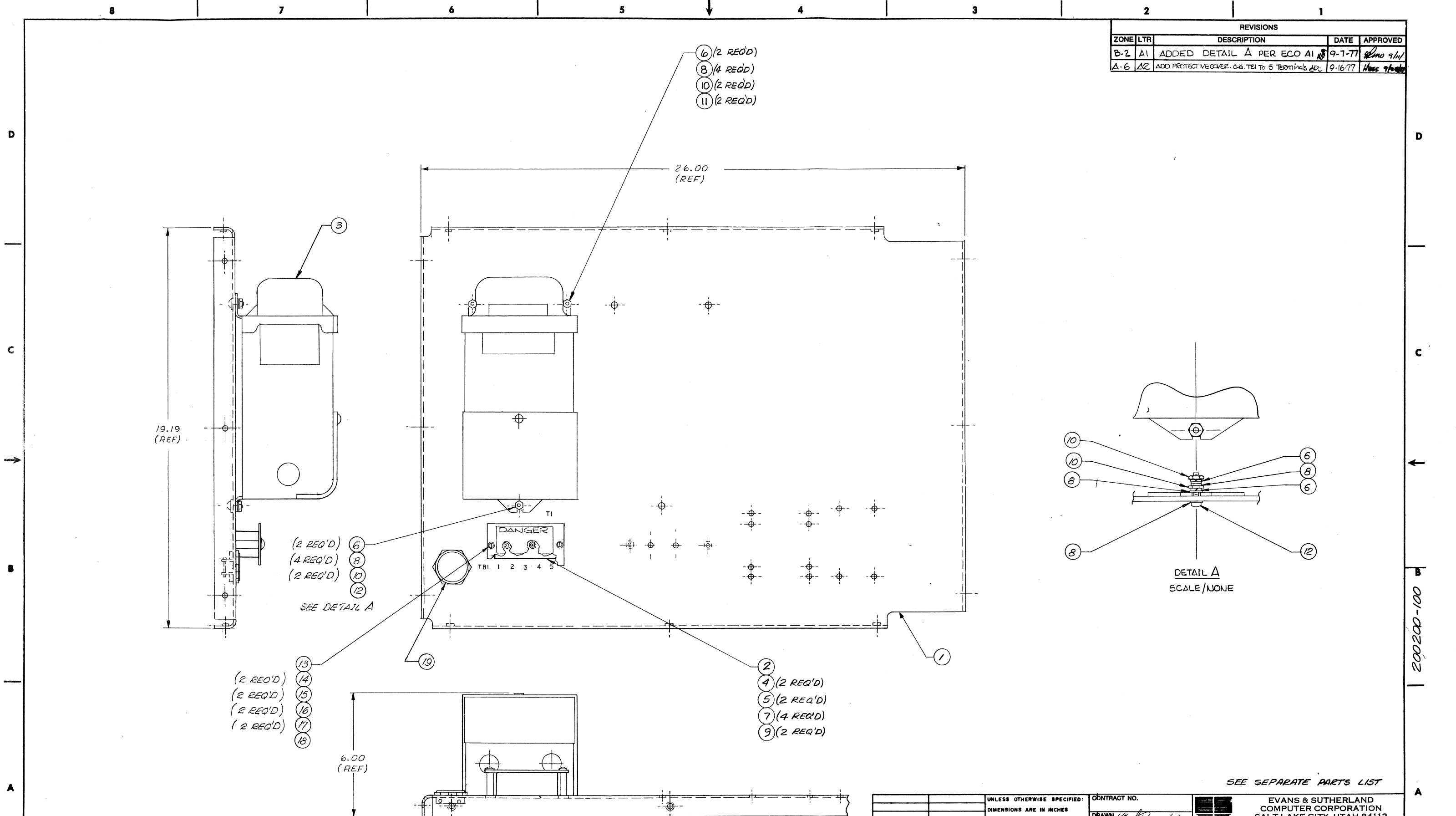
ASSY. NO.	ITEM #	REV. LEVEL
200185-112	200184-012	NC
200185-130	200184-030	NC
200185-160	200184-060	NC
200185-199	200184-100	NC



SEE SEPARATE PARTS LIST		EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
CONTRACT NO. DRAWN BY: <i>APROLL</i> 8-18-77		CHECKED BY: <i>Page</i> 23 AUG 77	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON .XX ± .XXX ± ANGLES ±		MECH ELEC PROJ. ENG APPROVED	
MATERIAL SEE PARTS LIST		ASSY, DISPLAY & INTCT CABLES, WITH SIG & PWR CBLs.	
NEXT ASSY APPLICATION		SIZE (CODE IDENT NO) C 53938 200185-TAB	
USED ON		SCALE 1/2"	
FINISH		REV SEE TABLE	
APPLICATION		SHEET 1 OF 1	



REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
B-2	A1	ADDED DETAIL A PER ECO AI	9-7-77	<i>[Signature]</i>
A-6	A2	ADD PROTECTIVE COVER. CHG. TEL TO 5 TERMINALS	9-16-77	<i>[Signature]</i>



1. SEE 200154-100 SHEET 3 FOR WIRING DIAGRAM.
NOTES:

SEE SEPARATE PARTS LIST

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON .XX± .XXX± ANGLES± MATERIAL FINISH		CONTRACT NO. DRAWN <i>[Signature]</i> CHECKED <i>[Signature]</i> MECH <i>[Signature]</i> ELEC	EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
200101-100 NEXT ASSY APPLICATION		NOVOVIEW USED ON	PROJ. ENG. DESIG. AUTO. APPROVED	TITLE ASSY, BOOST-BUCK (NOVOVIEW SP)
			CODE IDENT NO D 53938	REV AR
			SCALE 1/2	SHEET 1 OF 1

200200-100



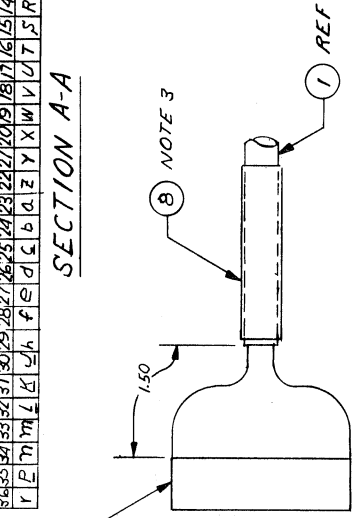
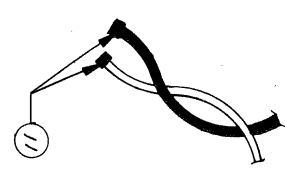
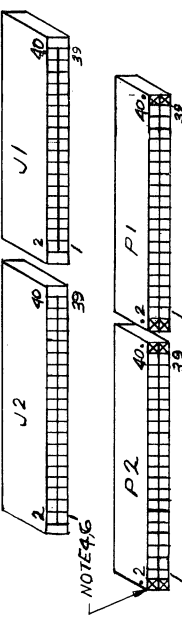
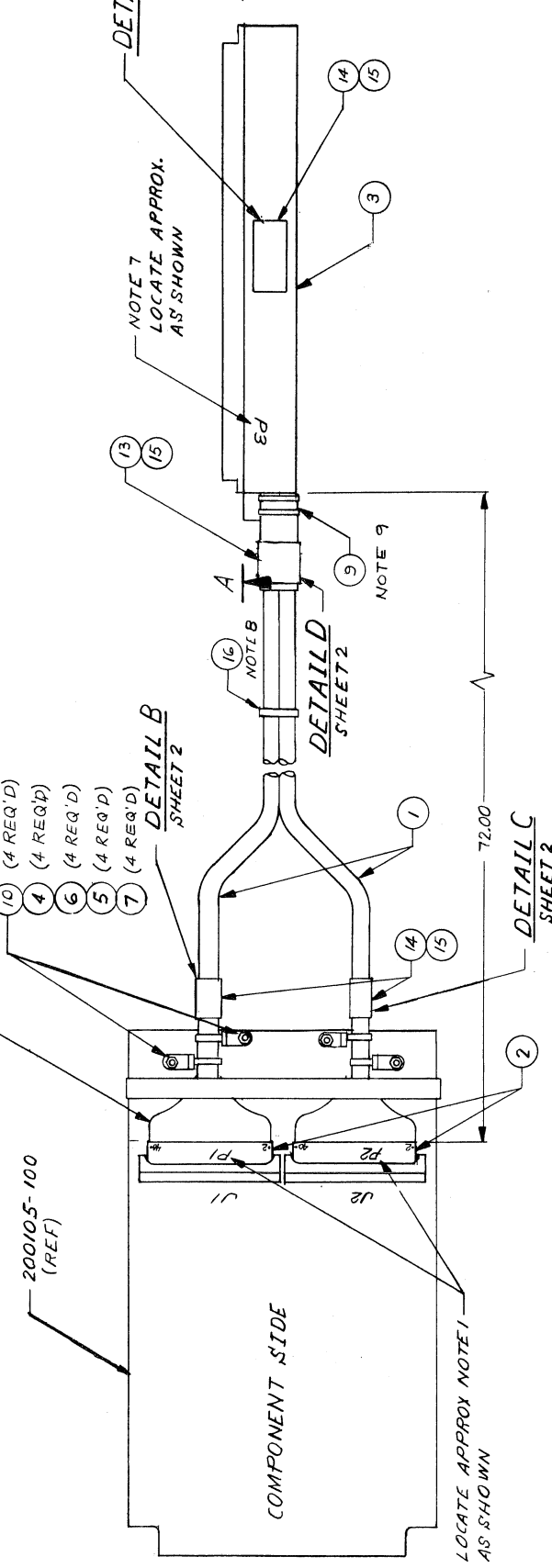
DETAIL A

- 10 (4 REQ'D)
- 4 (4 REQ'D)
- 6 (4 REQ'D)
- 5 (4 REQ'D)
- 7 (4 REQ'D)

DETAIL B
SHEET 2

DETAIL C
SHEET 2

DETAIL E



1. MARK P1, P2 ON TOP OF THE CONNECTOR USING A WHITE EPOXY INK.
2. ON THE 4 WIRES TERMINATING AT P3-36, USE ITEM 12 TO CONNECT THE WIRES TOGETHER, INCLUDING ONE ADDITIONAL WIRE WHICH IS SOLDERED TO P3-36.
3. CUT OFF UNUSED WIRES, ADD SHRINKABLE TUBING, ITEM 8.
4. END CAVITIES ON P1/P2 ARE NOT USED WHEN MATED WITH J1 OR J2 CONNECTOR.
5. ATTACH ITEM 10 LOOSELY AROUND CABLE BUT DO NOT TIGHTEN OR CUT OFF FINAL ALIGNMENT WILL BE DONE AT NEXT HIGHER ASSY.
6. INSTALL DUMMY PINS ITEM 11 IN P1-39, 40 & P2-42.
7. MARK P3 ON TOP OF THE CONNECTOR USING ITEM 13.
8. INSTALL ITEM 16 EVERY 12.00.
9. FOLD BACK UNUSED WIRES, ADD SHRINK TUBING ITEM 9.

CABLE PREPARATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTRACT NO.
.XX ± .05	DRAWN M WING 9-19-77
.XXX ±	CHECKED J. MANNING 9-22-77
ANGLES ±	MECH
✓	ELEC P. S. 27 SEPT 77
	PROJ. ENG MKD 4 OCT 77
	APPROVED
MATERIAL	
FINISH	
NEXT ASSY	USED ON
APPLICATION	

SEE SEPARATE PARTS LIST	
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112	
ASSY, CABLE, T.I. DMA CONTROLLER	
SIZE CODE IDENT NO	REV
C 53938	200203-006 NC
SCALE	SHEET 1 OF 2

NOTES:

TWISTED PAIR INDICATION		CABLE			CABLE			TWISTED PAIR INDICATION	
SIGNAL NAME	WIRE	F 1	CONNECTOR (P3)	SIGNAL NAME	WIRE	P 2	CONNECTOR (P3)		
	RED	NOT USED	P3-19	SIGROUND 12	BLACK	P2-5	P3-11		
	BLACK	P1-1		SIGROUND 12	YEL	6	A		
	ORN	2	19	SIGROUND 12	BLACK	7	A		
	BLACK	3	20	SIGROUND 12	BLACK	8	A		
	YEL	4	X	*DMRCH	RED	12	D		
	BLACK	5	21	*DMSTR	ORN	13	5		
	GRN	6	Y	*DMACCR	GRN	15	5		
	BLACK	7	22	DMD 15	BLACK	17	7		
	BLUE	P1-8	Z	DMD 14	BLUE	18	H		
	BLACK	NOT USED		DMD 13	BLACK	19	J		
	GRAY	P1-9	23	DMD 12	YEL	20	J		
	BLACK	35	36	DMD 11	BLACK	21	9		
	VIO	10	a	DMD 10	GRY	22	K		
	WHT	11	24	DMD 9	BLACK	23	10		
	BLACK	12	36	DMD 8	WHT	24	L		
	BLACK	16	36	DMD 7	BLACK	25	11		
	ORN	13	25	DMD 6	RED	26	11		
	BLACK	34	1	DMD 5	BLACK	27	12		
	RED	14	1	DMD 4	ORN	28	13		
	BLACK	33	1	DMD 3	BLACK	29	13		
	YEL	15	26	DMD 2	YEL	30	14		
	BLACK	17	27	DMD 1	BLACK	31	14		
	GRN	18	2	DMD 0	GRN	32	R		
	BLACK	19	28	DMADR 15	BLACK	33	15		
	BLUE	20	7	DMADR 14	BLU	34	5		
	BLACK	22	29	DMADR 13	BLACK	35	16		
	VIO	21	A	DMADR 12	VIO	36	T		
	BLACK	23	30	DMADR 10	WHT	37	17		
	GRY	24	J	DMADR 11	BLACK	38	U		
	BLACK	25	31	DMADR 9	RED	39	18		
	WHT	26	K	DMADR 8	BLACK	P2-40	P3-V		
	BLACK	27			BLACK	NOT USED	NOT USED		
	BLACK	28							
	BLACK	29							
	BLACK	30							
	BLACK	31							
	BLACK	32							
	BLACK	33							
	ORN	30	m						
	BLACK	31	31						
	YEL	32	31						
	BLACK	P1-36	P3-36						

NOTE 2

NOTE 2

14 REF
 15 REF

A1A1DMA
 200203-006
 A1A1FJ2

DETAIL E

13 REF
 15 REF

200203-006
 S/N ECO

DETAIL D

14 REF
 15 REF

A1A2J2
 200203-006
 A1A1DMA

DETAIL C

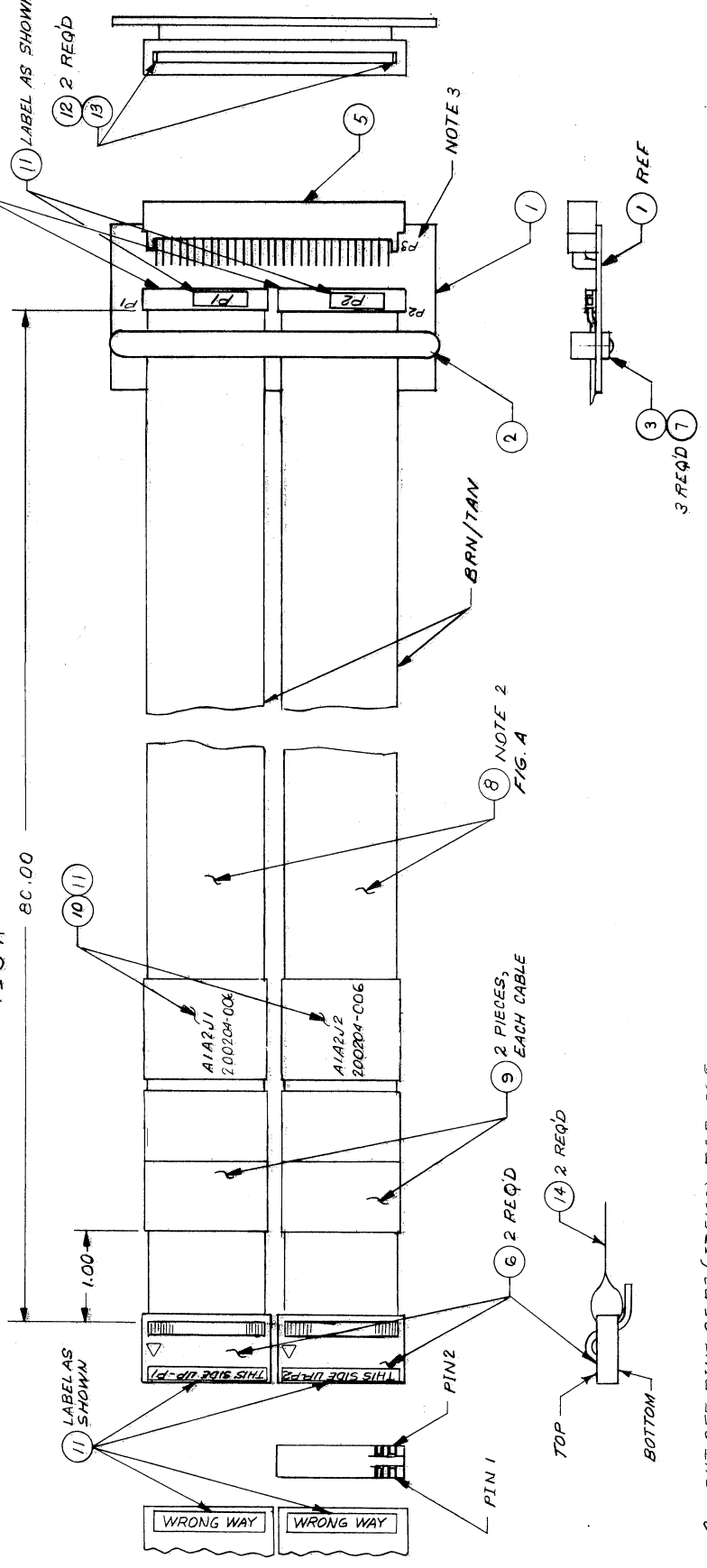
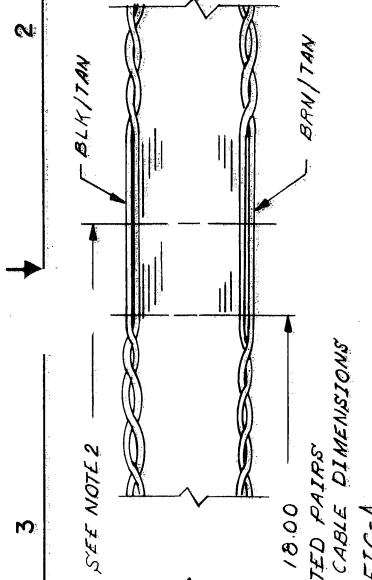
14 REF
 15 REF

A1A2J1
 200203-006
 A1A1DMA

DETAIL B

MOMENT CLATURE TO BE ADDED TO TAGS

ZONE	LTR	DESCR	E	APPROVED
AI	AI	MADE SEVERAL DWG. CHANGES. 10. 11-26-78	D	11-26-78



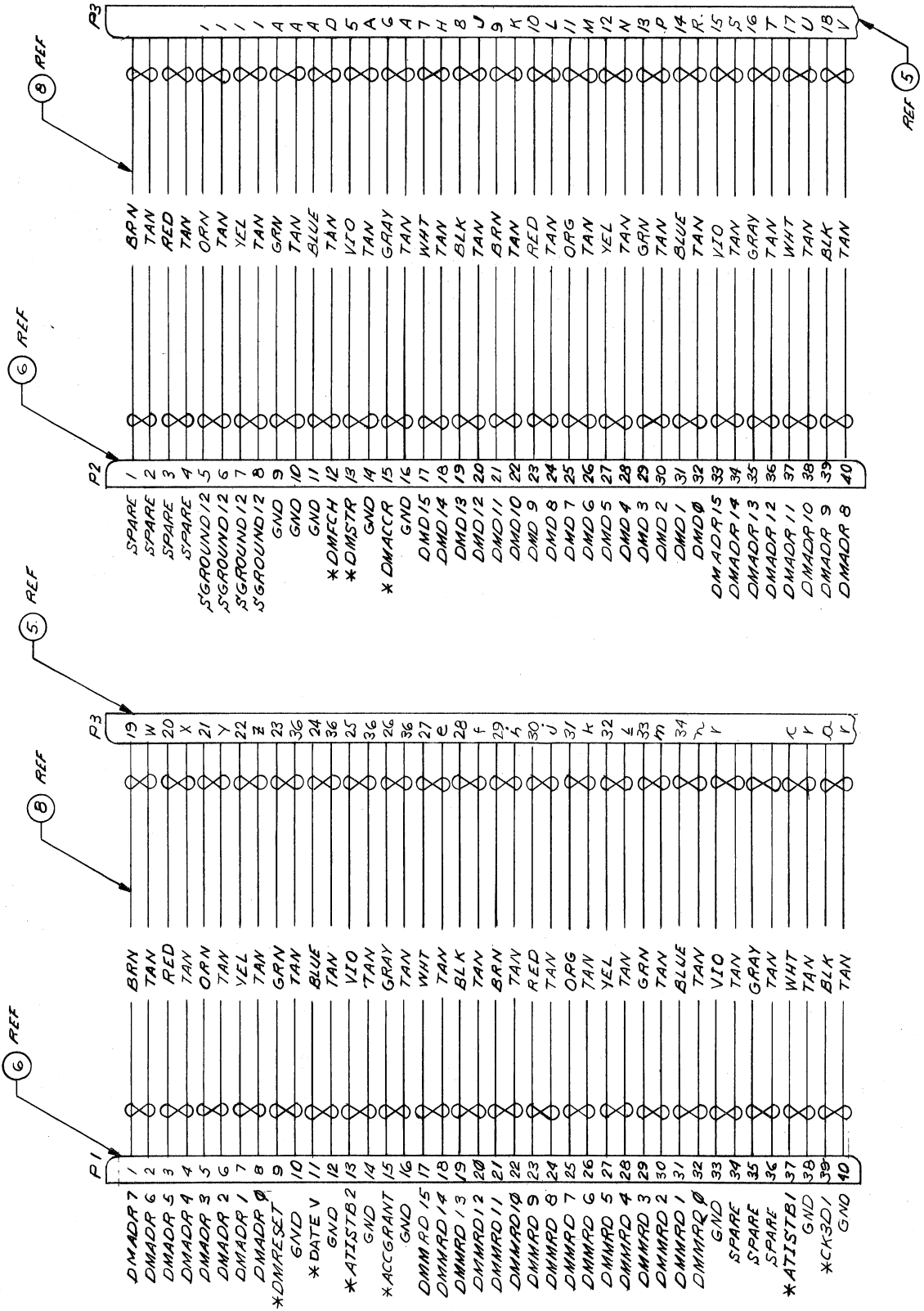
3. CUT OFF PINS OF P3 (ITEMS) PER EIC STANDARDS E-S-1.
 2. P3 PINS ARE TO BE INTERPRETED AS ETCHED ON THE PC. BOARD, IGNORE MARKING ON THE CONNECTOR.
 1. WHEN CUTTING CABLE CUT LINES ARE IN 20 INCH INCREMENTS. CUT TO BE CENTERED IN 2.0 INCH FLAT RIBBON AREA (SEE FIG A).
- ~~MARK CHARACTERS USING ORANGE INDUSTRIAL MARKING PROCESS 35-A OR EQUIV.~~

SEE SEPARATE PARTS LIST		CONTRACT NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	MATERIAL SEE PARTS LIST	FINISH
EVANS & SUTHERLAND COMPUTER CORPORATION SALT LAKE CITY, UTAH 84112		DRAWN 14 WING 3-14-78	TOLERANCES ON .XX ± .50 .XXX ± ANGLES ±		
CABLE ASSY, COMPUTER-DMA		MECH ELEC R S PROJ. ENG R S	APPROVED	101000-249	N/D
SIZE CODE IDENT NO	REV	APPROVED	APPROVED	NEXT ASSY	USED ON
C 53938	200204-006 AI	2 MAY 78	2 MAY 78	APPLICATION	
SCALE NONE	DO NOT SCALE	SHEET 1 OF 2			

200204-006

4 3 2 1

ZONE	LTR	DESCRIPTION	DATE	APPROVED



SIZE CODE IDENT NO	REV	OF 2
C 53938	200204-006	A1
SCALE	NAME	SH

B 200204-006

D C B A

APPENDIX A

NSP SYSTEM CONFIGURATIONS

A.1 PURPOSE OF APPENDIX

The purpose of this appendix is to define the various configurations of the NSP system.

A.2 OVERALL SYSTEM CONFIGURATION

Table A-1 lists the overall configuration of NSP systems by system part number for the systems 101000-168 through 101000-260. The overall configuration of systems delivered subsequent to system part number 101000-260 are provided on a fill-in sheet at the end of this appendix. Table A-1 includes the following information:

CHANNELS	Lists the number of channels in the system.
DISPLAYS	Lists the number of displays in the system.

NSP 901181-100B
NSP SYSTEM CONFIGURATIONS

CABINET 1	Lists the E&S part number of cabinet 1.
CABINET 2	Lists the E&S part number of cabinet 2.
TI COMPUTER	Lists the E&S part number for the TI 980B computer in the system.
AED FLOPPY DISK	Lists the E&S part number for the AED 6200 floppy disk in the system.
PRI. PWR. CONT.	Lists the E&S part number for the primary power control in the system.
CABLE SET	Lists the E&S part number for the data and power cables in the system that are not part of another assembly.
OPTIONS	Lists the number of optional peripheral units and raster mask option in the system.

Table A-2 provides a break down of the TAB numbers for the TI 980B computer listed in table A-1.

Table A-3 provides a break down of the TAB numbers for the AED 6200 floppy disk listed in table A-1.

Table A-4 provides a break down for the TAB numbers for the primary power control listed in table A-1.

Table A-5 provides a break down of the card sets for the back panel in cabinet 1 by the TAB number for cabinet 1 listed in table A-1.

Table A-6 provides a break down of the card sets for the back panel in cabinet 2 by the TAB number for cabinet 2 listed in table A-1.

Table A-7 provides a break down for the TAB numbers for the cable set listed in table A-1.

Table A-8 lists the quantity and TAB number of the display assembly cables for the system.

Table A-9 provides the E&S part number for the display assembly as it relates to the interconnect cable between the display and the display power supply.

Tables A-10 through A-15 provide a break down for the card set TAB numbers listed in tables A-5 and A-6. Note that various card sets include cables.

Table A-16 lists the quantity and card slot location of the cards of the input and interface, and viewpoint processor card sets. Note that all of these cards are located in the back panel of cabinet 1. Only a clock maintenance card of these groups is required in the back panel of cabinet 2 for systems with three or four channels.

Table A-17 lists the quantity and card slot location for the cards of the channel processor group for systems with up to four channels. Note that all channel processor cards are located in the back panel in cabinet 1.

Table A-18 lists the quantity and card slot locations for the card sets of the display processor, the basic display processor, the surface processor, and the light occultation card set. Note that the cards for channels 0 and 1 are located in the back panel of cabinet 1, and the cards for channels 2 and 3 are located in the back panel of cabinet 2.

In the following tables, a single digit column entry indicates quantity, a three digit column entry indicates the TAB number, and the entries using x/xxx indicate the quantity and the TAB number.

SYSTEM PART NUMBER 101000-TAB	CHANNELS	DISPLAYS	CABINET 1 200101-TAB	CABINET 2 200102-TAB	TI COMPUTER 150125-TAB	AED FLOPPY DISK 153145-TAB	PRI. PWR. CONT. 200154-TAB	CABLE SET 200104-TAB	OPTIONS			
									TEK 4010 TERMINAL	HAZELTINE TERMINAL	LINE PRINTER	RASTER MASK
168	3	4	101	101	107	100	100	101				
169	1	2	102		109	102	100	102				
170	4	6	100	100	118	101	100	100	1			
171	4	6	100	100	118	101	100	100	1			
172	4	6	100	100	118	101	100	100	1			
173	4	6	100	100	118	101	100	100	1			
174	4	6	100	100	118	101	100	100	1			
175	4	6	100	100	118	101	100	100	1			
176	4	6	100	100	118	101	100	100	1			
177	4	6	100	100	118	101	100	100	1			
186	3	4	101	101	107	100	100	101	1			
189	1	2	104		110	100	102	104		1	1	
190	1	2	104		110	100	102	104		1	1	
191	1	2	104		110	100	102	104		1	1	
192	1	2	104		110	100	102	104		1	1	
193	1	2	104		110	100	102	104		1	1	
198	1	3	105		111	100	102	105				
199	3	4	106	106	108	102	100	106				
200	1	2	107		108	102	103	107				
201	3	4	108	108	107	100	100	108				
206	1	2	109		111	100	102	109				
207	1	2	109		111	100	102	109				
208	1	2	110		111	100	103	110				
209	3	4	113	113	108	102	104	113				
210	2	4	111		108	102	102	111				
211	1	2	105		111	100	102	105				
212	3	4	108	108	107	100	100	108				
213	3	4	117	113	108	102	104	113				
228	1	3	112		108	100	102	112				
229	1	3	112		108	100	102	112				
232	3	4	117	113	108	102	104	113				
233	3	4	117	113	108	102	104	113				
234	1	2	118		111	103	102	118				
235	3	4	115	115	111	102	104	115				
237	1	2	114		108	103	103	114				
238	1	2	103		111	100	102	103				
239	2	4	116		108	102	104	116				
258	1	2	103		111	100	102	103				
259	1	2	103		111	100	102	103				
260	1	2	103		111	100	102	103				

TABLE A-1
OVERALL SYSTEM CONFIGURATION

TI 908B COMP. 150125 -TAB Note 1	COMP. MEMORY TOTAL	ASR 733 801392 -105 Note 2	INTERVAL TIMER 801852 -001	DATA MODULE		COMM. MODULE 801909 -101
				801894 -101	801894 -102	
-107	24K	1		1		
-108	40K	1		1		
-109	48K	1	1	1		
-110	32K			1		1
-111	32K	1		1		
-118	48K	1	1	1	1	

Note 1: All P/N 150125-TABs include the DMAC Interface Module P/N 801396-101.

Note 2: The COMM. Module for the ASR 733 is included in the P/N 801392-105.

TABLE A-2
TI 980B COMPUTER CONFIGURATION

FLOPPY DISK 153145- TAB	DRIVES/POWER REQ'D
100	Single disk drive - 60Hz
101	Dual disk drive - 60HZ
102	Single disk drive - 50HZ
103	Dual disk drive - 50HZ

TABLE A-3
AED 6200 FLOPPY DISK CONFIGURATION

NSP 901181-100B
NSP SYSTEM CONFIGURATIONS

PRIMARY POWER CONTROL 200154-TAB	+5V @150A 200151-100	+12V @15A 200152-100	+15V @2.8A 200153-100	CABLES REQUIRED						
				CABLE ASSY. 101368-001	CABLE ASSY. 101369-001	CABLE ASSY. 101370-001	CABLE ASSY. 101371-001	CABLE ASSY. 101372-001	00 +5V	00 GND
100	2	1	1	2	1	1	1	1	2	2
102	1	1	1	1	1	1	1	1	1	1
103	1	1	1	1	1	1	1	1	1	1
104	2	1	1	2	1	1	1	1	2	2

TABLE A-4
PRIMARY POWER CONTROL CONFIGURATION

CABINET 1 200101-TAB	CARD SETS						
	INPUT & INTERFACE CARD SET 200141-TAB	VIEWPOINT PROC. CARD SET 200142-TAB	CHANNEL PROC. CARD SET 200143-100	DISP. PROC. CARD SET 200191-TAB	SURFACE PROC. CARD SET 200192-TAB	LEGHT OCCULT. CARD SET 200193-TAB	
100	100	100	4	1/100	2/101		
101	100	100	3	1/101			
102	100	100	1	2/100	2/100	2/100	
103	101	100	1	1/100	1/103	1/100	
104	100	100	1	1/100	1/102	1/101	
105	101	100	1	1/100	1/101	1/100	
106	100	100	1	1/100	1/102	1/101	
107	100	100	3	2/100	1/103	2/100	
108	100	100	1	1/100	1/103	1/100	
109	100	100	3	2/100	2/100		
110	100	100	1	1/100	1/101	1/100	
111	100	100	1	1/100	1/101	1/100	
112	100	100	2	1/100	2/103	2/100	
113	101	100	1	1/101			
114	101	100	1	1/101	1/101	1/101	
115	101	100	3	2/100	2/103	2/101	
116	101	100	1	1/100	1/103	1/101	
117	101	100	3	2/100	2/101	2/101	
118	101	100	1	1/100	2/103	2/101	

TABLE A-5
CARD SETS CABINET 1 BACK PANEL

CABINET 2 200102- TAB	CARD SETS			
	CLOCK MAINTENANCE CARD 200107-100 (WITH CABLE)	DISP. PROC. CARD SET 200191-TAB	SURFACE PROC. CARD SET 200192-TAB	EIGHT OCCULT. CARD SET 200193-TAB
100	1	2/100	1/101	
101	1	1/100	1/100	1/100
106	1	1/100	1/103	1/100
108	1	1/100	1/100	
113	1	1/100	1/103	1/101
115	1	1/100	1/101	1/101

TABLE A-6
CARD SETS CABINET 2 BACK PANEL

CABLE SET 200104- TAB	DATA/POWER CABLE ASSEMBLIES										
	101328-003	101328-004	101374-002	101374-004	101375-005	101375-007	101376-005	101377-007	101378-006	101379-007	200178-024
100	4	5	2	1	1	1	1	1	1	1	2
101	4	3			1	1	1	1	1	1	1
102	2				1		1	1	1		1
103	2				1		1	1	1		1
104	2				1		1	1	1		1
105	2				1		1	1	1		1
106	4	3			1	1	1	1	1	1	1
107	2				1		1	1	1		1
108	4	3			1	1	1	1	1	1	1
109	2				1		1	1	1		1
110	2				1		1	1	1		1
111	4		1		1		1	1	1		1
112	2				1		1	1	1		1
113	7				1	1	1	1	1	1	1
114	2				1		1	1	1		1
115	7				1	1	1	1	1	1	1
116	4				1		1	1	1		1
118	2				1		1	1	1		1

TABLE A-7
DATA/POWER CABLE ASSEMBLIES

SYSTEM PART NUMBER 101000- TAB	DISPLAY CABLES		
	DISPLAY POWER CABLE ASSY. 200182-TAB	DISPLAY SIGNAL CABLE ASSY. 200183-TAB	DISPLAY INTERCON. CABLE ASSY. 200184-TAB
168	3/100	3/100	3/100
169	2/100	2/100	1/060
170	6/100	6/100	1/012,2/030,3-060
§	§	§	§ § §
177	6/100	6/100	1/012,2/030,3-060
186	4/100	4/100	4/100
189	2/100	2/100	2/060
§	§	§	§
193	2/100	2/100	2/060
198	2/100	2/100	2/060
199	4/100	4/100	4/060
200	2/100	2/100	2/060
201	4/100	4/100	4/060
206	2/100	2/100	2/060
207	2/100	2/100	2/060
208	2/100	2/100	2/060
209	4/100	4/150	4/100
210	4/100	4/100	1/012,3/100
211	2/100	2/100	2/060
212	4/150	4/150	4/060
213	4/100	4/100	4/060
228	1/075	1/075	1/012
	2/100	2/125	2/060
229	1/075	1/075	1/012
	2/100	2/125	2/060
232	4/100	4/100	4/060
233	4/100	4/100	4/060
234	2/150	2/150	2/060
235	4/150	4/150	4/060
237	2/100	2/150	2/060
238	2/100	2/100	2/060
239	4/100	4/150	4/060
258	2/100	2/100	2/060
259	2/100	2/100	2/060
260	2/100	2/100	2/060

TABLE A-8
 DISPLAY CABLE ASSEMBLIES

DISPLAY ASSY. PART NUMBER	INTERCONNECT CABLE PART NUMBER
200185-112	200184-012
200185-130	200184-030
200185-160	200184-060
200185-199	200184-100

TABLE A-9
DISPLAY ASSEMBLY PART NUMBERS

INPUT & INTERFACE CARD SET 200141- TAB	CABLE ASSY. 40 CONDUCTOR 101328-002	CABLE ASSY. TI DMA CONTROLLER 200202-006	CABLE ASSY. TI DMA CONTROLLER 200204-006	CARD ASSY. WW DMA INTERFACE 200105-100	CARD ASSY. WW DMA INTERFACE 200105-101	CARD ASSY. WW INPUT CONTROL 200106-100	CARD ASSY. WW CLOCK MAINTENANCE 200107-100	CARD ASSY. WW DISK INTERFACE 200108-100
100	1	1		1		1	1	1
101	1		1		1	1	1	1

TABLE A-10
INPUT AND INTERFACE CARD SETS
CONFIGURATIONS

VIEWPOINT PROCESSOR CARD SET 200142- TAB	CARD ASSY. PC ADDER 200110-100	CARD ASSY. WW DIRECTIONALITY 200111-100	CARD ASSY. WW BRIGHTNESS 200112-100	CARD ASSY. WW GP CONTROL 1 200113-100	CARD ASSY. WW GP CONTROL 2 200114-100	CARD ASSY. WW GP CONTROL 2 200114-101
100	3	1	1	1	1	
101	3	1	1	1		1

TABLE A-11
VIEWPOINT PROCESSOR CARD SET
CONFIGURATIONS

CHANNEL PROCESSOR CARD SET 200143- TAB	CARD ASSY. PC MULTIPLIER 200120-100	CARD ASSY. PC DIVIDER 200121-100	CARD ASSY. MW DB CONTROL 200122-100	CARD ASSY. MW DB MEMORY 200123-100
100	1	1	1	2

TABLE A-12
 CHANNEL PROCESSOR CARD SETS
 CONFIGURATIONS

BASIC DISPLAY PROCESSOR CARD SET 200191- TAB	CABLE ASSY. 40 CONDUCTOR 101329-002	CARD ASSY. PC DISPLAY DATA 200155-100	CARD ASSY. MW ANALOG CONTROL 200156-100	CARD ASSY. PC ANALOG INTENSITY 200157-100	CARD ASSY. PC SCOPE DRIVER 200158-100	CARD ASSY. PC X-Y D/A CONVERTER 200159-100
100	1	2	1	1	1	2
101	1	2	1	1	2	2
102	1	2	1	1	3	2

TABLE A-13
 BASIC DISPLAY PROCESSOR CARD SETS
 CONFIGURATIONS

SURFACE PROCESSOR CARD SET 200192 - TAB	CARD ASSY. PC FACE GENERATOR 200160-100	CARD ASSY. PC PRIORITY SORTER 200161-100	CARD ASSY. PC LINE ASSEMBLER 200162-100	CARD ASSY. MW E/F CONTROL 200163-100	CARD ASSY. MW S/L CONTROL 200164-100	CARD ASSY. PC EDGE GENERATOR 200165-100
100	1	1	4	1	1	1
101	2	1	4	1	1	2
102	3	1	4	1	1	3
103	4	1	4	1	1	4

TABLE A-14
 SURFACE PROCESSOR CARD SETS
 CONFIGURATIONS

LIGHT OCCULTATION CARD SET 200193 - TAB	CARD ASSY. MW L.O. TRANSFORM 200170-100	CARD ASSY. MW L.O. CONTROL 200171-100	CARD ASSY. MW L.O. MAP 200172-100	CARD ASSY. PC L.O. MAP 200172-101
100	1	1	1	
101	1	1		1

TABLE A-15
 LIGHT OCCULTATION CARD SETS
 CONFIGURATIONS

NSP 901181-100B
 NSP SYSTEM CONFIGURATIONS

CARD NAME/ PART NUMBER	CABINET 1 BACK PANEL CARD SLOT	CABINET 2 BACK PANEL CARD SLOT
DMAC (Note 1) 200105-	2	
INPUT CONTROL (Note 1) 200106-100	3	
CLOCK MAINTENANCE (Note 2) 200107-100	12	12
DISK INTERFACE (Note 1) 200108-100	1	
ADDER (Note 3) 200110-100 (X axis) (Y axis) (Z axis)	4 5 6	
DIRECTIONALITY (Note 1) 200111-100	7	
BRIGHTNESS (Note 1) 200112-100	8	
GP CONTROL 1 (Note 1) 200113-100	9	
GP CONTROL 2 (Note 1) 200114-	10	

Note 1: Only one card of this type is required for each system.

Note 2: One card of this type is required for each back panel in the system. The clock maintenance card requires unique switch settings determined by the back panel in which it is to be used; refer to subsection 5.2.3.

Note 3: Three cards of this type are required for each system, one for each axis as indicated.

TABLE A-16
 INPUT AND INTERFACE, AND VIEWPOINT
 PROCESSOR CARD SETS LOCATION

CARD NAME/ PART NUMBER	CABINET 1 BACK PANEL CARD SLOT / CHANNEL			
	CH#0	CH#1	CH#2	CH#3
MULTIPLIER (Note 1) 200120-100	13	16	19	22
DIVIDER (Note 1) 200121-100	14	17	20	23
DISPLAY BUFFER CONTROL (Note 1) 200122-100	15	18	21	24
DISPLAY BUFFER MEMORY (Note 2) 200123-100 (X axis)	25	27	29	31
(Y axis)	26	28	30	32

Note 1: Only one card of this type is required for each channel.

Note 2: Two cards of this type are required for each channel, one for each axis as indicated.

TABLE A-17
 CHANNEL PROCESSOR
 CARD SETS LOCATIONS

NSP 901181-100B
NSP SYSTEM CONFIGURATIONS

CARD NAME/ PART NUMBER	CARD SLOT / CHANNEL			
	CABINET 1		CABINET 2	
	BACK PANEL	BACK PANEL	BACK PANEL	BACK PANEL
	CH#0	CH#1	CH#2	CH#3
DISPLAY DATA (Note 1) 200155-100 X axis Y axis	49 50	81 82	49 50	81 82
ANALOG CONTROL (Note 2) 200156-100	51	83	51	83
ANALOG INTENSITY (Note 2) 200157-100	56	88	56	88
SCOPE DRIVER (Note 3) 200158-100 (Main display) (Optional display)	62 64	94 96	62 64	94 96
X-Y D/A CONVERTER (Note 1) 200159-100 (X axis) (Y axis)	58 60	90 92	58 60	90 92
FACE GENERATOR (Note 4) 200160-100 (Option 0) (Option 1) (Option 2) (Option 3)	42 43 44 45	74 75 76 77	42 43 44 45	74 75 76 77
PRIORITY SORTER (Note 2) 200161-100	46	78	46	78
LINE ASSEMBLER (Note 5) 200162-100 (#0) (#1) (#2) (#3)	55 54 53 52	87 86 85 84	55 54 53 52	87 86 85 84
E/F CONTROL (Note 2) 200163-100	47	79	47	79
S/L CONTROL (Note 2) 200164-100	48	80	48	80
EDGE GENERATOR (Note 4) 200165-100 (Option 0) (Option 1) (Option 2) (Option 3)	38 39 40 41	70 71 72 73	38 39 40 41	70 71 72 73
LQ TRANSFORM (Note 6) 200170-100	36	68	36	68
LQ CONTROL (Note 6) 200171-100 1102	37	69	37	69
LQ MAP (Note 6) 200172- 1101	35	67	35	67

TABLE A-18 (Sheet 1)
DISPLAY PROCESSOR CARD SETS LOCATIONS

- Note 1: Two cards of this type are required for each channel, one for each axis as indicated.
- Note 2: One card of this type is required for each channel.
- Note 3: The optional display scope driver card may be assigned to only one channel.
- Note 4: The number of the E/F option determines the number of the edges and faces, and the number of cards for the channel.
- Option 0 - One card of each type: 64 edges and 64 faces.
- Option 1 - Two cards of each type: 128 edges and 128 faces.
- Option 2 - Three cards of each type: 192 edges and 192 faces.
- Option 3 - Four cards of each type: 256 edges and 256 faces.
- Note 5: Four cards of this type are required for each channel.
- Note 6: The Light Occultation cards are optional and may be omitted from the system, or when used may not be assigned to all channels in the system.

TABLE A-18 (Sheet 2)
DISPLAY PROCESSOR CARD SETS LOCATIONS

NSP SYSTEM CONFIGURATION
 FOR SYSTEM PART NUMBER

101000-

CHANNELS	DISPLAYS	CABINET 1 200101-TAB	CABINET 2 200102-TAB	TI CONMUTER 150125-TAB	AED FLOPPY DISK 153145-TAB	PRI. PWR. CONT. 200154-TAB	CABLE SET 200204-TAB	OPTIONS			
								TEK 4010 TERMINAL	HAZELTINE TERMINAL	LINE PRINTER	RASTER MASK

OVERALL CONFIGURATION

	INPUT & INTERFACE CARD SET 200141-TAB	VIEWPOINT PROC. CARD SET 200142-TAB	CHANNEL PROC. CARD SET 200143-100	BASIC DISP. PROC. CARD SET 200191-TAB	SURFACE PROC. CARD SET 200192-TAB	LIGHT OCCULT. CARD SET 200193-TAB	CLOCK MAINTENANCE CARD 200107-100 (WITH CABLE)
CABINET 1							
CABINET 2							

CARD SET CONFIGURATIONS

DISPLAY CABLES			DATA AND POWER CABLES										
DISPLAY POWER CABLE ASSY. 200182-TAB	DISPLAY SIGNAL CABLE ASSY. 200183-TAB	DISPLAY INTCON CABLE ASSY. 200184-TAB	101328-003	101328-004	101374-002	101374-004	101375-005	101375-007	101376-005	101377-007	101378-006	101379-007	200178-024

CABLE CONFIGURATION

APPENDIX B

ROM TABLES

B.1 INTRODUCTION

This appendix consists of tables of ROMs in the 101000-170 system, listed numerically by the part number of the ROM. The circuit card number and ROM position on the card is listed following the ROM part number for each occurrence of that ROM.

NSP 901181-100B
ROM TABLES

CARD P/N 200106-100	
POS	PROM P/N
U42	807739-055 A75
U52	807739-055 A76
U70	807808-016 A14
U72	807739-055 A77

CARD P/N 200107-100	
POS	PROM P/N
U10	807739-055 A58
U12	807739-055 A59
U13	807739-055 A58
U22	807808-016 A22

CARD P/N 200108-100	
POS	PROM P/N
U21	807808-016 A03
U31	807808-016 A04
U33	807739-055 A71
U41	807808-016 A05
U50	807739-055 A96
U51	807808-016 A06
U53	807739-055 A72
U61	807808-016 A07
U71	807808-016 A08

CARD P/N 200111-100	
POS	PROM P/N
U21	807802-055 A01
U22	807802-055 A05
U31	807802-055 A00

CARD P/N 200112-100	
POS	PROM P/N
U46	807802-055 A35
U56	807802-055 A36
U74	807220-018 A09

CARD P/N 200113-100	
POS	PROM P/N
U13	807220-018 A01
U14	807220-018 A02
U60	807739-055 A73
U74	807220-018 A03

CARD P/N 200114-100	
POS	PROM P/N
U16	807802-055 A06
U25	807802-055 A07
U32	807802-055 A08
U33	807802-055 A09
U35	807802-055 A10
U42	807802-055 A11
U43	807802-055 A12
U45	807802-055 A13
U46	807802-055 A14
U52	807802-055 A15
U53	807802-055 A16
U71	807220-018 A04
U73	807220-018 A05
U74	807220-018 A06
U75	807220-018 A07
U76	807220-018 A08

CARD P/N 200114-101	
POS	PROM P/N
U16	THRU SAME AS 200114-100
U43	
U45	807802-055 A41
U46	THRU SAME AS 200114-100
U76	

CARD P/N 200121-100	
POS	PROM P/N
U60	807629-055 B02
U70	807629-055 B03

CARD P/N 200122-100	
POS	PROM P/N
U62	807629-055 A96
U74	807739-055 A61
U75	807739-055 A62

CARD P/N 200155-100	
POS	PROM P/N
U61	807739-055 A47
U71	807739-055 A69

CARD P/N 200156-100	
POS	PROM P/N
U35	807808-016 A09
U45	807808-016 A10
U55	807808-016 A11
U65	807808-016 A12
U75	807808-016 A13

CARD P/N 200158-100	
POS	PROM P/N
U31	807739-055 A95
U62	807808-016 A23

CARD P/N 200159-100	
POS	PROM P/N
U64	807820-016 A01

CARD P/N 200160-100	
POS	PROM P/N
U93	807739-055 A56

CARD P/N 200162-100	
POS	PROM P/N
U13	807626-055 A77
U16	807626-055 A32
U20	807626-055 A73
U23	807626-055 A80

CARD P/N 200163-100	
POS	PROM P/N
U22	807629-055 A85
U23	807629-055 A86

CARD P/N 200164-100	
POS	PROM P/N
U55	807739-055 A57

TABLE B-1
ROM TABLES FOR THE NSP SYSTEM