VOLUME 002 MACHINE 3705- -0015984 MODEL E08 SYSTEM 0004XBW MODE BOX SHIP 82/12/30

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
AA001*		CCU BASIC ALD'S E-H	0001750123	315053	.W. 0001750039
AA002*		CCU BASIC ALD'S E-H	0001750124	315053	.W. 0001750039
AA003*		CCU BASIC ALD'S E-H	0001750125	315053	.W. 0001750039
AA004		CCU BASIC ALD'S A-H	0005997553	309545	.W. 0001750039
AA005*		CCU BASIC ALD'S E-H	0001750126	315053	.W. 0001750039
AB001		CCU BASIC ALD'S A-L	0001785378	315621	.W. 0001750039
AB002		CCU BASIC ALD'S A-L	0001785379	315621	.W. 0001750039
AB003		CCU BASIC ALD'S A-L	0001785380	315621	.W. 0001750039
AB004		CCU BASIC ALD'S A-L	0001785381	315621	.W. 0001750039
AB005		CCU BASIC ALD'S A-L	0001785382	315621	.W. 0001750039
AB006		CCU BASIC ALD'S A-L	0001785383	315621	.W. 0001750039
AB007		CCU BASIC ALD'S A-L	0001785384	315621	.W. 0001750039
AB008		CCU BASIC ALD'S A-L	0001785385	315621	.W. 0001750039
AB009		CCU BASIC ALD'S E-H	0001749379	315621	.W. 0001750039
AB010		CCU BASIC ALD'S A-H	0001749380	315621	.W. 0001750039
AJ001*		CCU BASIC ALD'S E-H	0001750128	315053	.W. 0001750039
AJ002*		CCU BASIC ALD'S E-H	0001750129	315053	.W. 0001750039
AM001*		CCU BASIC ALD'S E-H	0001750130	315053	.W. 0001750039
AM002*		CCU BASIC ALD'S E-H	0001750131	315053	.W. 0001750039
AP001*		CCU BASIC ALD'S E-H	0001750132	315053	.W. 0001750039
AP002		CCU BASIC ALD'S A-H	0005997567	309545	.W. 0001750039
AP003		CCU BASIC ALD'S A-H	0005997568	309545	.W. 0001750039
AP004*		CCU BASIC ALD'S E-H	0001750133	315053	.W. 0001750039
AP005		CCU BASIC ALD'S A-H	0005997570	309533	.W. 0001750039
AP006		CCU BASIC ALD'S A-H	0005997571	309545	.W. 0001750039
AP007		CCU BASIC ALD'S A-H	0005997572	309533	.W. 0001750039
AP008*		CCU BASIC ALD'S E-H	0001750134	322689	.W. 0001750039
AP009*		CCU BASIC ALD'S E-H	0001750135	315053	.W. 0001750039
AP010*		CCU BASIC ALD'S E-H	0001750136	315053	.W. 0001750039
AP011*		CCU BASIC ALD'S E-H	0001750137	315053	.W. 0001750039
AP012*		CCU BASIC ALD'S E-H	0001750138	315053	.W. 0001750039
AP013*		CCU BASIC ALD'S E-H	0001750139	315053	.W. 0001750039
AP014*		CCU BASIC ALD'S E-H	0001750140	315053	.W. 0001750039
AP015*		CCU BASIC ALD'S E-H	0001750141	315053	.W. 0001750039
AU001*		CCU BASIC ALD'S E-H	0001750142	315053	.W. 0001750039
CA001*		CCU BASIC ALD'S E-H	0001750143	315053	.W. 0001750039
CA002*		CCU BASIC ALD'S E-H	0001750144	315053	.W. 0001750039
CA003*		CCU BASIC ALD'S E-H	0001750145	315053	.W. 0001750039
CA004*		CCU BASIC ALD'S E-H	0001750146	315053	.W. 0001750039
CC001*		CCU BASIC ALD'S E-H	0001750147	315053	.W. 0001750039
CC002		CCU BASIC ALD'S E-H	0005997587	310268	.W. 0001750039
CC003*		CCU BASIC ALD'S E-H	0001750148	315053	.W. 0001750039

VOLUME 002 MACHINE 3705- -0015984 MODEL E08 SYSTEM 0004XBW MODE BOX SHIP 82/12/30

LOGIC TYPE -0- SYSTEMS DIAGRAMS

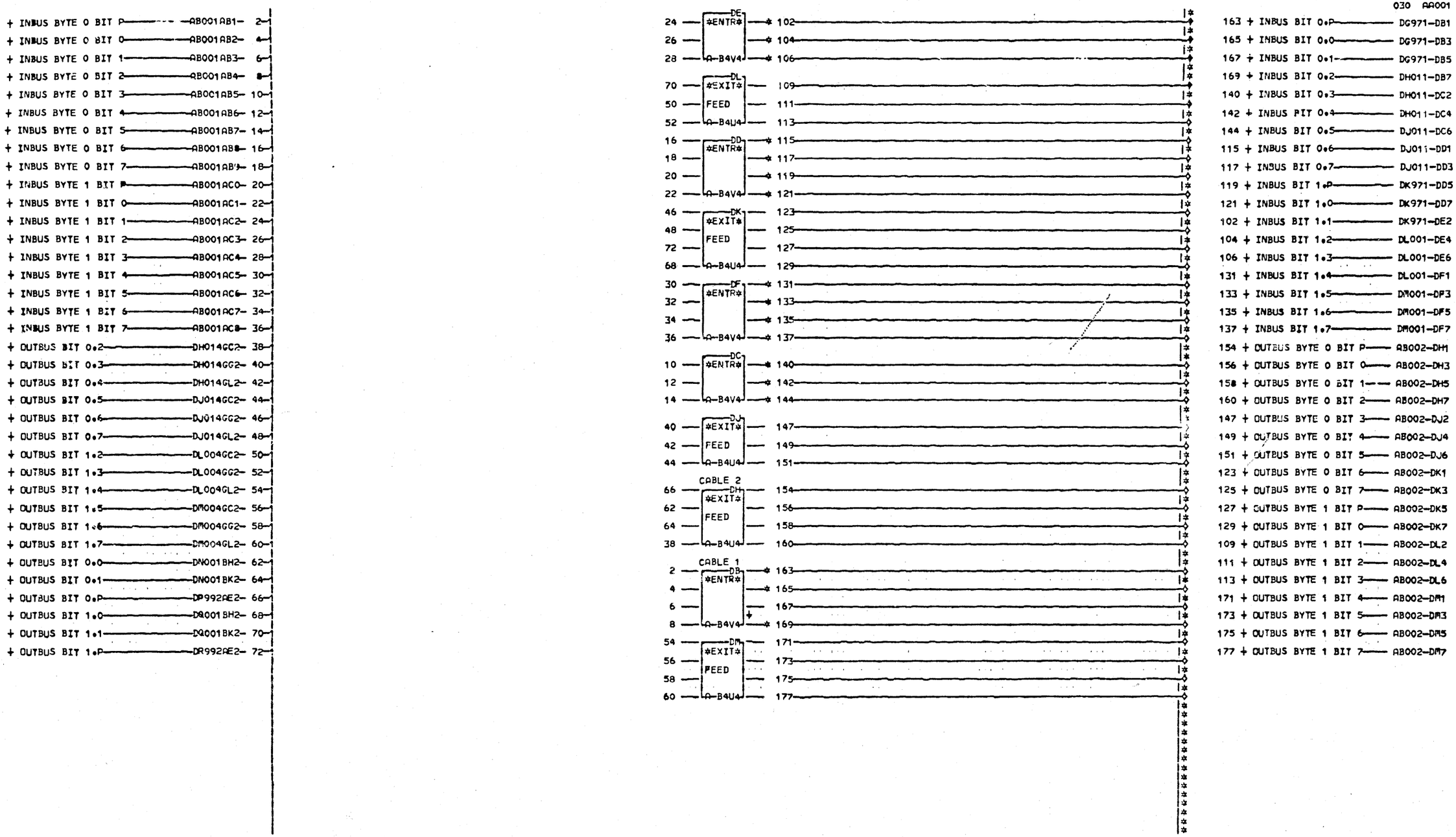
PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
CC004*		CCU BASIC ALD'S E-H	0001750149	315053	.W. 0001750039
CC005*		CCU BASIC ALD'S E-H	0001750150	315053	.W. 0001750039
CC006*		CCU BASIC ALD'S E-H	0001750151	315053	.W. 0001750039
CC007*		CCU BASIC ALD'S E-H	0001750152	315053	.W. 0001750039
CC008*		CCU BASIC ALD'S E-H	0001750153	315053	.W. 0001750039
CD001		CCU BASIC ALD'S E-H	0001750154	315053	.W. 0001750039
CD002*		CCU BASIC ALD'S E-H	0001750155	315053	.W. 0001750039
CD003*		CCU BASIC ALD'S E-H	0001750156	315053	.W. 0001750039
CD004*		CCU BASIC ALD'S E-H	0001750157	315053	.W. 0001750039
CF001		CCU BASIC ALD'S E-H	0005997598	309545	.W. 0001750039
CF002*		CCU BASIC ALD'S E-H	0001750158	315053	.W. 0001750039
CF003*		CCU BASIC ALD'S E-H	0001750159	315053	.W. 0001750039
CF004*		CCU BASIC ALD'S E-H	0001750160	315053	.W. 0001750039
CG001		CCU BASIC ALD'S E-H	0001750161	315053	.W. 0001750039
CK001*		CCU BASIC ALD'S E-H	0001750162	315053	.W. 0001750039
CK002*		CCU BASIC ALD'S E-H	0001750163	315053	.W. 0001750039
CK003*		CCU BASIC ALD'S E-H	0001750164	315053	.W. 0001750039
CK004		CCU BASIC ALD'S A-H	0005997605	309545	.W. 0001750039
CK005		CCU BASIC ALD'S A-H	0005997606	309545	.W. 0001750039
CK006*		CCU BASIC ALD'S E-H	0001750165	315053	.W. 0001750039
CK007*		CCU BASIC ALD'S E-H	0001750166	315053	.W. 0001750039
CL001*		CCU BASIC ALD'S E-H	0001750167	315053	.W. 0001750039
CL002		CCU BASIC ALD'S A-H	0005997610	309545	.W. 0001750039
CL003*		CCU BASIC ALD'S E-H	0001750168	315053	.W. 0001750039
CL004*		CCU BASIC ALD'S E-H	0001750169	315053	.W. 0001750039
CL005		CCU BASIC ALD'S A-H	0005997613	309545	.W. 0001750039
CM001*		CCU BASIC ALD'S E-H	0001750170	315053	.W. 0001750039
CM002*		CCU BASIC ALD'S E-H	0001750171	315053	.W. 0001750039
CM003*		CCU BASIC ALD'S E-H	0001750172	315053	.W. 0001750039
CP001*		CCU BASIC ALD'S E-H	0001750173	315053	.W. 0001750039
CP002		CCU BASIC ALD'S A-H	0005997618	309545	.W. 0001750039
CP003		CCU BASIC ALD'S A-H	0005997619	310268	.W. 0001750039
CP004*		CCU BASIC ALD'S E-H	0001750174	315053	.W. 0001750039
CP005*		CCU BASIC ALD'S E-H	0001750175	315053	.W. 0001750039
CP006		CCU BASIC ALD'S A-H	0005997622	309533	.W. 0001750039
CP007		CCU BASIC ALD'S A-H	0005997623	309533	.W. 0001750039
CQ001		CENTRAL CONTROL UNIT	0001750176	315053	.W. 0001750039
CQ002*		CCU BASIC ALD'S E-H	0001750177	315053	.W. 0001750039
CQ004*		CCU BASIC ALD'S E-H	0001750178	315053	.W. 0001750039
CQ005*		CCU BASIC ALD'S E-H	0001750179	315053	.W. 0001750039
CR001		CCU BASIC ALD'S A-H	0001857257	310268	.W. 0001750039
CR002		CCU BASIC ALD'S A-H	0001857258	310268	.W. 0001750039

VOLUME 002 MACHINE 3705- -0015984 MODEL E08 SYSTEM 0004XBW MODE BOX SHIP 82/12/30

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
CR003		CCU BASIC ALD'S A-H	0001857259	310268	.W. 0001750039
CR004		CCU BASIC ALD'S A-H	0001857260	310268	.W. 0001750039
CR005		CCU BASIC ALD'S A-H	0001857261	310268	.W. 0001750039
CR006*		CCU BASIC ALD'S E-H	0001750180	315053	.W. 0001750039
CR007		CCU BASIC ALD'S A-H	0001857263	310262	.W. 0001750039
CR008*		CCU BASIC ALD'S E-H	0001750181	315053	.W. 0001750039
CS001*		CCU BASIC ALD'S E-H	0001750182	315053	.W. 0001750039
CS002*		CCU BASIC ALD'S E-H	0001750183	315053	.W. 0001750039
CS003*		CCU BASIC ALD'S E-H	0001750184	315053	.W. 0001750039
CS004*		CCU BASIC ALD'S E-H	0001750185	315053	.W. 0001750039
CS005*		CCU BASIC ALD'S E-H	0001750186	315053	.W. 0001750039
CS006*		CCU BASIC ALD'S E-H	0001750187	315053	.W. 0001750039
CS007*		CCU BASIC ALD'S E-H	0001750188	315053	.W. 0001750039
CU001*		CCU BASIC ALD'S E-H	0001750189	315053	.W. 0001750039
CU003*		CCU BASIC ALD'S E-H	0001750190	315053	.W. 0001750039
CU004*		CCU BASIC ALD'S E-H	0001750191	315053	.W. 0001750039
CU005*		CCU BASIC ALD'S E-H	0001750192	315053	.W. 0001750039
CU006*		CCU BASIC ALD'S E-H	0001750193	315053	.W. 0001750039
CU007		CCU BASIC ALD'S A-H	0005997644	310268	.W. 0001750039
CU009		CCU BASIC ALD'S A-H	0005997645	309545	.W. 0001750039
CU010*		CCU BASIC ALD'S E-H	0001750194	315053	.W. 0001750039
CU011*		CCU BASIC ALD'S E-H	0001750195	315053	.W. 0001750039
CU012		CCU BASIC ALD'S E-H	0001750196	315053	.W. 0001750039
CU013*		CCU BASIC ALD'S E-H	0001750197	315053	.W. 0001750039
CU014*		CCU BASIC ALD'S E-H	0001750263	315053	.W. 0001750039
CU015*		CCU BASIC ALD'S E-H	0001750198	315053	.W. 0001750039
CV001*		CCU BASIC ALD'S E-H	0001750199	315053	.W. 0001750039
CV011*		CCU BASIC ALD'S E-H	0001750200	315053	.W. 0001750039
CV021		CCU BASIC ALD'S A-H	0005997654	309545	.W. 0001750039
CV031		CCU BASIC ALD'S A-H	0005997655	309545	.W. 0001750039
CV041		CCU BASIC ALD'S E-H	0001757973	315053	.W. 0001750039
CV051		CCU BASIC ALD'S A-H	0005997657	309545	.W. 0001750039
CV061*		CCU BASIC ALD'S E-H	0001750201	315053	.W. 0001750039

TOTAL PART NUMBERS THIS VOLUME 117



THIS PAGE IS FOR 3705-II ONLY.

AA001
030 SIM TO PN 5997550 EC 310268

EDGE CONN. LOC. TYPE

102	A-B4V4D05	135	A-B4V4D11
104	A-B4V4D06	137	A-B4V4D13
106	A-B4V4D07	140	A-B4V4B08
115	A-B4V4B12	142	A-B4V4B09
117	A-B4V4B13	144	A-B4V4B10
119	A-B4V4D02	163	A-B4V4B02
121	A-B4V4D03	165	A-B4V4B04
131	A-B4V4D09	169	A-B4V4B06
133	A-B4V4D10		

ADAPTER INTERFACE

E.C. HISTORY	D	MACH.27RNB	
312922 314419		FRAME 01	
DATE LAST EC 10-06-76 315053		IBM CORP.SDD P.N. 1750123	AA001 030

+ BID PROGRAM LEV 1 — AB003AB1 — 2
+ BID PROGRAM LEV 2 — AB003AB2 — 4
+ BID PROGRAM LEV 3 — AB003AB3 — 6
+ ADAPTER I-O ADDRESS DECODED — AB003AB4 — 8
+ BID CHANNEL 1 — AB004AB0 — 10
+ BID CHANNEL 2 — AB004AB1 — 12
+ CHANNEL 1 INTF A ENABLED — AB004AB2 — 14
+ CHANNEL 2 INTF A ENABLED — AB004AB3 — 16
+ CHANNEL 1 INTF B ENABLED — AB004AB4 — 18
+ CHANNEL 2 INTF B ENABLED — AB004AB5 — 20
+ BID CSB 3 — AB004AB6 — 22
+ ADBUS BYTE X BIT P — AB004AB8 — 24
+ ADBUS BYTE X BIT 6 — AB004AB9 — 26
+ ADBUS BYTE X BIT 7 — AB004AC1 — 28
+ CHANNEL 1 ENABLE INTF A POS — AP007EG4 — 30
+ CHANNEL 1 DISABLE INTF A POS — AP007EH4 — 32
+ CHANNEL 1 DISABLE INTF B POS — AP007EJ4 — 34
+ CHANNEL 1 ENABLE INTF B POS — AP007EK4 — 36
+ CHANNEL 2 ENABLE INTF A POS — AP007EL4 — 38
+ CHANNEL 2 DISABLE INTF A POS — AP007EM4 — 40
+ CHANNEL 2 DISABLE INTF B POS — AP007FM4 — 42
+ CHANNEL 2 ENABLE INTF B POS — AP007FN4 — 44
+ GATE INPUT DATA ON INBUS — CQ001CD2 — 46
+ SAMPLE OUTPUT DATA ON OUTBUS — CQ001EA2 — 48
− GATE INPUT 76 — CQ005CJ6 — 50
− GATE INPUT 77 — CQ005CL6 — 52
+ RESET — CU010GM6 — 54
+ I-O REG ADDR BIT 1 — DN001GB2 — 56
+ I-O REG ADDR BIT 2 — DN001GC2 — 58
+ I-O REG ADDR BIT 3 — DN002GB2 — 60
+ I-O REG ADDR BIT 4 — DQ001GA2 — 62
+ I-O REG ADDR BIT 5 — DQ001GB2 — 64
+ I-O REG ADDR BIT 6 — DQ001GC2 — 66
+ I-O REG ADDR BIT P — DQ002FK6 — 68
+ I-O REG ADDR BIT 7 — DQ002GB2 — 70
+ I-O REG ADDR BIT 0 — DQ002GD2 — 72

DE *EXIT* FEED A-B4V5: 50 — 102; 52 — 104; 54 — 106
DM *ENTR* A-B3A4: 16 — 108; 18 — 110; 20 — 112; 22 — 114
DD *EXIT* FEED A-B4V5: 70 — 116; 68 — 118; 48 — 120; 46 — 122
CABLE 4
DH *EXIT* FEED A-B3A4: 30 — 125; 38 — 127; 32 — 129; 40 — 131
DK *EXIT* FEED A-B3A4: 42 — 133
DC *EXIT* FEED A-B4V5: 62 — 141; 64 — 143; 66 — 145
DL *ENTR* A-B3A4: 10 — 148; 12 — 150; 14 — 152
DF *ENTR* A-B4V5: 2 — 154; 4 — 156; 6 — 158; 8 — 160
CABLE 3
DB *EXIT* FEED A-B4V5: 72 — 163; 56 — 165; 58 — 167; 60 — 169
DN *ENTR* A-B3A4: 24 — 172; 26 — 174; 28 — 176
DJ *EXIT* FEED A-B3A4: 36 — 179; 44 — 181; 34 — 183

163 + I-O REG ADDR BUS BIT 0 — AB003-DB1
165 + I-O REG ADDR BUS BIT 1 — AB003-DB3
167 + I-O REG ADDR BUS BIT 2 — AB003-DB5
169 + I-O REG ADDR BUS BIT 3 — AB003-DB7
141 + I-O REG ADDR BUS BIT 4 — AB003-DC2
143 + I-O REG ADDR BUS BIT 5 — AB003-DC4
145 + I-O REG ADDR BUS BIT 6 — AB003-DC6
116 + I-O REG ADDR BUS BIT 7 — AB003-DD1
118 + I-O REG ADDR BUS BIT P — AB003-DD3
120 + SAMPLE OUTPUT DATA ON OUTBUS — DD5 AB003
122 + GATE INPUT DATA ON INBUS — DD7 AB003
102 − GATE 1ST TEST PNTS ON INBUS — DE2 AB003
104 − GATE 2ND TEST PNTS ON INBUS — DE4 AB003
106 + RESET — AB003-DE6
154 + BID PROGRAM LEV 1 — CP005-DF1
156 + BID PROGRAM LEV 2 — CP005-DF3
158 + BID PROGRAM LEV 3 — CP005-DF5
160 + ADAPTER I-O ADDRESS DECODED — DF7 CQ001
125 + CHANNEL 1 ENABLE INTF A POS — DH1 AB004
127 + CHANNEL 2 ENABLE INTF A POS — DH3 AB004
129 + CHAN 1 DISABLE INTF A POS — DH5 AB004
131 + CHAN 2 DISABLE INTF A POS — DH7 AB004
179 + CHANNEL 1 ENABLE INTF B POS — DJ2 AB004
181 + CHANNEL 2 ENABLE INTF B POS — DJ4 AB004
183 + CHANNEL 1 DISABLE INTF B POS — DJ6 AB004
133 + CHANNEL 2 DISABLE INTF B POS — DK1 AB004
148 + BID CHANNEL 1 — CP001-DL2
150 + BID CHANNEL 2 — CP001-DL4
152 + CHANNEL 1 INTF A ENABLED — DL6 CU001
108 + CHANNEL 2 INTF A ENABLED — DM1 CU001
110 + CHANNEL 1 INTF B ENABLED — DM3 CU001
112 + CHANNEL 2 INTF B ENABLED — DM5 CU001
114 + BID CSB 3 — CP001-DM7
172 + ADBUS BYTE X BIT P — DF971-DN2
174 + ADBUS BYTE X BIT 6 — DF971-DN4
176 + ADBUS BYTE X BIT 7 — DF971-DN6

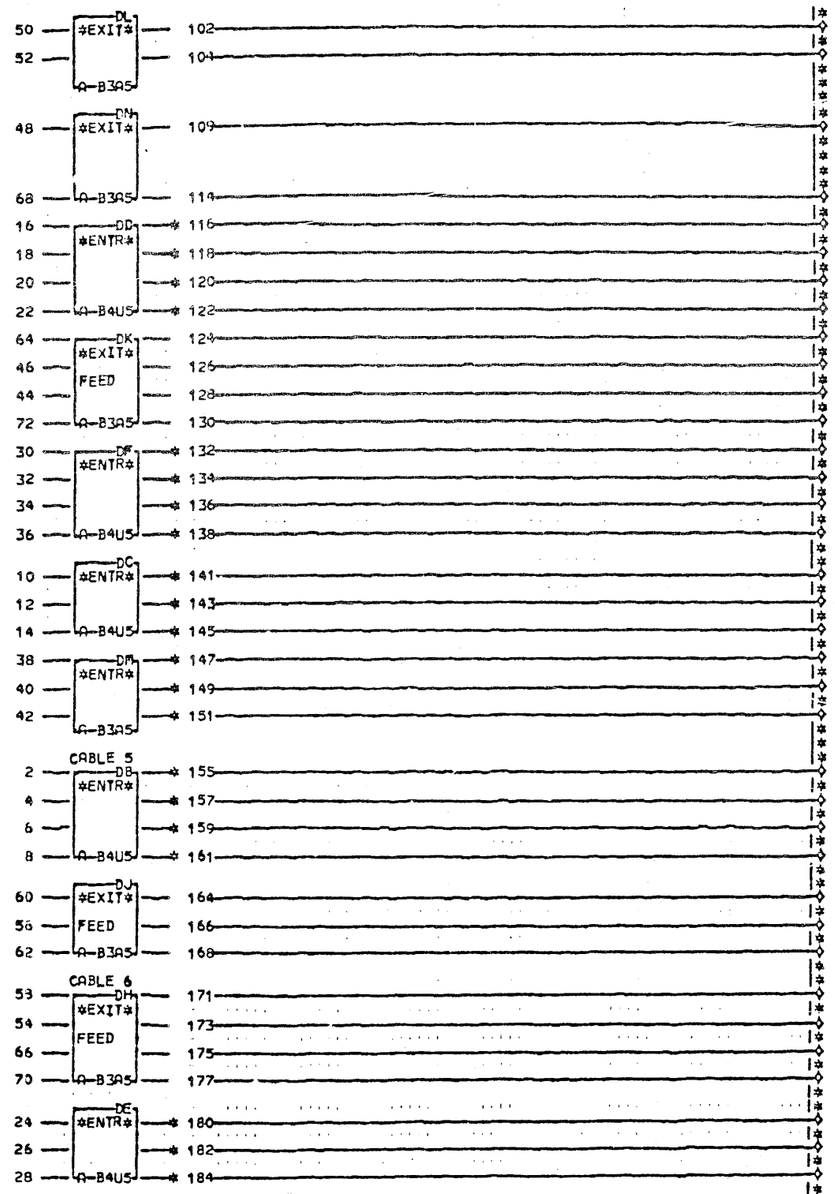
THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
108 A-B3A4D02 | 01A-B3B1B13 | 174 A-B3A4D11
110 A-B3A4D03 | 158 A-B4V5D13 | 01A-B3C1B11
112 A-B3A4D05 | 01A-B4B6C04 | 01A-B4C6B02
114 A-B3A4D09 | 01A-B3B1C13 | 176 A-B3A4D13
148 A-B3A4D06 | 160 A-B4V5D09 | 01A-B3C1B13
150 A-B3A4D07 | 01A-B4B6D02 | 01A-B4C6B04
152 A-B3A4B13 | C1A-B3B1D11
154 A-B4V5D10 | 172 A-B3A4D10
01A-B4B6B04 | 01A-B3C1A13
01A-B4C6A04

LOC. TYPE

ADAPTER INTERFACE
E.C. HISTORY: 312922, 314416, 314419
MACH. 27RNB
FRAME 01
IBM CORP. SDD
P.N. 1750124
DATE 10-06-76 LAST EC 315053

+ ADBUS BYTE 0 BIT P	AB005AB1	2
+ ADBUS BYTE 0 BIT 0	AB005AB2	4
+ ADBUS BYTE 0 BIT 1	AB005AB3	6
+ ADBUS BYTE 0 BIT 2	AB005AB4	8
+ ADBUS BYTE 0 BIT 3	AB005AB5	10
+ ADBUS BYTE 0 BIT 4	AB005AB6	12
+ ADBUS BYTE 0 BIT 5	AB005AB7	14
+ ADBUS BYTE 0 BIT 6	AB005AB8	16
+ ADBUS BYTE 0 BIT 7	AB005AB9	18
+ ADBUS BYTE 1 BIT P	AB005AC0	20
+ ADBUS BYTE 1 BIT 0	AB005AC1	22
+ ADBUS BYTE 1 BIT 1	AB005AC2	24
+ ADBUS BYTE 1 BIT 2	AB005AC3	26
+ ADBUS BYTE 1 BIT 3	AB005AC4	28
+ ADBUS BYTE 1 BIT 4	AB005AC5	30
+ ADBUS BYTE 1 BIT 5	AB005AC6	32
+ ADBUS BYTE 1 BIT 6	AB005AC7	34
+ ADBUS BYTE 1 BIT 7	AB005AC8	36
+ CHAN IPL REQUEST	AB006AB1	38
+ STORE BYTE 0	AB006AB2	40
+ STORE BYTE 1	AB006AB3	42
- T1+T2 TIME A4 BOARD	CC007HJ9	44
- T0+T1 TIME A4 BOARD	CC007HK3	46
+ GO CSB 3	CP001GG2	48
+ GO CHAN 1	CP001GK2	50
+ GO CHAN 2	CP001GM2	52
- SAMPLE C.S. DATA ON OUTBUS	CQ002BC6	54
- ADDRESS ERROR	CQ002BK6	56
+ INTERLOCK	CQ002CA6	58
- GATE C.S. DATA ON INBUS	CQ002DD6	60
- BAD DATA	CQ002DF6	62
+ SAR EVEN PARITY	CR003GC6	64
- ALLOW CHANNELS ON LINE	CU006GC6	66
+ POWER ON RESET	CU010AJ6	68
- NOT INITIALIZED	CU010AJ2	70
+ POR OR RESET SW	CU010GK6	72



155 + ADBUS BIT 0.P	DG971-DB1	
157 + ADBUS BIT 0.0	DG971-DB3	
159 + ADBUS BIT 0.1	DG971-DB5	
161 + ADBUS BIT 0.2	DH011-DB7	
141 + ADBUS BIT 0.3	DH011-DC2	
143 + ADBUS BIT 0.4	DH011-DC4	
145 + ADBUS BIT 0.5	DJ011-DC6	
116 + ADBUS BIT 0.6	DJ011-DD1	
118 + ADBUS BIT 0.7	DJ011-DD3	
120 + ADBUS BIT 1.P	DK971-DD5	
122 + ADBUS BIT 1.0	DK971-DD7	
180 + ADBUS BIT 1.1	DK971-DE2	
182 + ADBUS BIT 1.2	DL001-DE4	
184 + ADBUS BIT 1.3	DL001-DE6	
132 + ADBUS BIT 1.4	DL001-DF1	
134 + ADBUS BIT 1.5	DM001-DF3	
136 + ADBUS BIT 1.6	DM001-DF5	
138 + ADBUS BIT 1.7	DM001-DF7	
171 + INTERLOCK	AB006-DH1	
173 - SAMPLE C.S. DATA ON OUTBUS (AB006)	DH3	
175 - ALLOW CHANNELS ON LINE	AB006-DH5	
177 - NOT INITIALIZED	AB006-DH7	
164 - GATE C.S. DATA ON INBUS	AB006-DJ2	
166 - ADDRESS ERROR	AB006-DJ4	
168 - BAD DATA	AB006-DJ6	
124 + SAR EVEN PARITY	AB006-DK1	
126 + T2+T3 TIME A4 BOARD	AB006-DK3	
128 + T3+T0 TIME A4 BOARD	AB006-DK5	
130 + POR OR RESET SW	AB006-DK7	
102 + GO CHANNEL 1	AB006-DL2	
104 + GO CHANNEL 2	AB006-DL4	
147 + CHAN IPL REQUEST	CU010-DM1	
149 + STORE BYTE 0	CQ001-DM3	
151 + STORE BYTE 1	CQ001-DM5	
109 + GO CSB 3	AB006-DN2	
114 + POWER ON RESET	AB006-DN7	

THIS PAGE IS FOR 3705-II ONLY.

AA003
030 SIM TO PN 5997552 EC 314416

EDGE CONN. 143 A-B4U5B09 182 A-B4U5D06
116 A-B4U5B12 145 A-B4U5B10 184 A-B4U5D07
118 A-B4U5B13 147 A-B3A5D10
120 A-B4U5D02 149 A-B3A5D11
122 A-B4U5D03 151 A-B3A5D13
132 A-B4U5D09 155 A-B4U5B02
134 A-B4U5D10 157 A-B4U5B04
136 A-B4U5D11 159 A-B4U5B05
138 A-B4U5D13 161 A-B4U5B06
141 A-B4U5B08 180 A-B4U5D05

LOC. TYPE

ADAPTER INTERFACE

E.C.-HISTORY	MACH.27RNB	
312922 314419	FRAME 01	
	IBM CORP.SDD	AA003
DATE LAST EC 10-06-76 313053	P.N. 1750125	030

+ CSB 1 WANTS A PRI REG — AB008AB1 — 2
+ CSB 2 WANTS A PRI REG — AB008AB2 — 4
+ CSB 3 WANTS A PRI REG — AB008AB3 — 6
+ CSB 4 WANTS A PRI REG — AB008AB4 — 8
+ CSB 1 TO COMMON BIT A — AB008AB5 — 10
+ CSB 2 TO COMMON BIT A — AB008AB6 — 12
+ CSB 3 TO COMMON BIT A — AB008AB7 — 14
+ CSB 4 TO COMMON BIT A — AB008AB8 — 16
+ CSB 1 TO COMMON BIT B — AB008AB9 — 18
+ CSB 2 TO COMMON BIT B — AB008AC1 — 20
+ CSB 3 TO COMMON BIT B — AB008AC2 — 22
+ CSB 4 TO COMMON BIT B — AB008AC3 — 24
- T1+T2 TIME A3 BOARD — CC007HJ0 — 26
- T0+T1 TIME A3 BOARD — CC007HK2 — 28
- FETCH BUFFER — CX001GJ6 — 30
- PRI REG AVAILABLE PARITY — CX002GD6 — 32
- PRI REG 00 AVAILABLE — CX002GF6 — 34
- PRI REG 01 AVAILABLE — CX002GH6 — 36
- PRI REG 10 AVAILABLE — CX002GK6 — 38
- PRI REG 11 AVAILABLE — CX002GM6 — 40
- IDENTIFY CSB 1 — CX003FD4 — 42
- IDENTIFY CSB 2 OR 3 OR 4 — CX003FE4 — 44
+ LINEADBUS BIT 6 — CX010BH4 — 46
+ LINEADBUS BIT 2 — CX010DB4 — 48
+ LINEADBUS BIT 4 — CX010DE4 — 50
+ LINEADBUS BIT 3 — CX010ED4 — 52
+ LINEADBUS BIT 5 — CX010EF4 — 54
+ LINEADBUS BIT 0 — CX010FA6 — 56
+ LINEADBUS BIT 7 — CX010FG4 — 58
+ LINEADBUS BIT 8 — CX010FK4 — 60
+ LINEADBUS BIT P — CX010FN4 — 62
+ LINEADBUS BIT 1 — CX010GB6 — 64
+ LINEADBUS BIT 9 — CX010GL4 — 66
- SYNC CSB CLOCKS — CX011GN6 — 68

EDGE CONN. 170 A-B3A3B08
108 A-B3A3B12 172 A-B3A3B09
110 A-B3A3B13 174 A-B3A3B10
112 A-B3A3D02
114 A-B3A3D03
134 A-B3A3D05
136 A-B3A3D06
138 A-B3A3D07
148 A-B3A3D09
150 A-B3A3D10

AA004
000

50 — DC *EXIT* — 102
54 — FEED — 104
46 — 106
8 — DK *ENTR* — 108
10 — 110
12 — 112
14 — 114

CABLE 8

42 — DH *EXIT* — 117
44 — FEED — 119
44 — 121
44 — 123
38 — DF *EXIT* — 125
28 — FEED — 127
26 — 129
30 — 131
16 — DL *ENTR* — 134
18 — 136
20 — 138
58 — DD *EXIT* — 140
60 — FEED — 142
66 — 144
62 — 146
22 — DM *ENTR* — 148
24 — 150
68 — DE *EXIT* — 156
34 — FEED — 158
36 — 160
40 — DN *EXIT* — 163
32 — 165
2 — DJ *ENTR* — 170
4 — 172
6 — 174

CABLE 7

56 — DE *EXIT* — 177
64 — FEED — 179
48 — 181
52 — 183

LOC. TYPE

C00 AA004

177 + LINEADBUS BIT 0 — AB007-DB1
179 + LINEADBUS BIT 1 — AB007-DB3
181 + LINEADBUS BIT 2 — AB007-DB5
183 + LINEADBUS BIT 3 — AB007-DB7
102 + LINEADBUS BIT 4 — AB007-DC2
104 + LINEADBUS BIT 5 — AB007-DC4
106 + LINEADBUS BIT 6 — AB007-DC6
140 + LINEADBUS BIT 7 — AB007-DD1
142 + LINEADBUS BIT 8 — AB007-DD3
144 + LINEADBUS BIT 9 — AB007-DD5
146 + LINEADBUS BIT P — AB007-DD7
156 - SYNC CSB CLOCKS — AB007-DE2
158 - PRI REG 1 AVAILABLE — AB007-DE4
160 - PRI REG 2 AVAILABLE — AB007-DE6
125 - PRI REG 3 AVAILABLE — AB007-DF1
127 + T2+T3 TIME A3 BOARD — AB007-DF3
129 + T3+T0 TIME A3 BOARD — AB007-DF5
131 - FETCH BUFFER — AB007-DF7
117 - IDENTIFY CSB 1 — AB008-DH1
119 - IDENTIFY CSB 2 — AB008-DH3
121 - IDENTIFY CSB 3 — AB008-DH5
123 - IDENTIFY CSB 4 — AB008-DH7
170 + CSB 00 WANTS A PRI REG — CX003-DJ2
172 + CSB 01 WANTS A PRI REG — CX003-DJ4
174 + CSB 02 WANTS A PRI REG — CX003-DJ6
108 + CSB 03 WANTS A PRI REG — CX003-DK1
110 + CSB 1 TO COMMON BIT A — CX004 CX007 — DK3
112 + CSB 2 TO COMMON BIT A — CX004 CX007 — DK5
114 + CSB 3 TO COMMON BIT A — CX004 CX007 — DK7
134 + CSB 4 TO COMMON BIT A — CX004 CX007 — DL2
136 + CSB 1 TO COMMON BIT B — CX004 CX007 — DL4
138 + CSB 2 TO COMMON BIT B — CX004 CX007 — DL6
148 + CSB 3 TO COMMON BIT B — CX004 CX007 — DM1
150 + CSB 4 TO COMMON BIT B — CX004 CX007 — DM3
163 - PRI REG 4 AVAILABLE — AB008-DN2
165 - PRI REG AVAILABLE PARITY — AB008 — DN4

ADAPTER INTERFACE

E.C. HISTORY — B
309521C
309538

MACH.27AAB
FRAME 01
IBM CORP.SDD
P.N. 5997553

DATE LAST EC
04-19-72 309545

AA004
000

030 AA005

DDRESS COMPARE TEST PIN——CU004FF2— 2—

)IAG SCOPE SYNC POINT 1——CU015CL2— 12—

DIAG SCOPE SYNC POINT 2——CU015CN2— 22—

12 —
22 —
2 —
5A
EXIT

THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE

ADAPTER INTERFACE
EXIT TRI-LEADS
—E.C.—HISTORY——D—BACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD AA005
DATE LAST EC
10-05-76 315053
P.N. 1750126 030

A005
030 SIM TO PN 5997554 EC 314416

620-0133-0 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

1785378 **B**

CABLE 1

01A-B4 SOCKET	SINK	3705 CCU LINE NAME	PIN	SOCKET	CS A3 BOARD	SOCKET	SOCKET	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD	SOCKET
ENTRY	DG971	+INBUS BYTE 0 BIT P	B02						
	DG971	+INBUS BYTE 0 BIT 0	B04						
	DG971	+INBUS BYTE 0 BIT 1	B05		SEE NOTE 1			SEE NOTE 1	
	DH011	+INBUS BYTE 0 BIT 2	B06						
	DH011	+INBUS BYTE 0 BIT 3	B08						
	DH011	+INBUS BYTE 0 BIT 4	B09						
	DJ011	+INBUS BYTE 0 BIT 5	B10						
	DJ011	+INBUS BYTE 0 BIT 6	B12						
V4	DJ011	+INBUS BYTE 0 BIT 7	B13	V3	TYPE 2 OR 3 CS (ONLY)	Z4	A2	TYPE 1 CA/CS, TYPE 2,3, OR 4 CA, REMOTE OR "DUMMY BOARD"	A4
	DK971	+INBUS BYTE 1 BIT P	D02						
	DK971	+INBUS BYTE 1 BIT 0	D03						
	DK971	+INBUS BYTE 1 BIT 1	D05						
	DL001	+INBUS BYTE 1 BIT 2	D06						
	DL001	+INBUS BYTE 1 BIT 3	D07						
	DL001	+INBUS BYTE 1 BIT 4	D09						
	DM001	+INBUS BYTE 1 BIT 5	D10						
	DM001	+INBUS BYTE 1 BIT 6	D11						
	DM001	+INBUS BYTE 1 BIT 7	D13						

1) TYPE 1 CA/CS INTERFACE CABLE PAGE RA011
2) TYPE 2 CA INTERFACE CABLE PAGE QA011
3) TYPE 3 CA INTERFACE CABLE PAGE SA011
4) TYPE 4 CA INTERFACE CABLE PAGE PA011
5) TYPE 2 CS INTERFACE CABLE PAGE TA021
6) TYPE 3 CS INTERFACE CABLE PAGE TD010
7) MOD 1 REMOTE INTERFACE CABLE PAGE GA011
8) MOD 11 REMOTE INTERFACE CABLE PAGE GE011

NOTE

1 ONLY THE ENTRY AND EXIT SOCKETS ARE SHOWN FOR THE ADAPTERS. FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9 SEE AB010.

IBM

NAME	DATE	CHANGE NO
DESIGN		
DETAIL C & D NOV 75		
CHECK		
APPRO		
RED	MAR 72	309538
	OCT 75	312922
	DEC76	315621

SHT OF

CLASSIFICATION

MUST CONFORM TO ENG SPEC

DEVELOPMENT NO

LOGIC PG NO AB001

1785378 B

PART NO 1785378

LOGIC PG NO AB001

CABLE 2

01A-B4 SOCKET	3705 CCU SOURCE	LINE NAME	PIN	CS SOCKET	CS A3 BOARD	CS SOCKET	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD SOCKET	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD SOCKET
EXIT	DP992AD2	+OUTBUS BIT 0.P	B02						
	DN001BH2	+OUTBUS BIT 0.0	B04						
	DN001BK2	+OUTBUS BIT 0.1	B05		SEE NOTE [1]			SEE NOTE [1]	
	DH014GC2	+OUTBUS BIT 0.2	B06						
	DH014GG2	+OUTBUS BIT 0.3	B08						
	DH014GL2	+OUTBUS BIT 0.4	B09						
	DJ014GC2	+OUTBUS BIT 0.5	B10					TYPE 1 CA/CS, TYPE 2,3, OR 4 CA, REMOTE OR "DUMMY BOARD"	
U4	DJ014GG2	+OUTBUS BIT 0.6	B12	V4	TYPE 2 OR 3 CS (ONLY)	Z5	B2		B4 NOTE [2]
	DJ014GL2	+OUTBUS BIT 0.7	B13						
	DR992AD2	+OUTBUS BIT 1.P	D02						
	DQ001BH2	+OUTBUS BIT 1.0	D03						
	DQ001BK2	+OUTBUS BIT 1.1	D05						
	DL004GC2	+OUTBUS BIT 1.2	D06						
	DL004GG2	+OUTBUS BIT 1.3	D07						
	DL004GL2	+OUTBUS BIT 1.4	D09						
	DM004GC2	+OUTBUS BIT 1.5	D10						
	DM004GG2	+OUTBUS BIT 1.6	D11						
	DM004GL2	+OUTBUS BIT 1.7	D13						

1) TYPE 1 CA/CS INTERFACE CABLE PAGE RA011
2) TYPE 2 CA INTERFACE CABLE PAGE QA011
3) TYPE 3 CA INTERFACE CABLE PAGE SA011
4) TYPE 4 CA INTERFACE CABLE PAGE PA011
5) TYPE 2 CS INTERFACE CABLE PAGE TA021
6) TYPE 3 CS INTERFACE CABLE PAGE TD020
7) MOD I REMOTE INTERFACE CABLE PAGE GA011
8) MOD II REMOTE INTERFACE CABLE PAGE GE011

NOTES

[1] ONLY THE ENTRY AND EXIT SOCKETS ARE SHOWN FOR THE ADAPTERS. FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9 SEE AB010.

[2] TERMINATED IF LAST A4 BOARD.

LOGIC PAGE AB002

1785379

INTERNATIONAL BUSINESS MACHINES CORP.

	NAME	DATE	CHANGE NO.	DATE	CHANGE NO.
DESIGN		MAR72	309538		
DETAIL VR MAR72		OCT 75	312922		
CHECK		DEC76	315621		
APPRO					

MODEL

DRAW

CHECK

NOTE X PRINT TO ENG. SPEC. NO.

DEVELOPMENT NO.

1785379

FORM 820-9021-0 IBM 3790532109

GRAPHIC CONTROLS CORPORATION STOCK NO. N690T 14693

1785380

CABLE 3

3705									
01A-84 SOCKET	SOURCE/SINK	CCU LINE NAME	PIN	SOCKET	CS A3 BOARD	SOCKET	ENTRY SOCKET	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD	SOCKET
EXIT	DQ002GD2	+I-O REG ADDR BIT 0	B02		SEE NOTE 1			SEE NOTE 1	
	DN001GB2	+I-O REG ADDR BIT 1	B04						
	DN001GC2	+I-O REG ADDR BIT 2	B05						
V5	DN002GB2	+I-O REG ADDR BIT 3	B06						
	DQ001GA2	+I-O REG ADDR BIT 4	B08						
	DQ001GB2	+I-O REG ADDR BIT 5	B09						
	DQ001GC2	+I-O REG ADDR BIT 6	B10	V5	TYPE 2 OR 3 CS (ONLY)	Z6	C2	TYPE 1 CA/CS, TYPE 2,3, OR 4 CA, REMOTE OR "DUMMY BOARD"	C4 NOTE 2
	DQ002GB2	+I-O REG ADDR BIT 7	B12						
	DQ002FK6	+I-O REG ADDR BIT P	B13						
	CQ001EA2	+SAMPLE OUTPUT DATA ON OUTBUS	D02						
	CQ001CD2	+GATE INPUT DATA ON INBUS	D03						
	CQ005CJ6	-GATE INPUT 76	D05						
	CQ005CL6	-GATE INPUT 77	D06						
	CU010GM6	+RESET	D07						
ENTRY	AA002DF1	+BID PROGRAM LEVEL 1	D10						
	AA002DF3	+BID PROGRAM LEVEL 2	D11			NOTE 3			NOTE 3
	AA002DF5	+BID PROGRAM LEVEL 3	D13		OE			OE	
	AA002DF7	+ADAPTER I-0 ADDRESS DECODED	D09						

1) TYPE 1 CA/CS INTERFACE CABLE PAGE RA012
2) TYPE 2 CA INTERFACE CABLE PAGE QA012
3) TYPE 3 CA INTERFACE CABLE PAGE SA012
4) TYPE 4 CA INTERFACE CABLE PAGE PA012
5) TYPE 2 CS INTERFACE CABLE PAGE TA031
6) TYPE 3 CS INTERFACE CABLE PAGE TD020
7) MOD I REMOTE INTERFACE CABLE PAGE GA012
8) MOD II REMOTE INTERFACE CABLE PAGE GE012

NOTES

1 ONLY THE ENTRY AND EXIT SOCKETS ARE SHOWN FOR THE ADAPTERS. FOR THE PHYSICAL PATH OF THE CABLING FOR CACLES 1 THRU 9 SEE AB010.

2 TERMINATED AT LAST A4 BOARD

3 JUMPER A4C2D09 TO A4C4D09 WHEN A "DUMMY BOARD" IS INSTALLED IN THE A4 BOARD POSITION. THIS JUMPER ALLOWS THE + ADAPTER I/O ADDRESS DECODED SIGNAL TO PROPAGATE STRAIGHT THROUGH THE "DUMMY BOARD".

INTERNATIONAL BUSINESS MACHINES CORP.

NAME		DATE	CHANGE NO.
DESIGN		MAR72	309538
DETAIL	VR MAR72	SEP72	309940
CHECK		OCT 75	312922
APPRO		DEC76	315621

MODEL / DRAW / CHECK

NOTE X PRINT TO ENG. SPEC. NO.

DEVELOPMENT NO.

CABLE 4

01A-B3 SOCKET	3705 CCU SOURCE/SINK	3705 CCU LINE NAME	PIN	SOCKET	CS A3 BOARD	SOCKET	SOCKET	TYPE 1 CA/CS A4 BOARD, TYPE 2, 3, OR 4 CA A4 BOARD, TYPE 4 CA B1 OR B4 BOARD, (SEE NOTE 1) REMOTE B1 OR A4 BOARD OR "DUMMY A4 BOARD"	SOCKET
EXIT	AP007EG4	+CHANNEL 1 ENABLE INTF A POS	B02		SEE NOTE 1			NOTES 2 3 4	
	AP007EL4	+CHANNEL 2 ENABLE INTF A POS	B04					NOTES 2 3 4	
	AP007EH4	+CHANNEL 1 DISABLE INTF A POS	B05					NOTES 2 3 4	
	AP007EM4	+CHANNEL 2 DISABLE INTF A POS	B06					NOTES 2 3 4	
A4	AP007EK4	+CHANNEL 1 ENABLE INTF B POS	B08	B3	TYPE 3 CS (ONLY)	Z3	C3	NOTES 2 3 4	C5
	AP007EN4	+CHANNEL 2 ENABLE INTF B POS	B09					NOTES 2 3 4	
	AP007EJ4	+CHANNEL 1 DISABLE INTF B POS	B10					NOTES 2 3 4	
	AP007FM4	+CHANNEL 2 DISABLE INTF B POS	B12						
ENTRY	AA002DL2	+BID CHANNEL 1	D06					NOTES 2 3 4	
	AA002DL4	+BID CHANNEL 2	D07					NOTES 2 3 4	
	AA002DL6	+CHANNEL 1 INTF A ENABLED	B13					NOTES 2 3 4	
	AA002DM1	+CHANNEL 2 INTF A ENABLED	D02						
A4	AA002DM3	+CHANNEL 1 INTF B ENABLED	D03	B3		Z3	C3		C5
	AA002DM5	+CHANNEL 2 INTF B ENABLED	D05						
	AA002DM7	+BID CSR - 3	D09						
	AA002DN2	+ADBUS BYTE X BIT P	D10						
	AA002DN4	+ADBUS BYTE X BIT 6	D11						
	AA002DN6	+ADBUS BYTE X BIT 7	D13						

1) TYPE 1 CA/CS INTERFACE CABLE PAGE RA012 (SEE NOTE 2)
2) TYPE 2 CA INTERFACE CABLE PAGE QA012
3) TYPE 3 CA INTERFACE CABLE PAGE SA012
4) TYPE 4 CA INTERFACE CABLE PAGE PA012 (SEE NOTE 4)
5) TYPE 3 CS INTERFACE CABLE PAGE TD022
6) MOD 1 REMOTE INTERFACE CABLE PAGE GA012 (SEE NOTE 3)
7) MOD 11 REMOTE INTERFACE CABLE PAGE GE012 (SEE NOTE 3)

NOTES

1. ONLY THE ENTRY AND EXIT SOCKETS ARE SHOWN FOR THE ADAPTERS. FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9 SEE AB010.
2. THE ENABLE, DISABLE AND ENABLED CHAN 1 INTF A OR B SIGNALS AND THE BID CHANNEL 1 SIGNAL STOP FOR THE TYPE 1 CA. (THEY ARE NOT WIRED TO SOCKET C5)
3. THE ENABLE, DISABLE AND ENABLED SIGNALS AND BID CHANNEL 1 AND 2 SIGNALS ARE NOT CROSSED ON A REMOTE BOARD. THE MOD 11 REMOTE USES THE ENABLE AND ENABLED SIGNALS FOR ITS INTERNAL LOGIC.
4. IF MORE THAN TWO CA-4'S ARE INSTALLED THE ENABLE, DISABLE AND ENABLED WIRING IS ALTERED FROM WHAT IS DEPICTED ABOVE (SEE PA061 PAGES 1, 2 AND 3).

   FOR THE CA-4 BID CHANNEL 1 OR 2 SIGNAL PROPAGATION SEE PA054.

LOGIC PAGE AB004

1785382

CABLE 5

01A-B4 SOCKET	SINK	3705 CCU LINE NAME	PIN	SOCKET	CS A3 BOARD	SOCKET	SOCKET	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD	SOCKET
ENTRY	AA003DB1	+ADBUS BIT 0.P	B02						
	AA003DB3	+ADBUS BIT 0.0	B04						
	AA003DB5	+ADBUS BIT 0.1	B05		SEE NOTE [1]			SEE NOTE [1]	
	AA003DB7	+ADBUS BIT 0.2	B06						
	AA003DC2	+ADBUS BIT 0.3	B08						
	AA003DC4	+ADBUS BIT 0.4	B09						
	AA003DC6	+ADBUS BIT 0.5	B10						
	AA003DD1	+ADBUS BIT 0.6	B12					TYPE 1 CA/CS,	
U5	AA003DD3	+ADBUS BIT 0.7	B13	B5	TYPE 3 CS (ONLY)	Z1	A3	TYPE 2,3 OR 4 CA,	A5
	AA003DD5	+ADBUS BIT 1.P	D02						
	AA003DD7	+ADBUS BIT 1.0	D03					REMOTE OR	
	AA003DE2	+ADBUS BIT 1.1	D05					"DUMMY BOARD"	
	AA003DE4	+ADBUS BIT 1.2	D06						
	AA003DE6	+ADBUS BIT 1.3	D07						
	AA003DF1	+ADBUS BIT 1.4	D09						
	AA003DF3	+ADBUS BIT 1.5	D10						
	AA003DF5	+ADBUS BIT 1.6	D11						
	AA003DF7	+ADBUS BIT 1.7	D13						

1) TYPE 1 CA/CS INTERFACE CABLE PAGE RA013
2) TYPE 2 CA INTERFACE CABLE PAGE QA013
3) TYPE 3 CA INTERFACE CABLE PAGE SA013
4) TYPE 4 CA INTERFACE CABLE PAGE PA013
5) TYPE 3 CS INTERFACE CABLE PAGE TD024
6) MOD 1 REMOTE INTERFACE CABLE PAGE GA013
7) MOD 11 REMOTE INTERFACE CABLE PAGE GE013

NOTE

[1] ONLY THE ENTRY AND EXIT SOCKETS ARE SHOWN FOR THE ADAPTERS. FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9 SEE AB010.

LOGIC PAGE AB005

1785382

INTERNATIONAL BUSINESS MACHINES CORP.

NAME		DATE	CHANGE NO.	DATE	CHANGE NO.
DESIGN		MAR72	309538		
DETAIL	VR MAR72	OCT 75	312922		
CHECK		DEC76	315621		
APPRO					
MODEL					
DRAW					
CHECK					

NOTE X PRINT TO ENG. SPEC. NO.

DEVELOPMENT NO.

1785382

CABLE 6

01A-B3 SOCKET	SOURCE/SINK	3705 CCU LINE NAME	PIN
EXIT	CQ002CA6	+INTERLOCK	B06
	CQ002BC6	-SAMPLE C.S. DATA ON OUTBUS	D06
	CU006GC6	-ALLOW CHANNELS ON LINE	B02
	CU010FJ2	-NOT INITIALIZED	B04
	CQ002DD6	-GATE C.S. DATA ON INBUS	D05
A5	CQ002BK6	-ADDRESS ERROR	D02
	CQ002DF6	-BAD DATA	B13
	CK003GC6	-SAR EVEN PARITY	D03
	CC007HK3	-T0 + T1 TIME A4 BOARD	B08
	CC007HJ9	-T1 + T2 TIME A4 BOARD	B09
	CU010GK6	+POR OR RESET SW	B05
	CP001GK2	+GO CHAN 1	B10
	CP001GM2	+GO CHAN 2	B12
	AA003DN7	+POWER ON RESET	D07
	AA003DN2	+GO CSB - 3	D09
ENTRY	AA003DM1	+CHAN IPL REQUEST	D10
A5	AA003DM3	+STORE BYTE 0	D11
	AA003DM5	+STORE BYTE 1	D13

SOCKET	CS A3 BOARD	SOCKET
B4	TYPE 3 CS (ONLY)	Z2

SOCKET	CA A4, B1 OR B4 BOARD OR REMOTE B1 OR A4 BOARD	SOCKET
B3	TYPE 1 CA/CS, TYPE 2,3, OR 4 CA, REMOTE OR "DUMMY BOARD" NOTES [3] [4]	B5 NOTE [2]

SEE NOTE [1]

SEE NOTE [1]

1) TYPE 1 CA/CS INTERFACE CABLE PAGE RA013
2) TYPE 2 CA INTERFACE CABLE PAGE QA013
3) TYPE 3 CA INTERFACE CABLE PAGE SA013
4) TYPE 4 CA INTERFACE CABLE PAGE PA013 (SEE NOTE [4])
5) TYPE 3 CS INTERFACE CABLE PAGE TD026
6) MOD 1 REMOTE INTERFACE CABLE PAGE GA013 (SEE NOTE [3])
7) MOD II REMOTE INTERFACE CABLE PAGE GE013 (SEE NOTE [3])

NOTES

[1] ONLY THE ENTRY AND EXIT SOCKETS ARE SHOWN FOR THE ADAPTERS. FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9 SEE AB010.

[2] TERMINATED IF LAST A4 BOARD.

[3] THE GO CHANNEL 1 OR 2 SIGNALS ARE NOT CROSSED ON THE REMOTE BOARD.

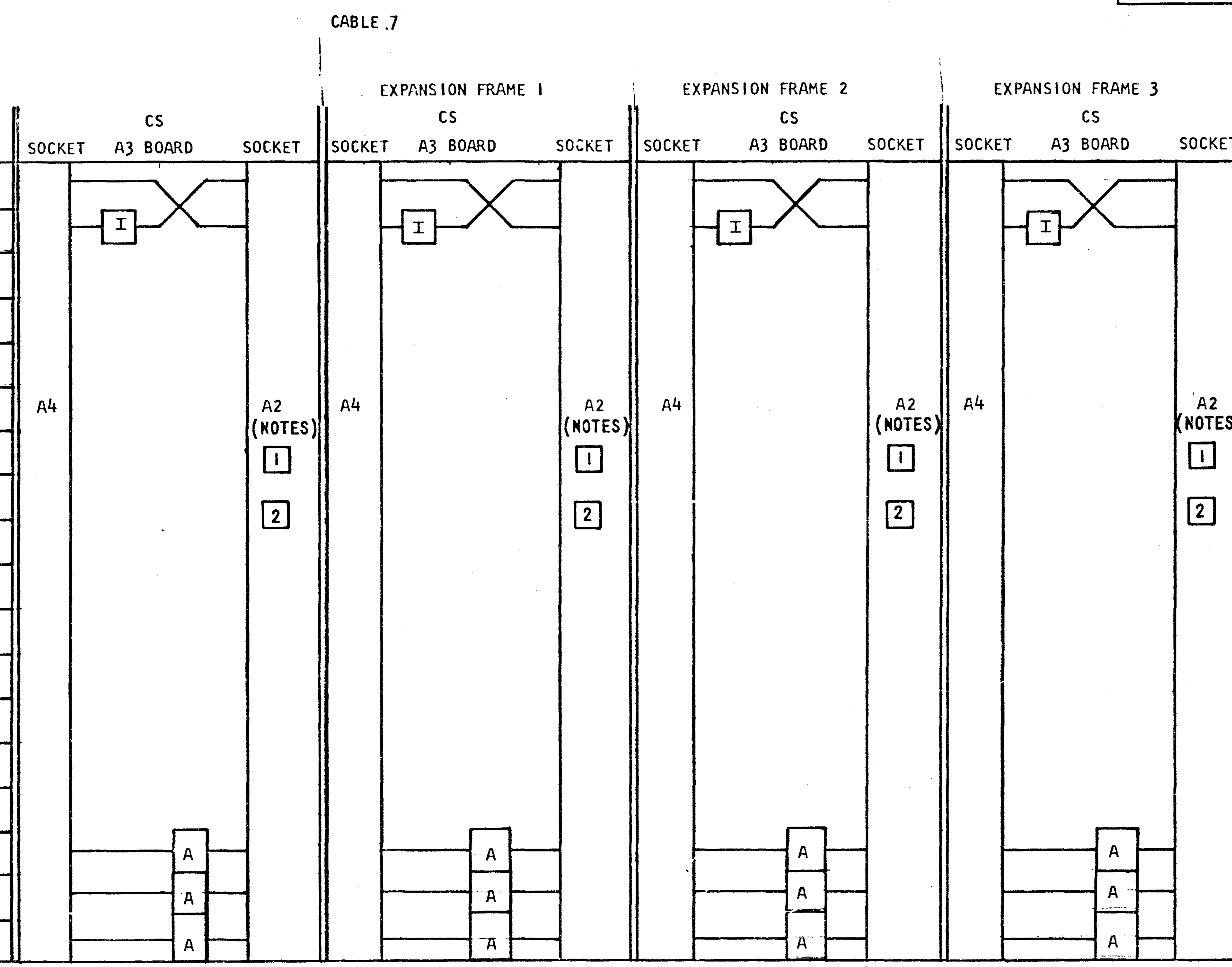
[4] FOR THE CA-4 GO CHANNEL 1 OR 2 SIGNAL PROPAGATION SEE PA054

LOGIC PAGE
AB006

1785383

3705

01A-B3 SOCKET	SOURCE AA004	CCU LINE NAME	PIN
EXIT	DB1	+LINE ADBUS BIT 0	B02
	DB3	+LINE ADBUS BIT 1	B04
	DB5	+LINE ADBUS BIT 2	B05
	DB7	+LINE ADBUS BIT 3	B06
	DC2	+LINE ADBUS BIT 4	B08
A2	DC4	+LINE ADBUS BIT 5	B09
	DC6	+LINE ADBUS BIT 6	B10
	DD1	+LINE ADBUS BIT 7	B12
	DD3	+LINE ADBUS BIT 8	B13
	DD5	+LINE ADBUS BIT 9	D02
	DD7	+LINE ADBUS BIT P	D03
	DE2	-SYNC CSB CLOCKS	D05
	DF7	-FETCH BUFFER	D06
	DF3	+T2 OR T3 TIME	D07
	DF5	+T3 OR T0 TIME	D09
	DE4	-PRI REG 00 AVAILABLE	D10
	DE6	-PRI REG 01 AVAILABLE	D11
	DF1	-PRI REG 02 AVAILABLE	D13



CS TYPE 2 INTERFACE CABLE LOGIC TA041 (ENTRY) TA061 (EXIT)
CS TYPE 3 INTERFACE CABLE LOGIC TD030 (ENTRY) TD050 (EXIT)

NOTE [1]: TERMINATOR CARD IF LAST CS INSTALLED

NOTE [2]: SEE AB010 FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9

LOGIC PAGE AB007

1785384

INTERNATIONAL BUSINESS MACHINES CORP.

NAME		MODEL
DESIGN		
DETAIL	VR MAR72	
CHECK		DRAW
APPRO		CHECK

DATE	CHANGE NO.	DATE	CHANGE NO.
MAR72	309538		
SEP72	309940		
OCT 75	312922		
DEC76	315621		

NOTE
X PRINT TO ENG. SPEC. NO.

DEVELOPMENT NO.

1785384

620-0133-0 MRO# 780522203 VERTICAL ELECTRICAL FORMAT

1785385 B

CABLE 8
(SEE NOTE [1])

01A-B3 SOCKET	SOURCE/SINK	CCU LINE NAME	PIN	3705 SOCKET	CS A3 BOARD	3705 SOCKET	EXPANSION FRAME ONE SOCKET	CS A3 BOARD	EXPANSION FRAME ONE SOCKET	EXPANSION FRAME TWO SOCKET	CS A3 BOARD	EXPANSION FRAME TWO SOCKET	EXPANSION FRAME THREE SOCKET	CS A3 BOARD	EXPANSION FRAME THREE SOCKET
EXIT A3	AA004DH1	-IDENTITY CSB 1	B02	A5		A3	A5		A3	A5		A3	A5		A3
	AA004DH3	-IDENTITY CSB 2	B04												
	AA004DH5	-IDENTITY CSB 3	B05												
	AA004DH7	-IDENTITY CSB 4	B06												
	AA004DN2	-PRI REG 03 AVAILABLE	D11		A		A			A			A		
	AA004DN4	-PRI REG AVAILABLE PARITY	D13		A		A			A			A		
ENTRY A3	CX003DJ2	+CSB 1 WANTS A PRI REG	B08	A5	A	A3	A5	A	A3	A5	A	A3	A5	A	A3
	CX003DJ4	+CSB 2 WANTS A PRI REG	B09												
	CX003DJ6	+CSB 3 WANTS A PRI REG	B10												
	CX003DK1	+CSB 4 WANTS A PRI REG	B12												
	CX004DK3	+CSB 1 TO COMMON BIT A	B13		A			A			A			A	
	CX004DK5	+CSB 2 TO COMMON BIT A	D02												
	CX004DK7	+CSB 3 TO COMMON BIT A	D03												
	CX004DL2	+CSB 4 TO COMMON BIT A	D05												
	CX004DL4	+CSB 1 TO COMMON BIT B	D06		A			A			A			A	
	CX004DL6	+CSB 2 TO COMMON BIT B	D07												
	CX004DM1	+CSB 3 TO COMMON BIT B	D09												
	CX004DM3	+CSB 4 TO COMMON BIT B	D10												

TYPE 2 CS INTERFACE PAGES TA041 (ENTRY) TA061 (EXIT)
TYPE 3 CS INTERFACE PAGES TD030 (ENTRY) TD050 (EXIT)

NOTE

[1] SEE AB010 FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9.

PART NO 1785385

LOGIC PG NO AB008

	NAME	DATE
DESIGN		
DETAIL	C & D	NOV 75
CHECK		
APPRO		

CLASSIFICATION SHT OF

MUST CONFORM TO ENG SPEC

DEVELOPMENT NO	DATE	CHANGE NO
DE676	MAR 72	309538
315621	SEPT 72	309940
312922	OCT 75	RED

LOGIC PG NO AB008

1785385 B

CABLE 9 (MOD II ONLY)

01A-B4 SOCKET	SINK	3705 CCU LINE NAME	PIN	SOCKET	REMOTE BI BOARD	SOCKET
ENTRY					SEE NOTE [1]	
A5				Y3	MOD II REMOTE (ONLY)	Y2
	DW001	+ SELECT ALT ROS [2]	D11			

1) MOD II REMOTE INTERFACE CABLE PAGE GE014

NOTES

[1] SEE AB010 FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9.

[2] CCU PAGE DW001 ONLY REFLECTS THE + SELECT ALT ROS SIGNAL IF THE REMOTE IS INSTALLED IN THE BI BOARD.

IBM

NAME	DESIGN	DETAIL	CHECK	APPRO
		TS		
		DEC76		

SHT OF

CLASSIFICATION

MUST CONFORM TO ENG SPEC

DEVELOPMENT NO

LOGIC PG NO AB009

DATE	CHANGE NO
DEC76	315621

DATE CHANGE NO

620-0133-1 MRO# 760522203 VERTICAL ELECTRICAL FORMAT

	NAME	DATE
APPRO		
CHECK		
DETAIL	TS	DEC76
DESIGN		

CLASSIFICATION

SHT 1 OF 2

IBM

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MUST CONFORM TO ENG SPEC

DEC76

CHANGE NO 315621

DEVELOPMENT NO

DATE

LOGIC PG NO AB010

CHANGE NO

DATE

AB010

1749380 B

ASTROCLOTH MADE IN U.S.A.

CABLES 1 THRU 9 PHYSICAL PATH

SHEET 1 OF 2

1749380 B

3705

	CCU	CCU	REMOTE OR CA-4		CS-2 OR CS-3		CA-1 WITH CS-1 CA-1, 2, 3 OR 4 REMOTE OR "DUMMY BOARD"
	01A-B4 BD	01A-B3 BD	01A-B1 BD	01A-B1 BD	01A-A3 BD	01A-A3 BD	01A-A4 BD
CABLE #1	EXITS AT V4		ENTERS AT A2 EXITS AT A4	ENTERS AT A2 EXITS AT A4	ENTERS AT V3 EXITS AT Z4	ENTERS AT V3 EXITS AT Z4	ENTERS AT A2 EXITS OR ENDS AT A4
CABLE #2	EXITS AT U4		ENTERS AT B2 EXITS AT B4	ENTERS AT B2 EXITS AT B4	ENTERS AT V4 EXITS AT Z5	ENTERS AT V4 EXITS AT Z5	ENTERS AT B2 EXITS OR TERMINATES AT B4 [1]
CABLE #3	EXITS AT V5		ENTERS AT C2 EXITS AT C4	ENTERS AT C2 EXITS AT C4	ENTERS AT V5 EXITS AT Z6	ENTERS AT V5 EXITS AT Z6	ENTERS AT C2 EXITS OR TERMINATES AT C4 [2]
CABLE #4		EXITS AT A4	ENTERS AT C3 EXITS AT C5	ENTERS AT C3 EXITS AT C5		ENTERS AT B3 EXITS AT Z3	ENTERS AT C3 EXITS OR ENDS AT C5
CABLE #5	EXITS AT U5		ENTERS AT A3 EXITS AT A5	ENTERS AT A3 EXITS AT A5		ENTERS AT B5 EXITS AT Z1	ENTERS AT A3 EXITS OR ENDS AT A5
CABLE #6		EXITS AT A5	ENTERS AT B3 EXITS AT B5	ENTERS AT B3 EXITS AT B5		ENTERS AT B4 EXITS AT Z2	ENTERS AT B3 EXITS OR TERMINATES AT B5 [2]
CABLE #7		EXITS AT A2			ENTERS AT A4 EXITS OR TERMINATES AT A2 [2]	ENTERS AT A4 EXITS OR TERMINATES AT A2 [2]	
CABLE #8		EXITS AT A3			ENTERS AT A5 EXITS OR ENDS AT A3	ENTERS AT A5 EXITS OR ENDS AT A3	
CABLE #9	EXITS AT A5		ENTERS AT Y3 ENDS AT Y2				

NOTE: THE CABLES PHYSICALLY START AT THE CCU (01A-B4 OR 01A-B3 BOARD) AND GO TO THE FIRST ADAPTER INSTALLED. E.G. CABLE #1 STARTS AT 01A-B4V4. IF A REMOTE OR CA-4 IS NOT INSTALLED IN THE 01A-B1 BOARD AND A CS-3 IS INSTALLED IN THE 01A-B3 BOARD, CABLE #1 WILL GO FROM 01A-B4V4 TO 01A-A3V3. FROM THERE IT WILL EXIT TO THE NEXT INSTALLED ADAPTER. BLANKS INDICATE THAT THE SPECIFIC CABLE NEVER GOES TO THAT ADAPTER.

FOR THE VARIOUS LOGIC PAGES AND BOARD SIGNAL CROSSINGS SEE THE APPROPRIATE AB PAGE:

CABLE #1	AB001	CABLE #5	AB005
CABLE #2	AB002	CABLE #6	AB006
CABLE #3	AB003	CABLE #7	AB007
CABLE #4	AB004	CABLE #8	AB008
		CABLE #9	AB009

[1] TERMINATOR CARD P/N 5862385

[2] TERMINATOR CARD P/N 5862384

CONTINUED ON SHEET 2

PART NO 1749380

LOGIC PG NO AB010

620 0133 1 MROE 780522203 VERTICAL ELECTRICAL FORMAT

CABLES 1 THRU 9 PHYSICAL PATH

SHEET 2 OF 2

1749380 B

(CONTINUED FROM SHEET 1)

	1ST EXPANSION FRAME				2ND EXPANSION FRAME (REPEATS FOR 3RD EXP FR)		
	CA-4	CS-2 OR CS-3		CA-2, 3, OR 4 OR "DUMMY BOARD" 8D	CS-2 OR CS-3		"DUMMY BOARD"
CBL	02A-B4 BD	02A-A3 BD	02A-A3 BD	02A-A4 BD	03A-A3 BD	03A-A3 BD	03A-A4 BD
#1	ENTERS AT A2 EXITS AT A4	ENTERS AT V3 EXITS AT Z4	ENTERS AT V3 EXITS AT Z4	ENTERS AT A2 EXITS OR ENDS AT A4	ENTERS AT V3 EXITS AT Z4	ENTERS AT V3 EXITS AT Z4	ENTERS AT A2 EXITS OR ENDS AT A4
#2	ENTERS AT B2 EXITS AT B4	ENTERS AT V4 EXITS AT Z5	ENTERS AT V4 EXITS AT Z5	ENTERS AT B2 EXITS OR TERMINATES AT B4 [1]	ENTERS AT V4 EXITS AT Z5	ENTERS AT V4 EXITS AT Z5	ENTERS AT B2 EXITS OR TERMINATES AT B4 [1]
#3	ENTERS AT C2 EXITS AT C4	ENTERS AT V5 EXITS AT Z6	ENTERS AT V5 EXITS AT Z6	ENTERS AT C2 EXITS OR TERMINATES AT C4 [2]	ENTERS AT V5 EXITS AT Z6	ENTERS AT V5 EXITS AT Z6	ENTERS AT C2 EXITS OR TERMINATES AT C4 [2]
#4	ENTERS AT C3 EXITS AT C5		ENTERS AT B3 EXITS AT Z3	ENTERS AT C3 EXITS OR ENDS AT C5		ENTERS AT B3 EXITS AT Z3	ENTERS AT C3 EXITS OR ENDS AT C5
#5	ENTERS AT A3 EXITS AT A5		ENTERS AT B5 EXITS AT Z1	ENTERS AT A3 EXITS OR ENDS AT A5		ENTERS AT B5 EXITS AT Z1	ENTERS AT A3 EXITS OR ENDS AT A5
#6	ENTERS AT B3 EXITS AT B5		ENTERS AT B4 EXITS AT Z2	ENTERS AT B3 EXITS OR TERMINATES AT B5 [2]		ENTERS AT B4 EXITS AT Z2	ENTERS AT B3 EXITS OR TERMINATES AT B5 [2]
#7		ENTERS AT A4 EXITS OR TERMINATES AT A2 [2]	ENTERS AT A4 EXITS OR TERMINATES AT A2 [2]		ENTERS AT A4 EXITS OR TERMINATES AT A2 [2]	ENTERS AT A4 EXITS OR TERMINATES AT A2 [2]	
#8		ENTERS AT A5 EXITS OR ENDS AT A3	ENTERS AT A5 EXITS OR ENDS AT A3		ENTERS AT A5 EXITS OR ENDS AT A3	ENTERS AT A5 EXITS OR ENDS AT A3	
#9							

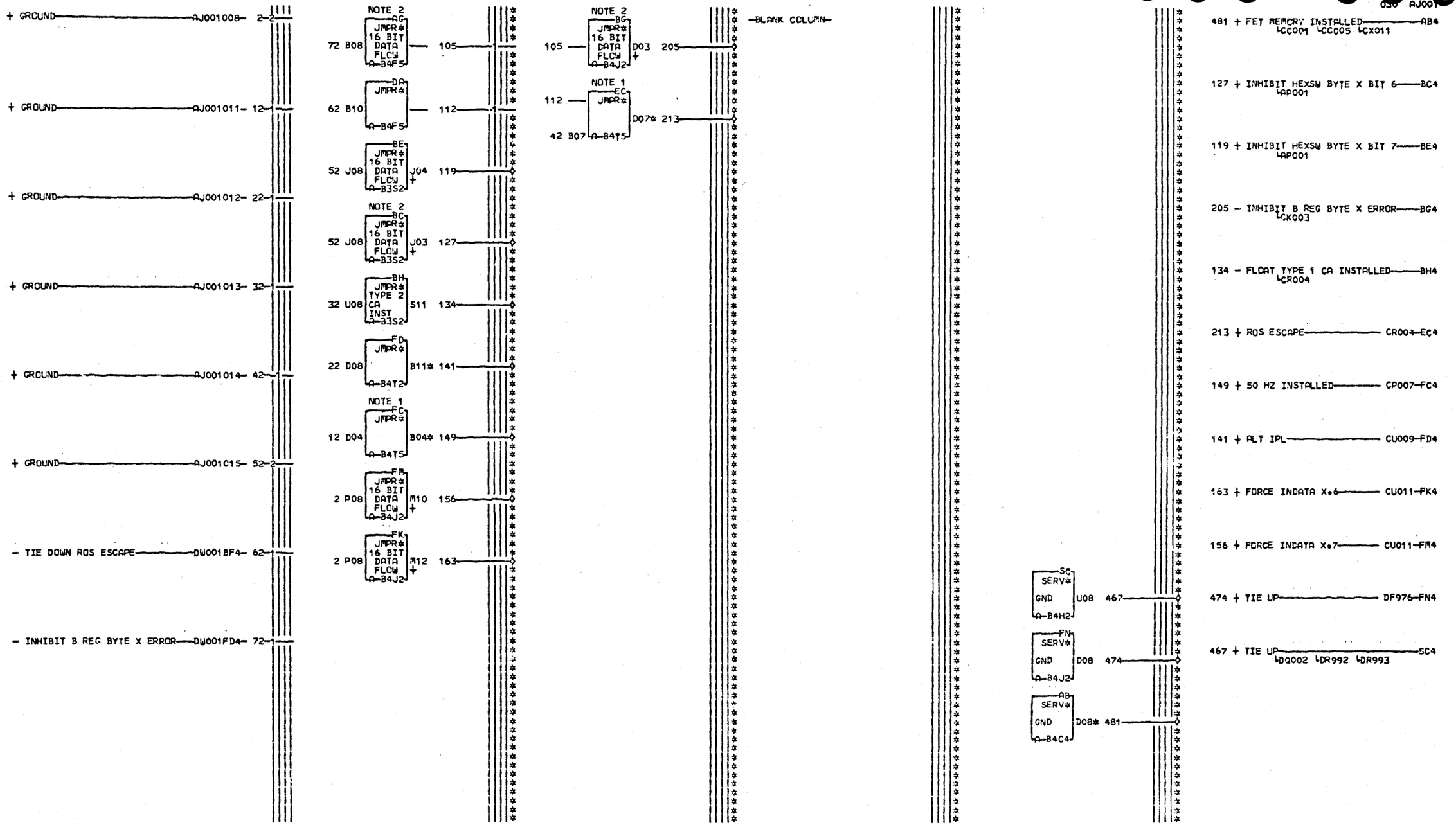
[1] TERMINATOR CARD P/N 5862885

[2] TERMINATOR CARD P/N 5862884

APPRO			
CHECK			
DETAIL	T3	DEC76	
DESIGN			SHT 2 OF 2
NAME			
DATE			
CHANGE NO		315621	
DEVELOPMENT NO			
LOGIC PG NO	AB010		

1749380 B

PART NO 1749380 LOGIC PG NO AB010



THIS PAGE IS FOR 3705-II ONLY.

NOTE 1. D04 AND B07 ARE WIRED TO GROUND THRU DY001FE

NOTE 2. JUMPER THESE BLOCKS IF MEMORY SIZE IS LESS THAN OR EQUAL TO 64K.

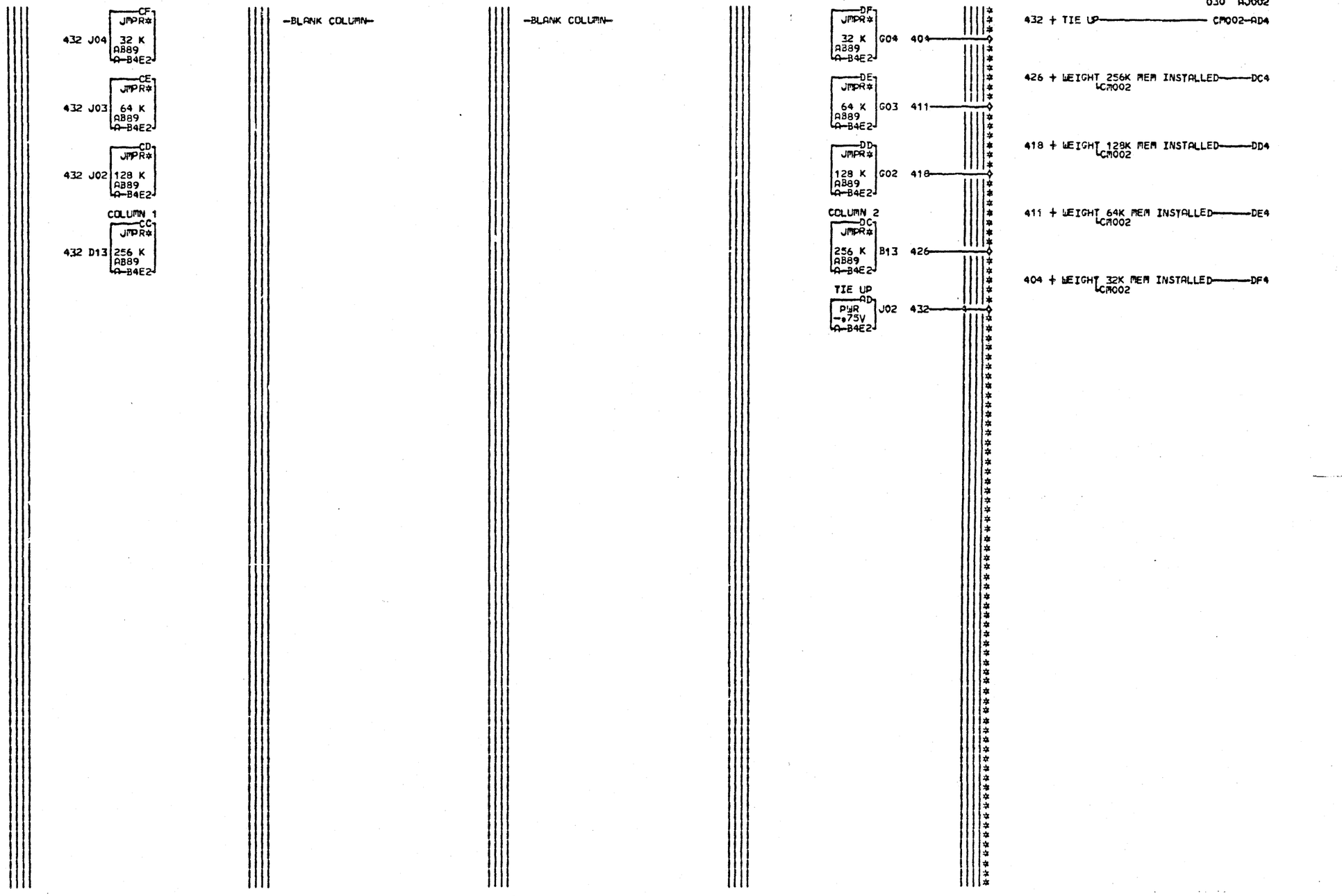
AJ001
030 SIM /O PN 5997556 EC 312926

EDGE CONN. 481 A-B4E6A02
141 A-B4E6C02 01A-B3E1A11
01A-B3E1C11
01A-B4T2811
149 A-B4T5B04
01A-B4E6B04
01A-B3E1B13
213 A-B4T5D07
01A-B4E6C04
01A-B3E1C13

LOC. TYPE

BOARD JUMPERS

E.C. HISTORY	D MACH.27RNB
312922	
314419	FRAME 01
DATE LAST EC	IBM CORP. SDD AJ001
08-31-76 315033	P.N. 1750128 030



THIS PAGE IS FOR 3705-II ONLY.

JUMPER FROM COLUMN 1
HORIZONTALLY TO COLUMN 2.
THE SUM OF THE BLOCKS JUMPERED
IS TO EQUAL THE
MEMORY SIZE INSTALLED.

AJ002

030 SIM TO PN 5997557 EC 309545

LOC. TYPE
A-B4E2 A889

MEMORY SIZE INSTALLED JUMPERS
01A-B4E2 SOCKET

E.C. HISTORY	D. MACH.27RNB
312922 314419	FRAME 01
DATE LAST EC 10-06-76 315053	IBM CORP. SDD AJ002 P.N. 1750129 030

+ MEM RESET — DB101FD1 — 2
+ STORE NEW — DB101FD2 — 4
+ RESET — DB101FD4 — 6
- ALLOW SET MEMORY DIAG REG — DB101FD5 — 8
+ RESET MEMORY DIAG REG — DB101FD6 — 10
+ BAD ADDRESS — DB101FD8 — 12
+ CABLE PLUGGED IN — DB101FH1 — 14
UNUSED — DB101FH2 — 16
UNUSED — DB101FH4 — 18
UNUSED — DB101FH5 — 20
UNUSED — DB101FH7 — 22
UNUSED — DB101FH8 — 24
UNUSED — DB101FM1 — 26
UNUSED — DB101FM3 — 28
UNUSED — DB101FM4 — 30
UNUSED — DB101FM5 — 32
UNUSED — DB101FM7 — 34
UNUSED — DB101FM8 — 36
+ SAR BIT X.6 — DS001FD1 — 38
+ SAR BIT 0.7 — DS001FD2 — 40
+ SAR BIT 1.0 — DS001FD4 — 42
+ SAR BIT 1.1 — DS001FD5 — 44
+ SAR BIT 1.2 — DS001FD6 — 46
+ SAR BIT 1.3 — DS001FD8 — 48
+ SAR BIT 1.4 — DS001FH1 — 50
+ SAR BIT 1.5 — DS001FH2 — 52
+ SAR BIT 1.6 — DS001FH4 — 54
+ MEM RD CALL WR CALL FRAME 1 — DS001FH5 — 56
+ SAR BIT X.7 — DS001FH7 — 58
+ SAR BIT 0.0 — DS001FH8 — 60
+ SAR BIT 0.1 — DS001FM1 — 62
+ SAR BIT 0.2 — DS001FM3 — 64
+ SAR BIT 0.3 — DS001FM4 — 66
+ SAR BIT 0.4 — DS001FM5 — 68
+ SAR BIT 0.5 — DS001FM7 — 70
+ SAR BIT 0.6 — DS001FM8 — 72

THIS PAGE IS FOR 3705-II ONLY.

AM001
030 SIM TO PN 5997558 EC 309545

DM: *EXIT* TO MEM 1 A-B4B2 — 50, 52, 54 — 102, 104, 106
DD: *EXIT* TO MEM CONTRL A-B4A4 — 16, 18, 20, 22 — 108, 110, 112, 114
DF: *EXIT* TO MEM CONTRL A-B4A4 — 30, 32, 34, 36 — 116, 118, 120, 122
DJ: *EXIT* TO MEM 1 A-B4B2 — 66, 68, 70 — 125, 127, 129
DL: *EXIT* TO MEM 1 A-B4B2 — 44, 46, 48 — 132, 134, 136
DC: *EXIT* TO MEM CONTRL A-B4A4 — 10, 12, 14 — 139, 141, 143
DH: *EXIT* TO MEM 1 A-B4B2 — 60, 62, 64 — 146, 148, 150
DK: *EXIT* TO MEM 1 A-B4B2 — 72, 40, 42 — 153, 155, 157

CABLE 31

DG: *EXIT* TO MEM 1 A-B4B2 — 38, 56, 58 — 161, 163, 165
DB: *EXIT* TO MEM CONTRL A-B4A4 — 2, 4, 6, 8 — 167, 169, 171, 173
DE: *EXIT* TO MEM CONTRL A-B4A4 — 24, 26, 28 — 176, 178, 180

LOC. TYPE

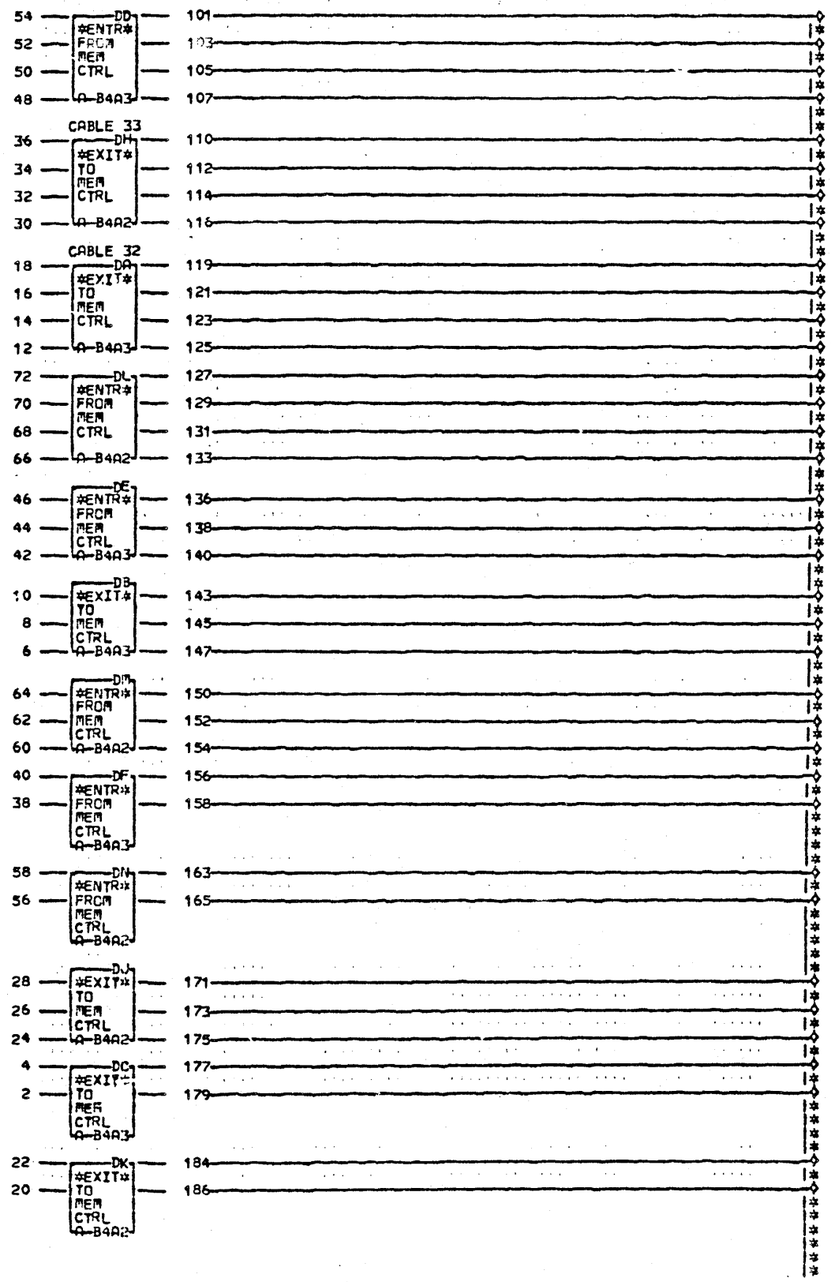
167 + MEM RESET — MM101-DB1
169 + STORE NEW — MM101-DB3
171 + RESET — MM101-DB5
173 - ALLOW SET MEMORY DIAG REG — DB7 MM101
139 + RESET MEMORY DIAG REG — MM101-DC2
141 + BAD ADDRESS — MM101-DC4
143 + CABLE PLUGGED IN — MM101-DC6
108 UNUSED — DD1
110 UNUSED — DD3
112 UNUSED — DD5
114 UNUSED — DD7
176 UNUSED — DE2
178 UNUSED — DE4
180 UNUSED — DE6
116 UNUSED — DF1
118 UNUSED — DF3
120 UNUSED — DF5
122 UNUSED — DF7
161 + FRAME 1 SAR BIT X.6 — MM201-DG2
163 + FRAME 1 RDCALL / WRCALL MM201-DG4
165 + FRAME 1 SAR BIT X.7 — MM201-DG6
146 + FRAME 1 SAR BIT 0.0 — MM201-DH2
148 + FRAME 1 SAR BIT 0.1 — MM201-DH4
150 + FRAME 1 SAR BIT 0.2 — MM201-DH6
125 + FRAME 1 SAR BIT 0.3 — MM201-DJ2
127 + FRAME 1 SAR BIT 0.4 — MM201-DJ4
129 + FRAME 1 SAR BIT 0.5 — MM201-DJ6
153 + FRAME 1 SAR BIT 0.6 — MM201-DK2
155 + FRAME 1 SAR BIT 0.7 — MM201-DK4
157 + FRAME 1 SAR BIT 1.0 — MM201-DK6
132 + FRAME 1 SAR BIT 1.1 — MM201-DL2
134 + FRAME 1 SAR BIT 1.2 — MM201-DL4
136 + FRAME 1 SAR BIT 1.3 — MM201-DL6
102 + FRAME 1 SAR BIT 1.4 — MM201-DM2
104 + FRAME 1 SAR BIT 1.5 — MM201-DM4
106 + FRAME 1 SAR BIT 1.6 — MM201-DM6

FET MEMORY INTERFACE FOR FRAME 1

E.C. HISTORY	D	MACH.27RNB
312922		FRAME 01
314419		IBM CORP. SDD
DATE LAST EC		P.N. 1750130
10-06-76 315053		AM001 030

+ STORE BIT 0.P	DS003BC1	2	
- STORE BIT 0.7	DS003BC2	4	
- STORE BIT 0.6	DS003BC4	6	
- STORE BIT 0.5	DS003BC5	8	
- STORE BIT 0.4	DS003BC6	10	
- STORE BIT 0.3	DS003BC8	12	
- STORE BIT 0.2	DS003BG1	14	
- STORE BIT 0.1	DS003BG2	16	
- STORE BIT 0.0	DS003BG4	18	
- STORE BIT 1.P	DS005BC1	20	
- STORE BIT 1.7	DS005BC2	22	
- STORE BIT 1.6	DS005BC4	24	
- STORE BIT 1.5	DS005BC5	26	
- STORE BIT 1.4	DS005BC6	28	
- STORE BIT 1.3	DS005BC8	30	
- STORE BIT 1.2	DS005BG1	32	
- STORE BIT 1.1	DS005BG2	34	
- STORE BIT 1.0	DS005BG4	36	
+ SENSE BIT 0.P	MM102CF5	38	
- SENSE BIT 0.7	MM102CF7	40	
- SENSE BIT 0.6	MM102CF8	42	
- SENSE BIT 0.5	MM102CJ1	44	
- SENSE BIT 0.4	MM102CJ3	46	
- SENSE BIT 0.3	MM102CJ4	48	
- SENSE BIT 0.2	MM102CJ5	50	
- SENSE BIT 0.1	MM102CJ7	52	
- SENSE BIT 0.0	MM102CJ8	54	
+ SENSE BIT 1.P	MM103CF5	56	
- SENSE BIT 1.7	MM103CF7	58	
- SENSE BIT 1.6	MM103CF8	60	
- SENSE BIT 1.5	MM103CJ1	62	
- SENSE BIT 1.4	MM103CJ3	64	
- SENSE BIT 1.3	MM103CJ4	66	
- SENSE BIT 1.2	MM103CJ5	68	
- SENSE BIT 1.1	MM103CJ7	70	
- SENSE BIT 1.0	MM103CJ8	72	

THIS PAGE IS FOR 3705-II ONLY.



030 AM002

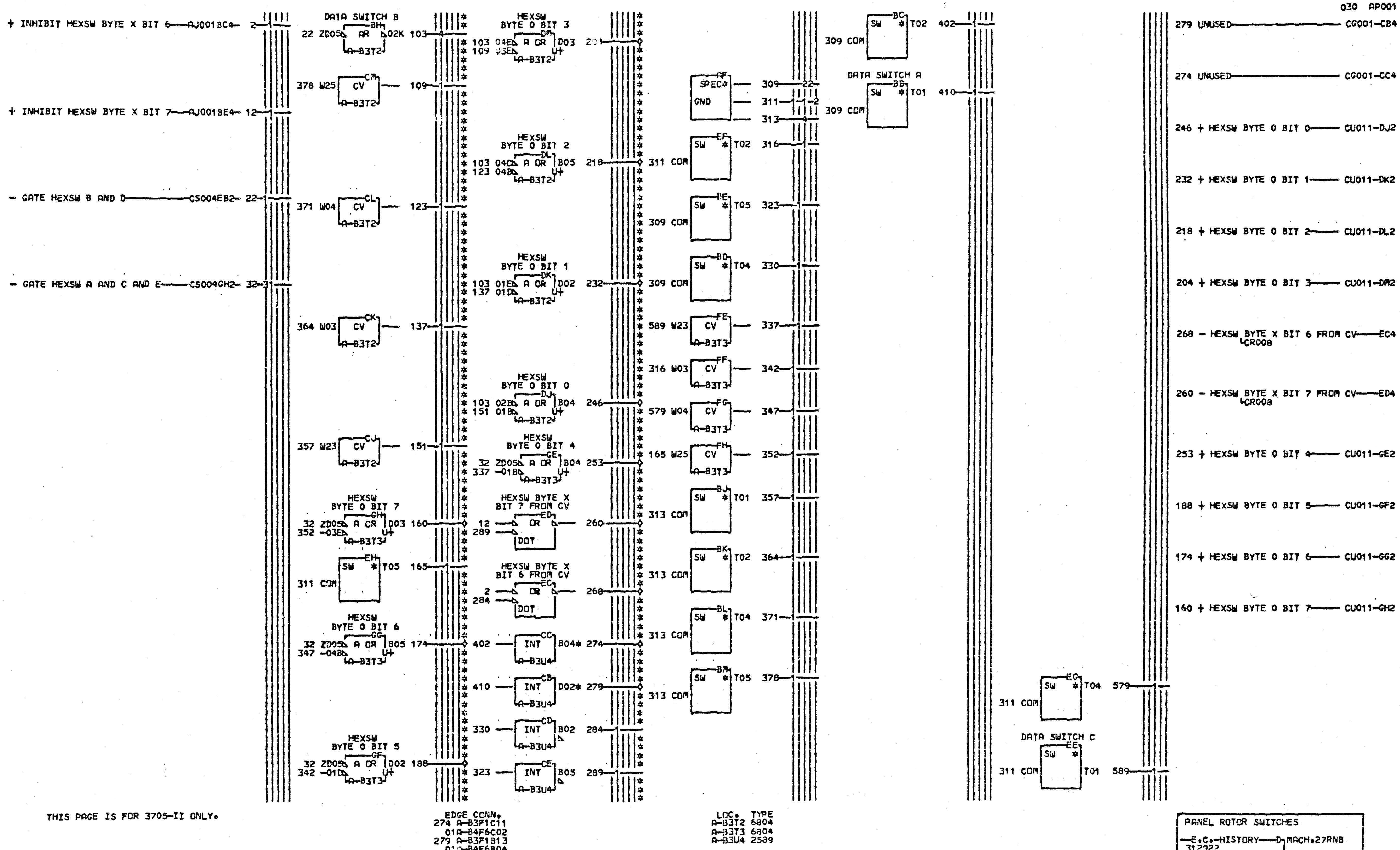
119 - STORE BIT 0.0	MM102-DA1	
121 - STORE BIT 0.1	MM102-DA3	
123 - STORE BIT 0.2	MM102-DA5	
125 - STORE BIT 0.3	MM102-DA7	
143 - STORE BIT 0.4	MM102-DB2	
145 - STORE BIT 0.5	MM102-DB4	
147 - STORE BIT 0.6	MM102-DB6	
177 - STORE BIT 0.7	MM102-DC1	
179 + STORE BIT 0.P	MM102-DC3	
101 - SENSE BIT 0.0	DS003-DD1	
103 - SENSE BIT 0.1	DS003-DD3	
105 - SENSE BIT 0.2	DS003-DD5	
107 - SENSE BIT 0.3	DS003-DD7	
136 - SENSE BIT 0.4	DS003-DE2	
138 - SENSE BIT 0.5	DS003-DE4	
140 - SENSE BIT 0.6	DS003-DE6	
156 - SENSE BIT 0.7	DS003-DF1	
158 + SENSE BIT 0.P	DS003-DF3	
110 - STORE BIT 1.0	MM103-DH1	
112 - STORE BIT 1.1	MM103-DH3	
114 - STORE BIT 1.2	MM103-DH5	
116 - STORE BIT 1.3	MM103-DH7	
171 - STORE BIT 1.4	MM103-DJ2	
173 - STORE BIT 1.5	MM103-DJ4	
175 - STORE BIT 1.6	MM103-DJ6	
184 - STORE BIT 1.7	MM103-DK1	
186 + STORE BIT 1.P	MM103-DK3	
127 - SENSE BIT 1.0	DS005-DL1	
129 - SENSE BIT 1.1	DS005-DL3	
131 - SENSE BIT 1.2	DS005-DL5	
133 - SENSE BIT 1.3	DS005-DL7	
150 - SENSE BIT 1.4	DS005-DM2	
152 - SENSE BIT 1.5	DS005-DM4	
154 - SENSE BIT 1.6	DS005-DM6	
163 - SENSE BIT 1.7	DS005-DN1	
165 + SENSE BIT 1.P	DS005-DN3	

LOC. TYPE

FET MEMORY FOR INTERFACE
FOR FRAME 1

E.C. HISTORY	MACH.27RNB	
312922 314419	FRAME 01	
DATE LAST EC 10-06-76 315053	IBM CORP.SDD P.N. 1750131	AM002 030

AM002
030 SIM TO PN 5997559 EC 309545


+ INHIBIT HEXSW BYTE X BIT 6—AJ001BC4— 2
+ INHIBIT HEXSW BYTE X BIT 7—AJ001BE4— 12
— GATE HEXSW B AND D—CS004EB2— 22
— GATE HEXSW A AND C AND E—CS004GH2— 32
DATA SWITCH B
DATA SWITCH A
DATA SWITCH C
HEXSW BYTE 0 BIT 3
HEXSW BYTE 0 BIT 2
HEXSW BYTE 0 BIT 1
HEXSW BYTE 0 BIT 0
HEXSW BYTE 0 BIT 4
HEXSW BYTE 0 BIT 7
HEXSW BYTE 0 BIT 6
HEXSW BYTE 0 BIT 5
HEXSW BYTE X BIT 7 FROM CV
HEXSW BYTE X BIT 6 FROM CV
279 UNUSED—CG001-CB4
274 UNUSED—CG001-CC4
246 + HEXSW BYTE 0 BIT 0— CU011-DJ2
232 + HEXSW BYTE 0 BIT 1— CU011-DK2
218 + HEXSW BYTE 0 BIT 2— CU011-DL2
204 + HEXSW BYTE 0 BIT 3— CU011-DM2
268 — HEXSW BYTE X BIT 6 FROM CV—EC4 CR008
260 — HEXSW BYTE X BIT 7 FROM CV—ED4 CR008
253 + HEXSW BYTE 0 BIT 4— CU011-GE2
188 + HEXSW BYTE 0 BIT 5— CU011-GF2
174 + HEXSW BYTE 0 BIT 6— CU011-GG2
160 + HEXSW BYTE 0 BIT 7— CU011-GH2
EDGE CONN.
274 A-B3F1C11
01A-B4F6C02
279 A-B3F1B13
01A-B4F6B04
LOC. TYPE
A-B3T2 6804
A-B3T3 6804
A-B3U4 2589

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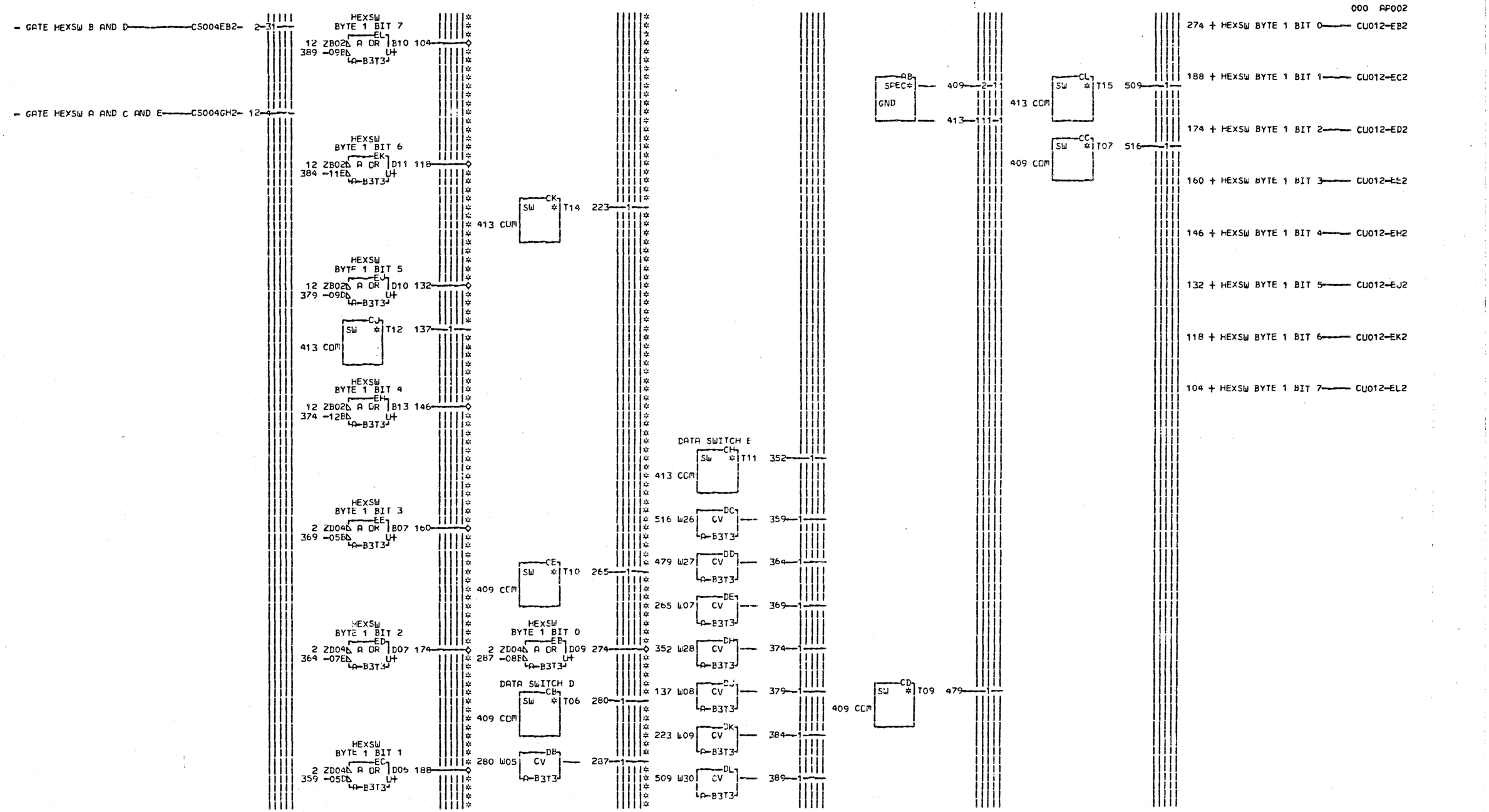
PANEL ROTOR SWITCHES	
E.C.—HISTORY— 312922 314419	MACH.27RNB
	FRAME 01
	IBM CORP.SDD
DATE 10-06-76 LAST EC 315053	P.N. 1750132

AP001
030



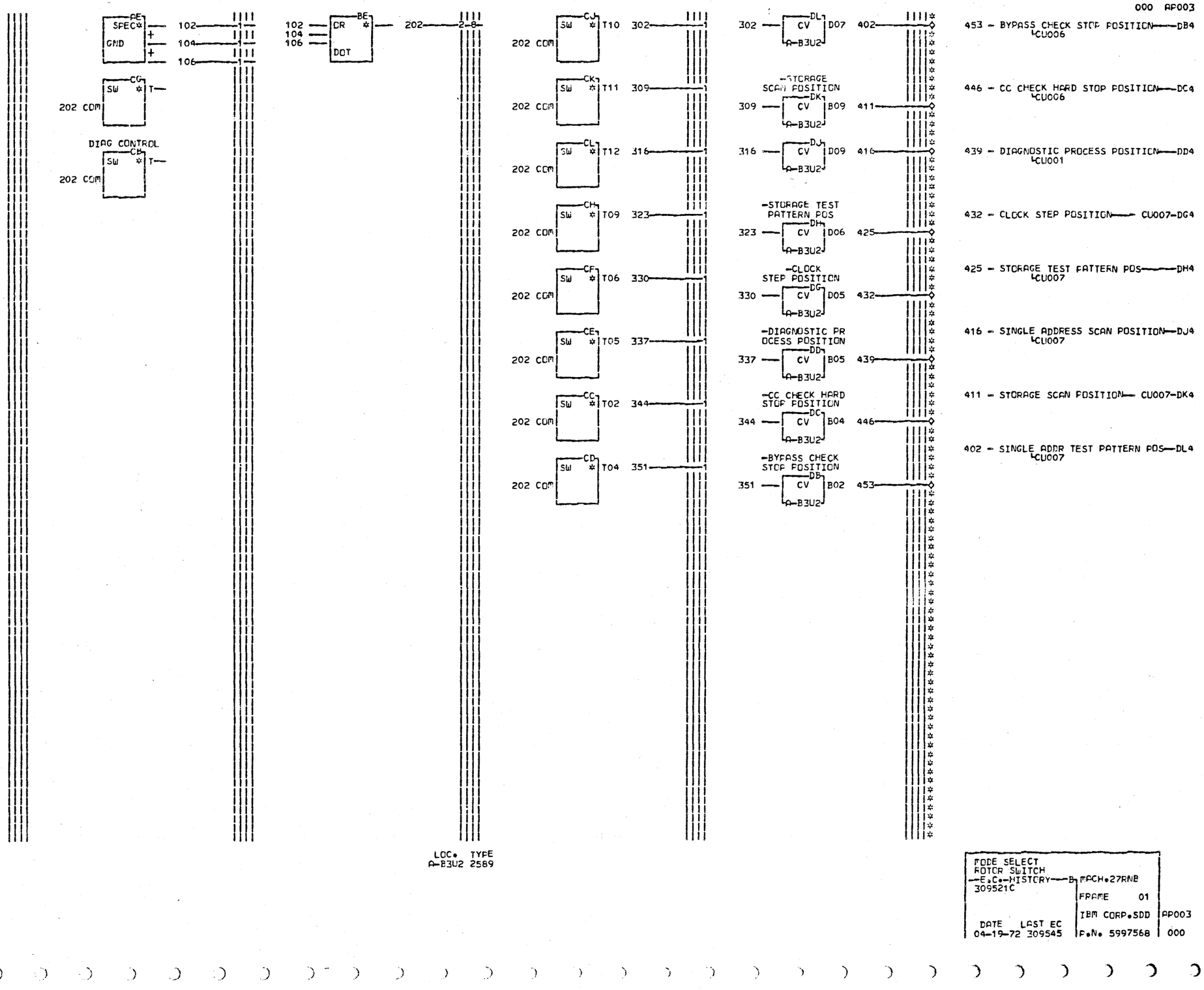
PANEL ROTOR SWITCHES

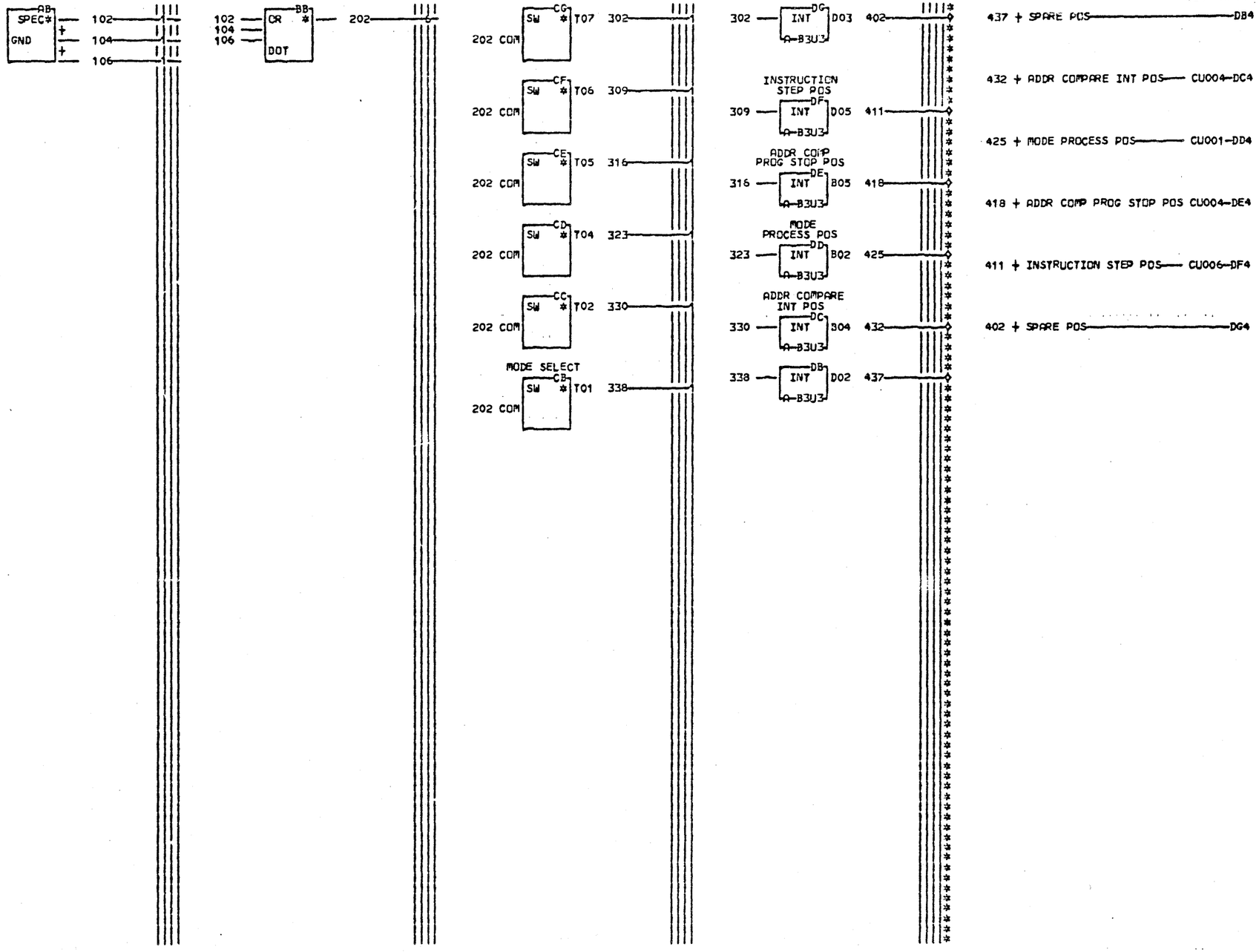
E.C.-HISTORY-B- MACH.27RNB
309521C

FRAME 01

IBM CORP.SDD AP002

DATE LAST EC P.N. 5997567 000
04-19-72 309545


AE
SPEC
GND
102
104
106
BE
CR
DOT
202
CG
SW
T
202 COM
DIAG CONTROL
CB
CJ
T10
302
CK
T11
309
CL
T12
316
CH
T09
323
CF
T06
330
CE
T05
337
CC
T02
344
CD
T04
351
DL
CV
D07
B3U2
402
-STORAGE SCAN POSITION
DK
B09
411
DJ
D09
416
-STORAGE TEST PATTERN POS
DH
D06
425
-CLOCK STEP POSITION
DG
D05
432
-DIAGNOSTIC PROCESS POSITION
DD
B05
439
-CC CHECK HARD STOP POSITION
DC
B04
446
-BYPASS CHECK STOP POSITION
DB
B02
453
453 - BYPASS CHECK STOP POSITION — DB4
CU006
446 - CC CHECK HARD STOP POSITION — DC4
CU006
439 - DIAGNOSTIC PROCESS POSITION — DD4
CU001
432 - CLOCK STEP POSITION — CU007-DG4
425 - STORAGE TEST PATTERN POS — DH4
CU007
416 - SINGLE ADDRESS SCAN POSITION — DJ4
CU007
411 - STORAGE SCAN POSITION — CU007-DK4
402 - SINGLE ADDR TEST PATTERN POS — DL4
CU007
LOC. TYPE
A-B3U2 2589
MODE SELECT
ROTOR SWITCH
E.C.-HISTORY
309521C
MPCH.27RNB
FRAME 01
IBM CORP.SDD
AP003
DATE LAST EC
04-19-72 309545
P.N. 5997568
000



THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE
A-B3U3 2589

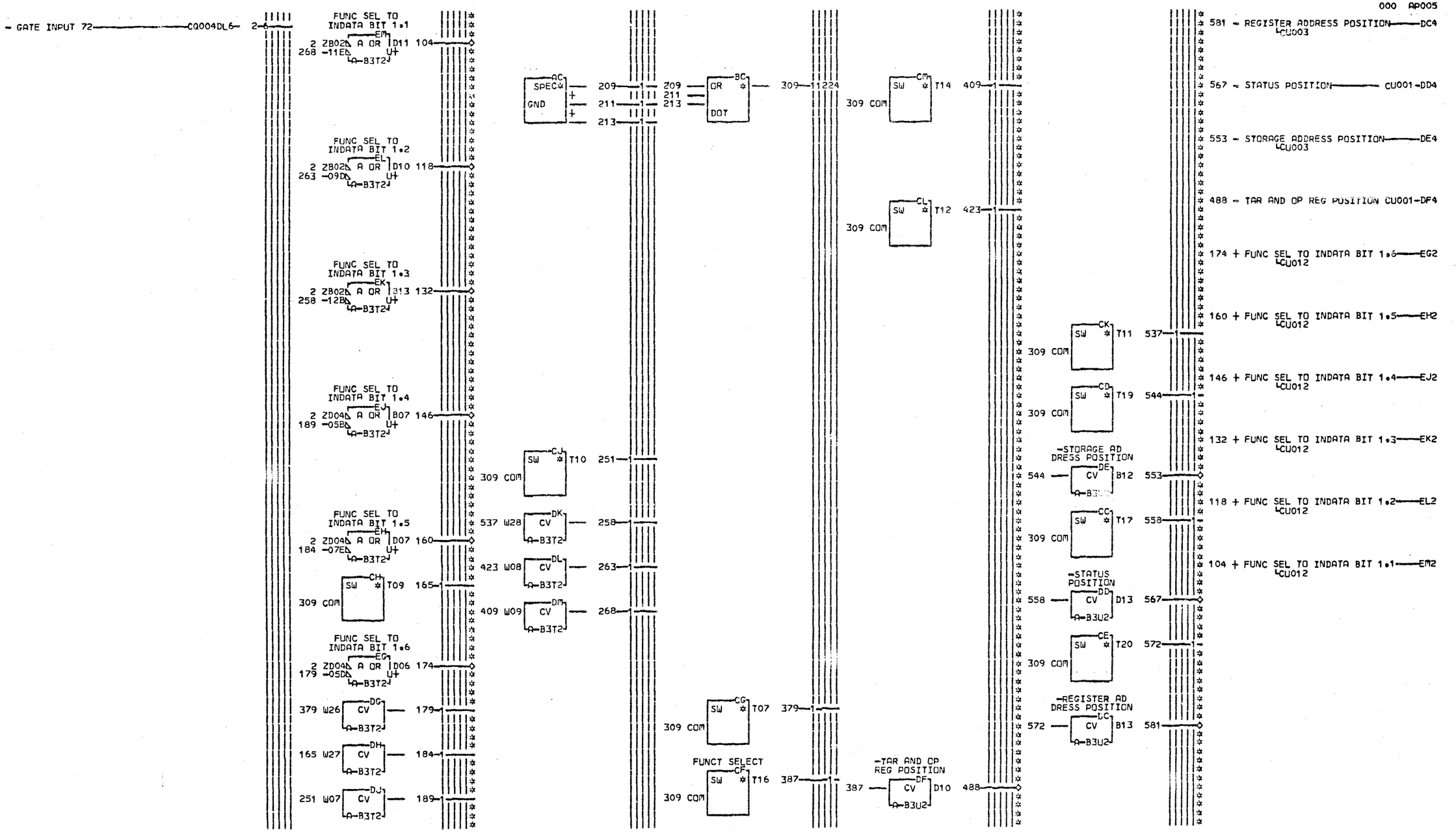
ADDRESS STOP-INTERRUPT SELECT
ROTOR SWITCH
—E.C.—HISTORY—D—
312922
314419

DATE LAST EC
10-06-76 315053

MACH.27RNB
FRAME 01
IBM CORP.SDD
P.N. 1750133

AP004
030

AP004
030 SIM TO PN 5997569 EC 310268


- GATE INPUT 72 CQ004DL6 2-6
FUNC SEL TO INDATA BIT 1.1
2 ZB02 A OR D11 104
268 -11E6 U+
A-B3T2
FUNC SEL TO INDATA BIT 1.2
2 ZB02 A OR D10 118
263 -09D6 U+
A-B3T2
FUNC SEL TO INDATA BIT 1.3
2 ZB02 A OR 313 132
258 -12B6 U+
A-B3T2
FUNC SEL TO INDATA BIT 1.4
2 ZD04 A OR B07 146
189 -05B6 U+
A-B3T2
FUNC SEL TO INDATA BIT 1.5
2 ZD04 A OR D07 160
184 -07E6 U+
A-B3T2
FUNC SEL TO INDATA BIT 1.6
2 ZD04 A OR D06 174
179 -05D6 U+
A-B3T2
AC SPEC GND
209 211 213
BC OR DOT
309 1224
CJ SW T10 251
309 COM
DK CV 258
537 W28
DL CV 263
423 W08
DM CV 268
409 W09
A-B3T2
CG SW T07 379
309 COM
CF SW T16 387
309 COM
CM SW T14 409
309 COM
CL SW T12 423
309 COM
CK SW T11 537
309 COM
CD SW T19 544
309 COM
-STORAGE AD DRESS POSITION
DE CV B12 553
CC SW T17 558
309 COM
-STATUS POSITION
DD CV D13 567
CE SW T20 572
309 COM
-REGISTER AD DRESS POSITION
DC CV B13 581
-TAR AND OP REG POSITION
DF CV D10 488
A-B3U2
581 - REGISTER ADDRESS POSITION DC4
CU003
567 - STATUS POSITION CU001-DD4
553 - STORAGE ADDRESS POSITION DE4
CU003
488 - TAR AND OP REG POSITION CU001-DF4
174 + FUNC SEL TO INDATA BIT 1.6 EG2
CU012
160 + FUNC SEL TO INDATA BIT 1.5 EH2
CU012
146 + FUNC SEL TO INDATA BIT 1.4 EJ2
CU012
132 + FUNC SEL TO INDATA BIT 1.3 EK2
CU012
118 + FUNC SEL TO INDATA BIT 1.2 EL2
CU012
104 + FUNC SEL TO INDATA BIT 1.1 EM2
CU012
LOC. TYPE
A-B3T2 6804
A-B3U2 2589
FUNCTION SELECT
ROTOR SWITCH
E.C.-HISTORY B MACH.27RNB
309521C
309545
FRAME 01
IBM CORP.SDD AP005
DATE LAST EC
06-28-72 309533
P.N. 5997570 000
AP005
000

-BLANK COLUMN-

AC SPEC* — 202 — 2
GND — 204 — 2
206 — 2

CM SW * T19 — 302 — 1
206 COM — N20 — 306 — 1

CK SW * T16 — 316 — 1
206 COM — N17 — 320 — 1

CH SW * T14 — 330 — 1
204 COM — N15 — 334 — 1

CF SW * T11 — 344 — 1
204 COM — N12 — 348 — 1

CD SW * T09 — 358 — 1
202 COM — N10 — 362 — 1

CB SW * T06 — 372 — 1
202 COM — N07 — 376 — 1

306 — DN CV B13 — 402
CHECK RESET PUSHBUTTON IN
302 — DM CV B12 — 411
320 — DL CV D13 — 416
RESET PUSHBUTTON IN
316 — DK CV D10 — 425
STORE PUSHBUTTON OUT
334 — DJ CV B10 — 432
STORE PUSHBUTTON IN
330 — DH CV D11 — 439
DISPLAY PUSHBUTTON OUT
348 — DG CV D09 — 446
DISPLAY PUSHBUTTON IN
344 — DF CV B09 — 453
362 — DE CV D07 — 458
STOP PUSHBUTTON IN
358 — DD CV D06 — 467
START PUSHBUTTON OUT
376 — DC CV D03 — 474
START PUSHBUTTON IN
372 — DB CV D05 — 481

All CV: A-B3U4

481 + START PUSHBUTTON IN — DB4
(CC007 CU003)

474 + START PUSHBUTTON OUT — DC4
(CC007 CU003)

467 + STOP PUSHBUTTON IN — CU004 — DD4

458 + STOP PUSHBUTTON OUT UNUSED — DE4

453 + DISPLAY PUSHBUTTON IN — CU003 — DF4

446 + DISPLAY PUSHBUTTON OUT — CU003 — DG4

439 + STORE PUSHBUTTON IN — CU003 — DH4

432 + STORE PUSHBUTTON OUT — CU003 — DJ4

425 + RESET PUSHBUTTON IN — CU010 — DK4

416 + RESET PUSHBUTTON OUT UNUSED — DL4

411 + CHECK RESET PUSHBUTTON IN — DM4
(CU006)

402 + CHECK RESET PB OUT UNUSED — DN4

LOC. TYPE
A-B3L4 2589

PUSH BUTTONS

E.C. HISTORY — B	MACH.27RNB	
309521C	FRAME 01	
	IBM CORP. SDD	AP006
DATE LAST EC	P.N. 5997571	000
04-19-72 309545		

-BLANK COLUMN-

AC
SPEC* 202 1-
GND 203 1-
205 2-
206 2-

DN SW * N19 302
206 COM T20 306

DL SW * T16 316
206 COM N17 320

DJ SW * N19 330
205 COM T20 334

DG SW * T16 344
205 COM N17 348

DC SW * T11 358
203 COM N12 362

DA SW * T14 372
202 COM N15 376

CHANNEL 2 ENABLE INTF B POS
306 W12 FN CV B11* 404
A-B3T3

CHANNEL 2 DISABLE INTF B POS
302 W32 FM CV B12* 411
A-B3T3

CHANNEL 2 DISABLE INTF A POS
320 W31 EM CV D13* 418
A-B3T3

CHANNEL 2 ENABLE INTF A POS
316 W10 EL CV D12* 425
A-B3T3

CHANNEL 1 ENABLE INTF B POS
334 W12 EK CV B11* 432
A-B3T2

CHANNEL 1 DISABLE INTF B POS
330 W32 EJ CV B12* 439
A-B3T2

CHANNEL 1 DISABLE INTF A POS
348 W31 EH CV D13* 446
A-B3T2

CHANNEL 1 ENABLE INTF A POS
344 W10 EG CV D12* 453
A-B3T2

LOAD PUSHBUTTON OUT
362 ED CV D09 460
A-B3U3

LOAD PUSHBUTTON IN
358 EC CV B09 467
A-B3U3

INTERRUPT PUSHBUTTON OUT
376 EB CV B10 474
A-B3U2

INTERRUPT PUSHBUTTON IN
372 EA CV D11 481
A-B3U2

481 + INTERRUPT PUSHBUTTON IN CU014—EA4

474 + INTERRUPT PUSHBUTTON OUT——EB4
└CU014

467 + LOAD PUSHBUTTON IN—— CU010—EC4

460 + LOAD PUSHBUTTON OUT—— CU010—ED4

453 + CHANNEL 1 ENABLE INTF A POS—EG4
└AA002

446 + CHANNEL 1 DISABLE INTF A POS—EH4
└AA002

439 + CHANNEL 1 DISABLE INTF B POS—EJ4
└AA002

432 + CHANNEL 1 ENABLE INTF B POS—EK4
└AA002

425 + CHANNEL 2 ENABLE INTF A POS—EL4
└AA002

418 + CHANNEL 2 DISABLE INTF A POS—EM4
└AA002

411 + CHANNEL 2 DISABLE INTF B POS—FM4
└AA002

404 + CHANNEL 2 ENABLE INTF B POS—FN4
└AA002

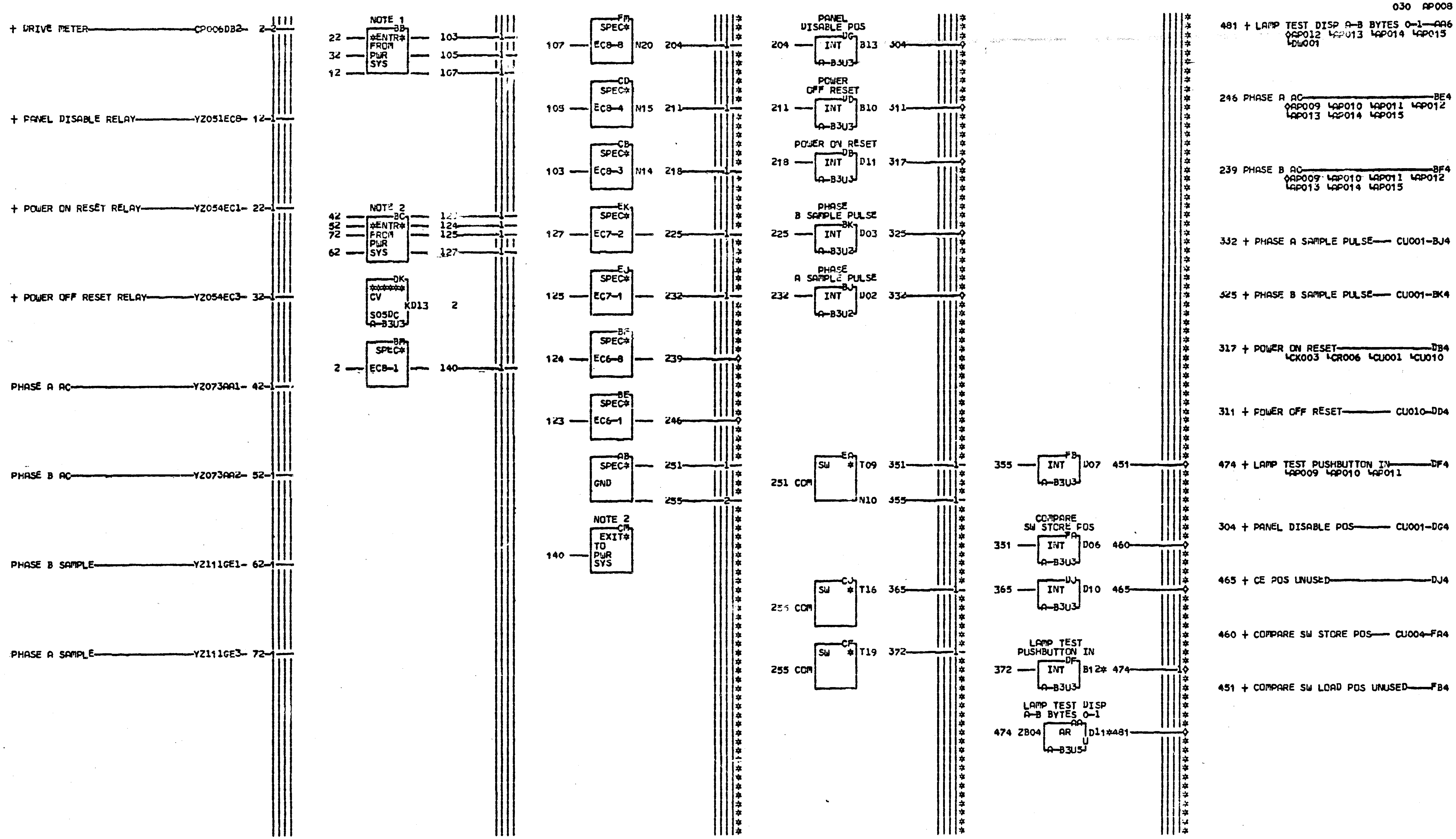
EDGE CONN.
404 A-B3A4B09
411 A-B3A4B12
418 A-B3A4B06
425 A-B3A4B04
432 A-B3A4B08
439 A-B3A4B10
446 A-B3A4B05
453 A-B3A4B02

LOC. TYPE
A-B3T2 6804
A-B3T3 6804
A-B3U2 2589
A-B3U3 2589

AP007
000

PUSH BUTTONS AND TOGGLES

—E.C.—HISTORY—B—	MACH.27RNB
309521C 309545	FRAME 01
	IBM CORP.SDD AP007
DATE LAST EC 06-28-72 309533	P.N. 5997572 000



THIS PAGE IS FOR 3705-II ONLY.

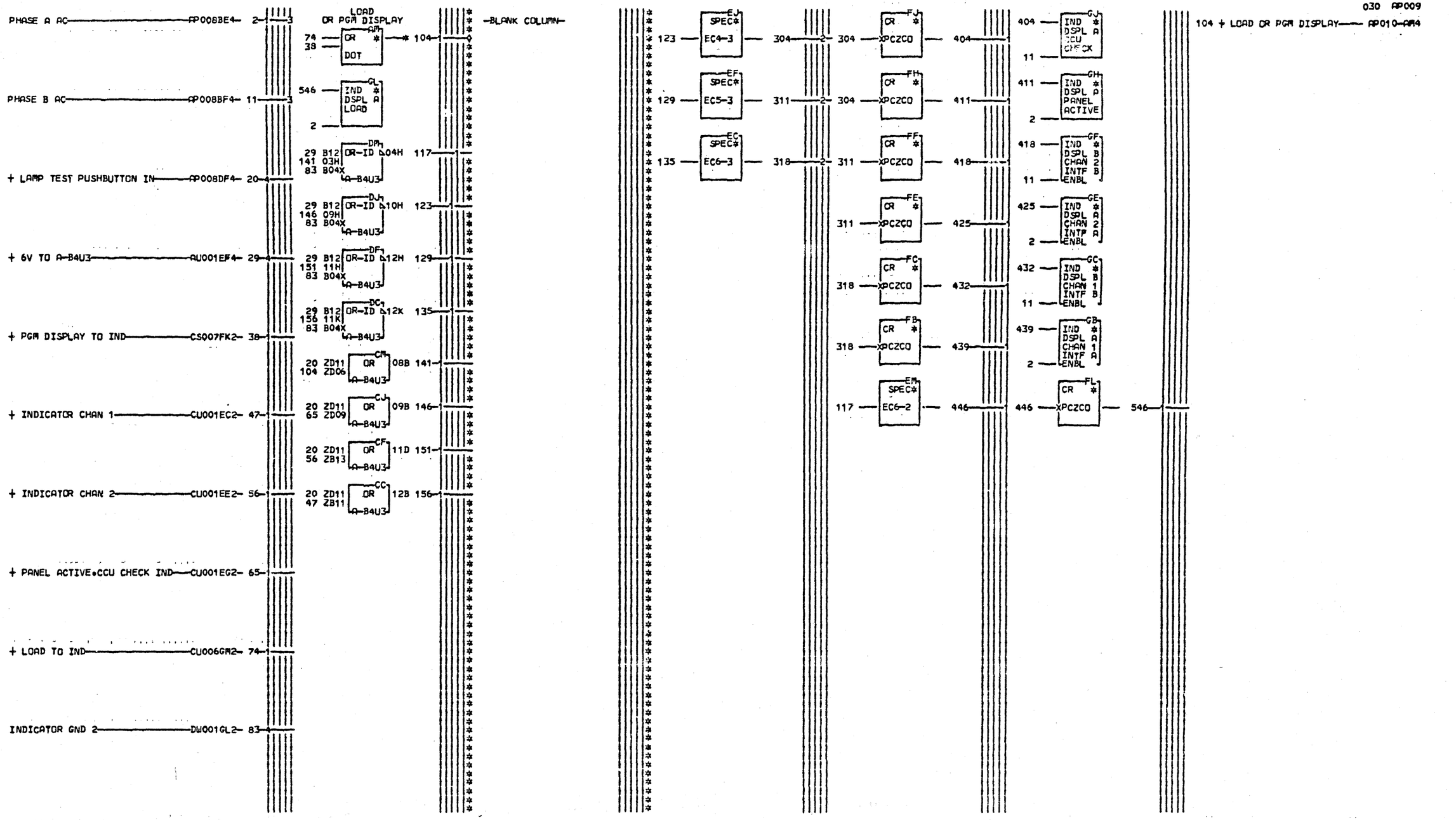
NOTE 1.
MATCH ECXX CONNECTORS
PAGE YZ054 OR YZ522.
NOTE 2.
MATCH ECXX CONNECTORS
AP008 PAGE YZ073 OR YZ522.

030 SIM TO PN 5997573 EC 310268

EDGE CONN.
474 A-B3V1B13
01A-B4V6B04
481 A-B3A1D11
01A-B4A6D02

LOC. TYPE
A-B3U2 2589
A-B3U3 2589
A-B3U5 AB85

POWER ON RESET RELAY POWER OFF SEQ RELAY CE CUST POWER ENABLE
E.C. HISTORY — E. MACH. 27RNB
312922
314419
315053
FRAME 01
IBM CORP. SDD
DATE 08-26-80 LAST EC 322689
P.N. 1750134
AP008
030



THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
104 A-B3V2B02
01A-B4V2B02

LOC. TYPE
A-B4U3 6845

INDICATOR CIRCUITS FOR
DISPLAY A AND B GUMDROP IND
E.C.-HISTORY — D-MACH.27RNB
312922
314419

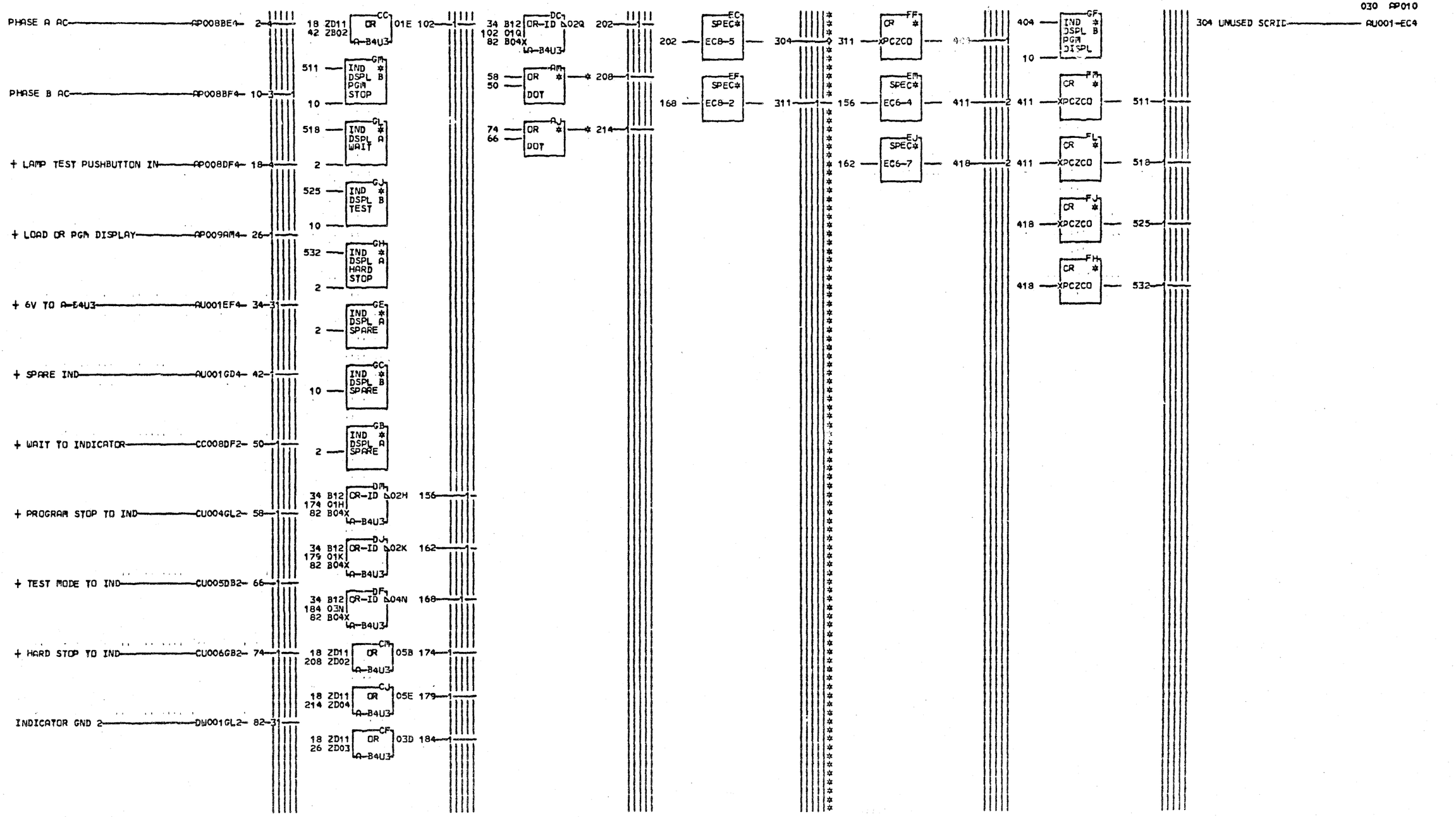
DATE	LAST EC
10-06-76	315053

FRAME 01

IBM CORP.SDD | AP009

P.N. 1750135 | 030

AP009
030 SIM TO PN 5997574 EC 309545



THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
208 A-B3V3B02
01A-B4V3B02
214 A-B3V2D05
01A-B4V2D05

LOC. TYPE
A-B4U3 6845

INDICATOR CIRCUITS FOR
DISPLAY A AND B OTHER IND
—E.C.—HISTORY—D. MACH.27 NB
312922
314419

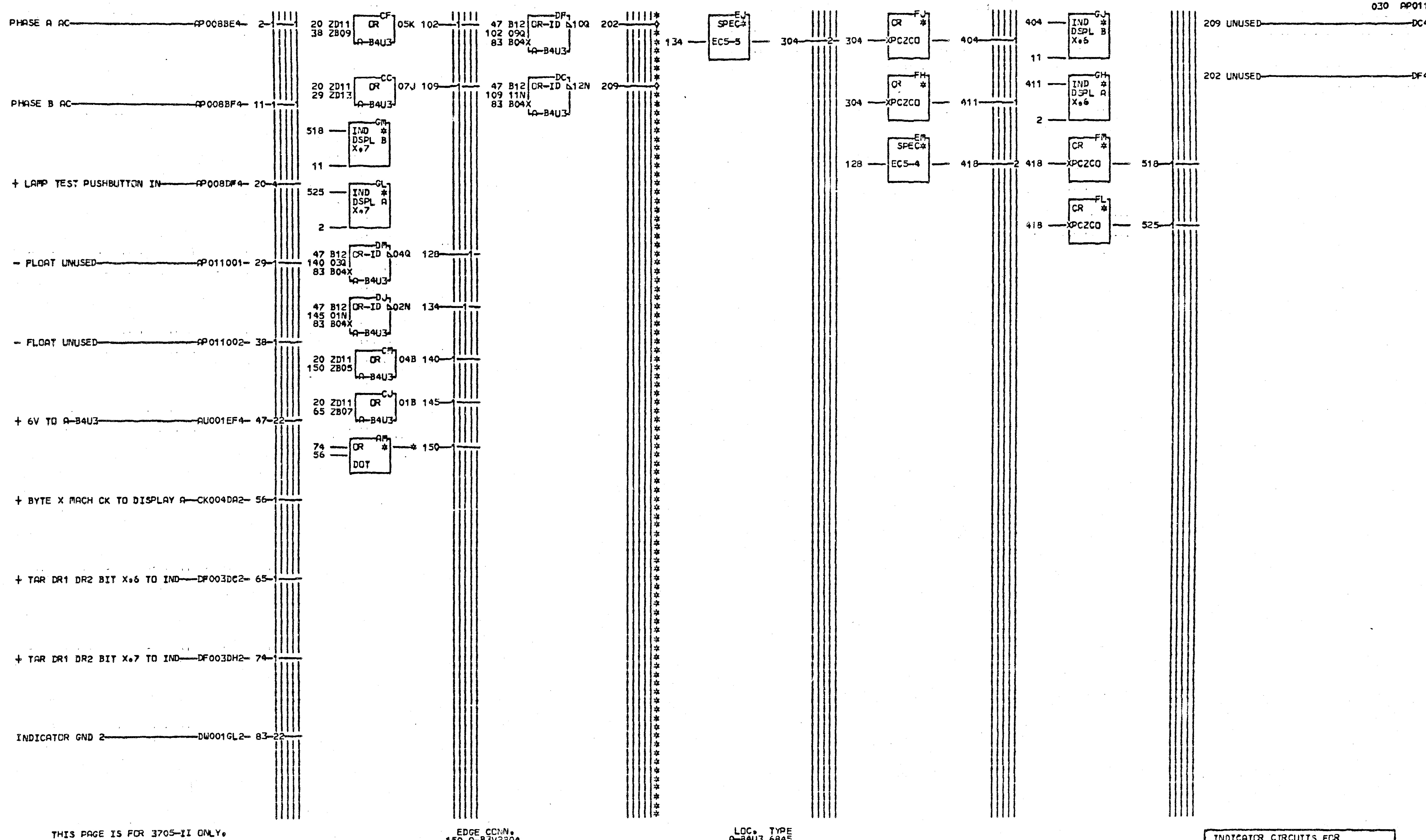
FRAME 01

DATE LAST EC
10-06-76 315053

IBM CORP. SDD
P.N. 1750136

AP010
030

AP010
030 SIM TO PN 5997575 EC 309545

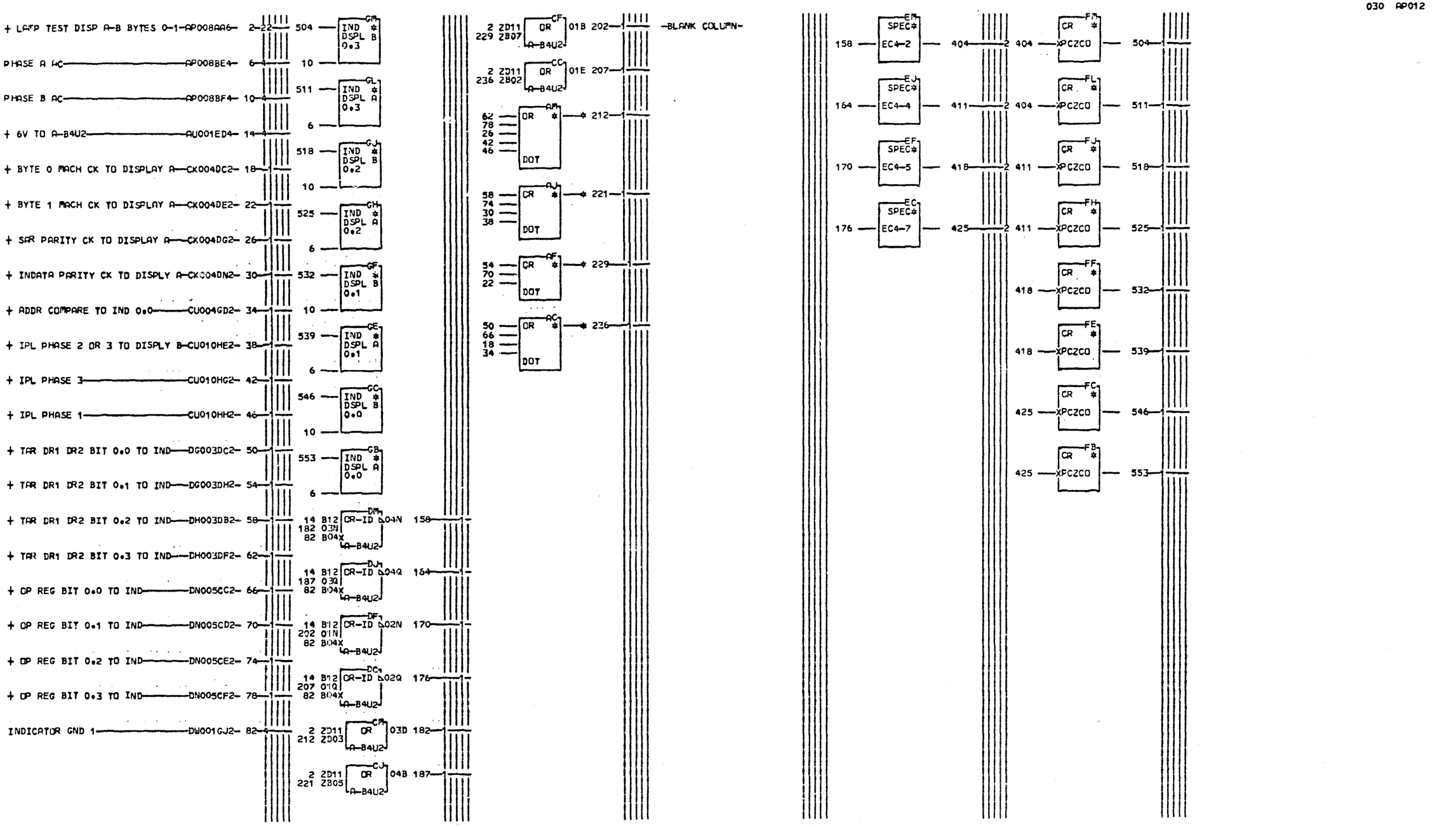


THIS PAGE IS FOR 3705-II ONLY.

FOR LOAD INDICATORS SEE AP009.

INDICATOR CIRCUITS FOR
DSPL A AND B AND BYTE X

E.C.-HISTORY— 312922 314419	D. MACH.27RNB
	FRAME 01
	IBM CORP.SDD
DATE 10-06-76 LAST EC 315053	P.N. 1750137



THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
212 A-B3V2809
01A-B4V2809
221 A-B3V2808
01A-B4V2808
229 A-B3V2806
01A-B4V2806
236 A-B3V2805
01A-B4V2805

LOC. TYPE
A-B4U2 6845

INDICATOR CIRCUITS FOR DISPLAY A AND B BYTE 0

E.C.-HISTORY-D.-MACH.27RNB
312922
314419

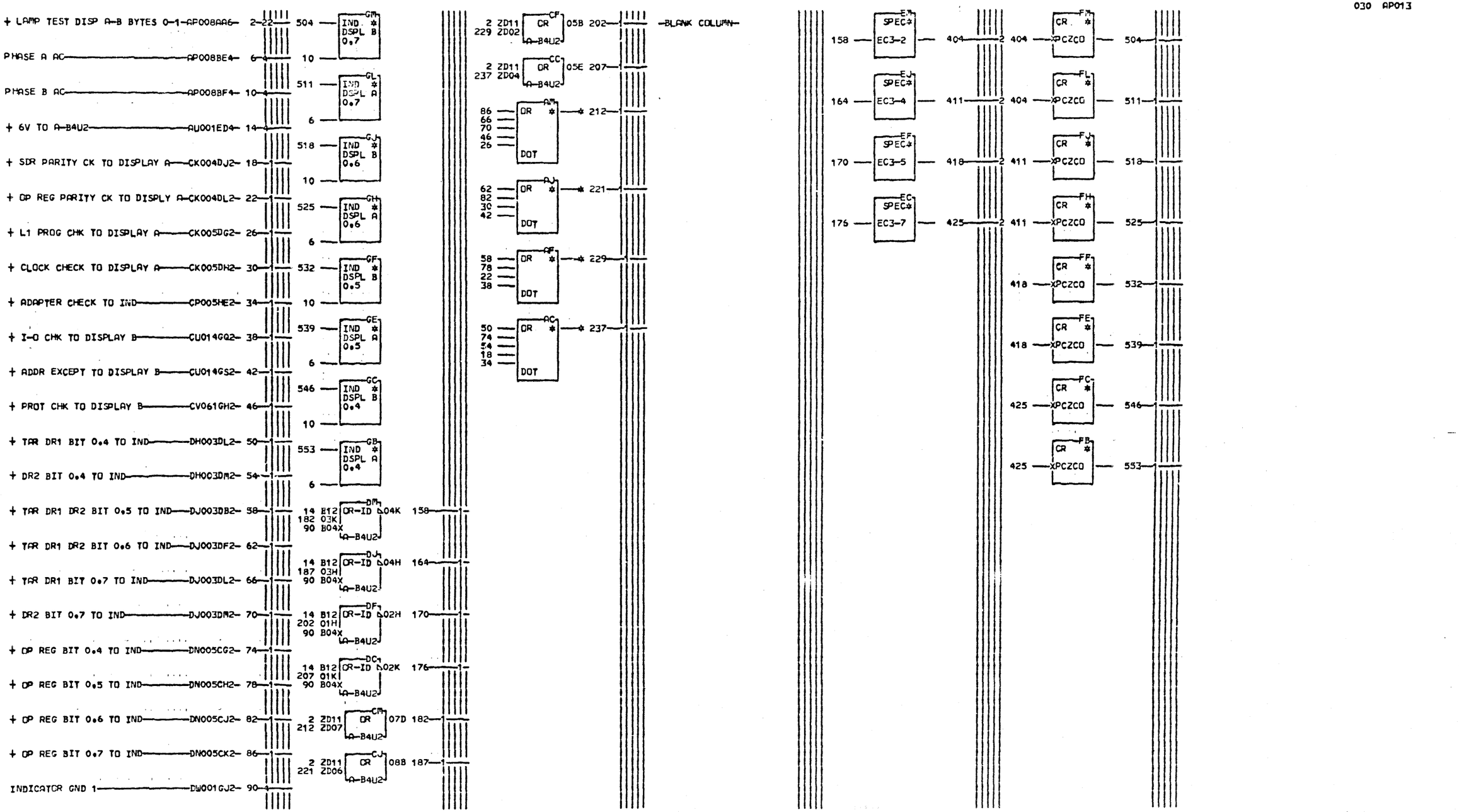
DATE LAST EC
10-06-76 315033

FRAME 01

IBM CORP. SDD AP012

P.N. 1750138 030

AP012
030 SIM TO PN 5997577 EC 309545



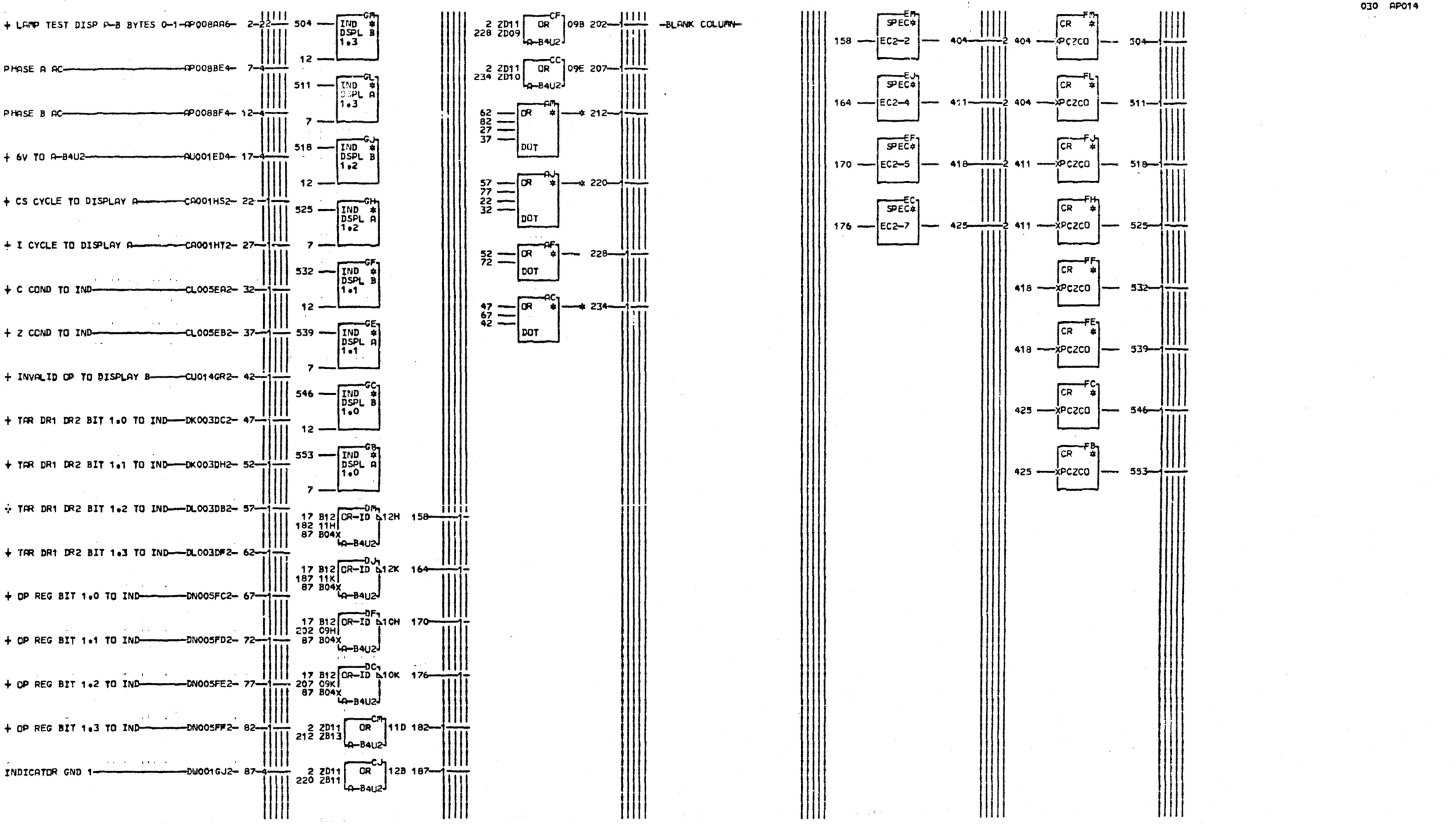
THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
212 A-B3V2D02
01A-B4V2D02
221 A-B3V2B13
01A-B4V2B13
229 A-B3V2B12
01A-B4V2B12
237 A-B3V2B10
01A-B4V2B10

LOC. TYPE
A-B4U2 6845

INDICATOR CIRCUITS FOR
DISPLAY A AND B BYTE 0
E.C.-HISTORY-D. MACH.27RNB
312922
314419
FRAME 01
DATE LAST EC
10-06-76 315053
IBM CORP.SDD AP013
P.N. 1750139 030

AP013
030 SIM TO PN 5997578 EC 309545



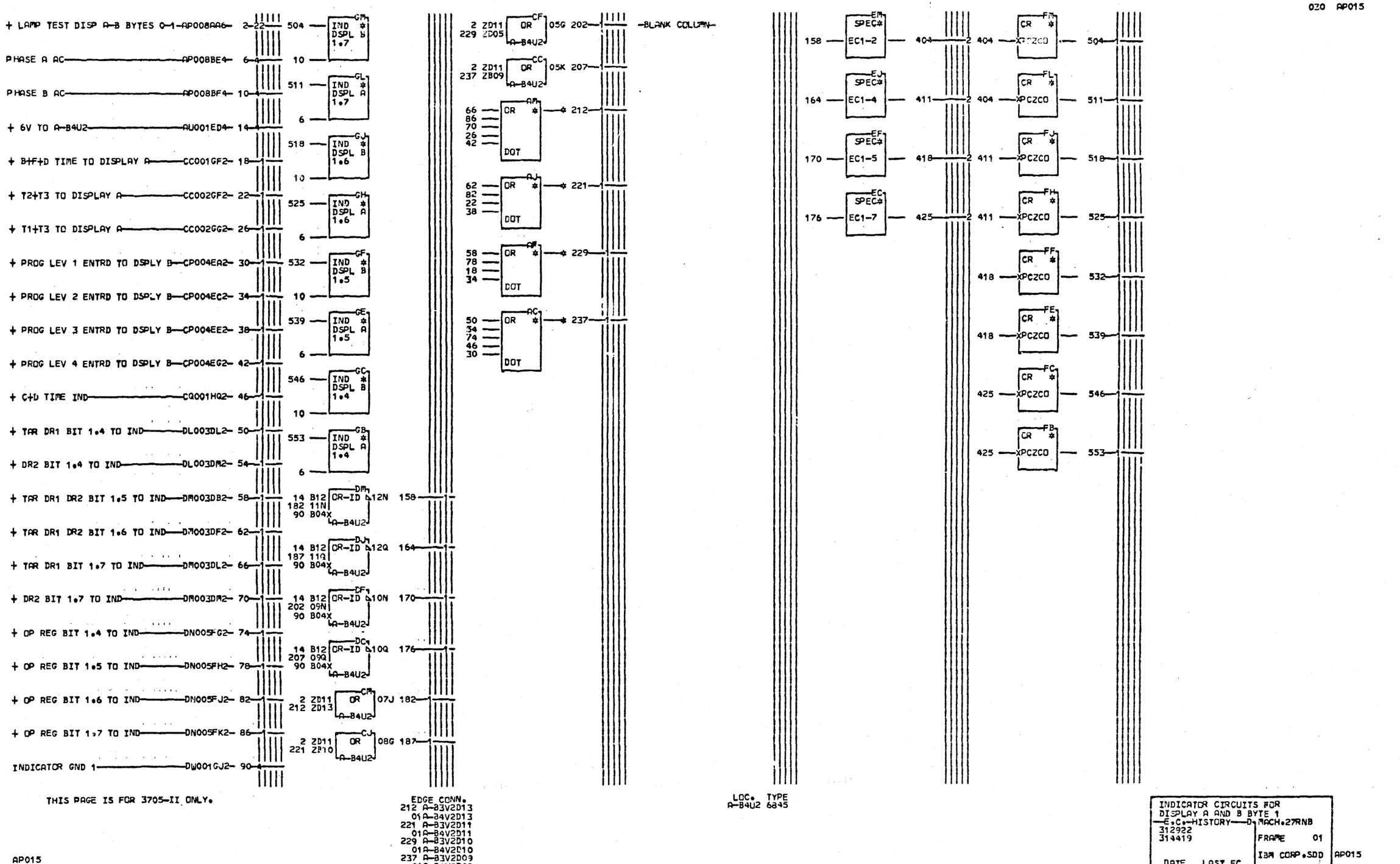
THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
212 A-B3V2D07 01A-B4V2D07
220 A-B3V2D08 01A-B4V2D06
234 A-B3V2D03 01A-B4V2D03

LOC. TYPE
A-B4U2 6845

INDICATOR CIRCUITS FOR DISPLAY A AND B BYTE 1
E.C. HISTORY: 312922, 314419
DATE 10-06-76 LAST EC 315053
MACH.27RN8
FRAME 01
IBM CORP. SDD
P.N. 1750140
AP014 030

AP014
030 SIM TO PN 5997579 EC 309545



THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
212 A-B3V2D13
01 A-B4V2D13
221 A-B3V2D11
01 A-B4V2D11
229 A-B3V2D10
01 A-B4V2D10
237 A-B3V2D09
01 A-B4V2D09

LOC. TYPE
A-B4U2 6845

INDICATOR CIRCUITS FOR DISPLAY A AND B BYTE 1

E.C.-HISTORY — D. MACH.27RNB
312922
314419

FRAME 01

DATE LAST EC
10-06-76 315033

IBM CORP.SDD AP015
P.N. 1750141 030

AP015
030 SIM TO PN 5997580 EC 309545

UNUSED SCRID ——— AP010EC4 — 2

— INT SCOPING POINT UNLOADED — CQ001BF6 — 12

NOTE

AM
EXIT*
12 — SCOPE

AK
EXIT*
2 — UNUSED

— BLANK COLUMN —

— BLANK COLUMN —

GH
ENTR* — 402
UNUSED — 404

GF
SERV*
GND — D08 — 411
A-B4U4

GD
ENTR*
UNUSED — 418

EF
SPEC*
+ 6V
TO — 425
A-B4U3

ED
SPEC*
+ 6V
TO — 432
A-B4U2

AH
ENTR*
UNUSED — 439

030 AL001

439 + CARD TEST RESET ——— CC007—AH4

432 + 6V TO A-B4U2 ——— ED4
LAP012 LAP013 LAP014 LAP015

425 + 6V TO A-B4U3 ——— EF4
LAP009 LAP010 LAP011

418 + SPARE IND ——— AP010—GD4

411 + TIE UP ——— GF4
◊DF975 LDF977 LDG975 LDN002
LDP992 LDP993 LDP994

402 — FLOAT UNUSED ——— GH2
◊CA003 LCD001 LDF002 LDG002
LDK003

404 — SPARE GO FOR PROTECTION KEY — GH4
LCV011

THIS PAGE IS FOR 3705-II ONLY.

NOTE
WHEN NOT PROGRAM
LEVEL 5 + ADPT I-O
CK◊ — CCU I-O CK

AU001

030 SIM TO PN 5997581 EC 310268

LOC. TYPE

UNUSED SIGNALS SPARES FLOATS
TIE UPS AND SCOPE POINTS

—E.C.—HISTORY—	—D. MACH.27RNB	
312922		
314419	FRAME 01	
	IBM CORP. SDD	AU001
DATE LAST EC	P.N. 1750142	030
10-06-76 315053		

- OP XXXX XXXX X111 XXXX — CA003BB0 — 2
- OP XXXX XXXX X000 XXXX — CA003B41 — 4
- CD TIME — CC001EJ2* — 6
- I1 XC TIME — CC003BH6 — 8
- I1 CD TIME — CC003BJ6 — 10
- I2 XC TIME — CC003CH6 — 12
- I2 CD TIME — CC003CJ6 — 14
- I3 XC TIME — CC003DH6 — 16
- CS1 XC TIME — CC003EH6 — 18
+ TO TIME — CC006AF2* 20
- ANY I TIME — CC008BM2 — 22
- CS1+CS2 TIME — CC008BN2 — 24
- OP X000 XXXX XXXX XXXX — CD001BH3 — 26
- RI INST TYPE — CD002CF6 — 28
- RR INST TYPE-BYTE — CD002CH6 — 30
+ OUTPUT IAR INST — CD002EK2 — 32
- L INST — CD003AH6 — 34
- RR INST TYPE-ADDRESS — CD003BN6 — 36
- INPUT + OUTPUT INST — CD003CK6 — 38
- RR INST TYPE-HALFWORD — CD003CM6 — 40
- IC+STC+LH+STH+L+ST INST — CD003DF2 — 42
- IC+LH+ICT INST — CD004AD2 — 44
- LS I-O REG ADDR — CD004AK6 — 46
- BAL+BALR+LA INST — CD004DM2 — 48
- PRE FORCE CONSTANT 00000 — CF002CM6 — 50
- PRESELECT LS REG 0 — CL002EH6 — 52
- GATE STATUS TO DISPLAY A — CU001EH6 — 54
- START CD — CU003GB6 — 56
- DISPLAY REGISTER CD — CU003GD6 — 58
- CYCLE STEAL CD — CU003GK6 — 60
- OP REG BIT 0.7 — DP992GA6 — 62

I1 CD TIME
10 ZJ11 — BA AR — 23K 103 — 3
A-B3J2

10 ZJ11 — AB A — 39H 109 — 2
26 -3HH
28 ZU10
A-B3J2

OP REG BIT 0.7
62 ZU12 — AH N — 01J 124 — 2
A-B3J2

6 ZD10 — AA A — 24E 137 — 1
24 ZG11 — +
56 ZD12
46 ZB11 — OR
58 ZJ02
A-B3J2

OUTPUT IAR INH
IBIT B REG CHK
10 ZJ11 — AG A PH — J03 148
32 ZB05 — CD
20 ZP04 — AR C
A-B3J2

CS CYCLE
TO DISPLAY A
24 ZG11 — BB A OR — G07 158
54 ZJ07 — U+
A-B3J2

I CYCLE
TO DISPLAY A
22 ZB09 — BC A OR — G09 165
54 ZJ07 — U+
A-B3J2

103 -36T — BH A OR
2 ZM11
103 -37U — A
4 -M10
A-B3J2 — 36V 206 — 23

34 ZP06 — CE A OR — 34E 230 — 1
16 ZM08 — +
42 -P07
26 -33F
103 -33E
A-B3J2

109 -03N — CM A OR — 04Q 302
62 ZU12
206 -04R — A
62 -U12
30 -U13
A-B3J2

109 38T — CK A OR — 39V 316
124 39T
124 39S — A
206 38S
30 U13
A-B3J2

230 — AC OR — 330
137 — DOT

A DIRECT
BITS 1.0-1.7
302 -29U — FF AR CR — 29T 404
206 -41H — A
40 ZM09
A-B3J2

A DIRECT
BITS 0.0-0.7
316 -26T — AE AR CR — 26U 418
206 -41H — A
40 ZM09
A-B3J2

A DIRECT
BITS X.6-1.7
330 -36K — AD AR CR — 36J 432
50 ZB02 — A
52 ZB03
18 -B07 — A
60 -B04
8 -S10 — A
28 -U10
12 ZS02 — A
44 ZM13
206 -41E — A
36 -U05
14 -S04 — A
48 -S03
10 ZJ11 — A
32 -B05
38 -D05
46 -B11
A-B3J2

432 + A DIRECT BITS X.6-1.7 — CA004-DB6
418 + A DIRECT BITS 0.0-0.7 — CA004-DK6
404 + A DIRECT BITS 1.0-1.7 — CA004-DL6
148 + OUTPUT IAR INHIBIT B REG CHK — FA2 (CK003)
124 + OP REG BIT 0.7 — HH2 (CA002 CA003)
103 - I1 CD TIME — CA003-HQ6
158 + CS CYCLE TO DISPLAY A — AP014-HS2
165 + I CYCLE TO DISPLAY A — AP014-HT2

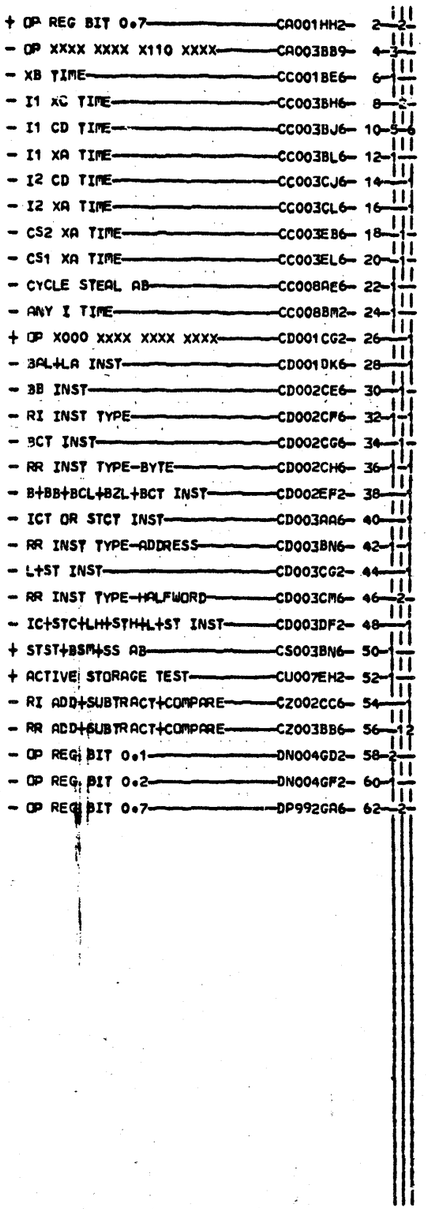
THIS PAGE IS FOR 370-II ONLY.

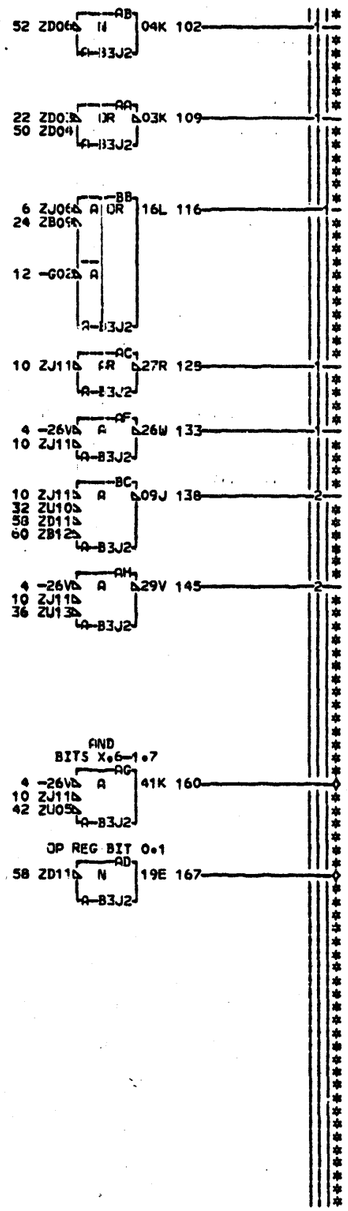
EDGE CONN.
6 RESISTOR
A-B1J2D10
20 RESISTOR
A-B3J2P04

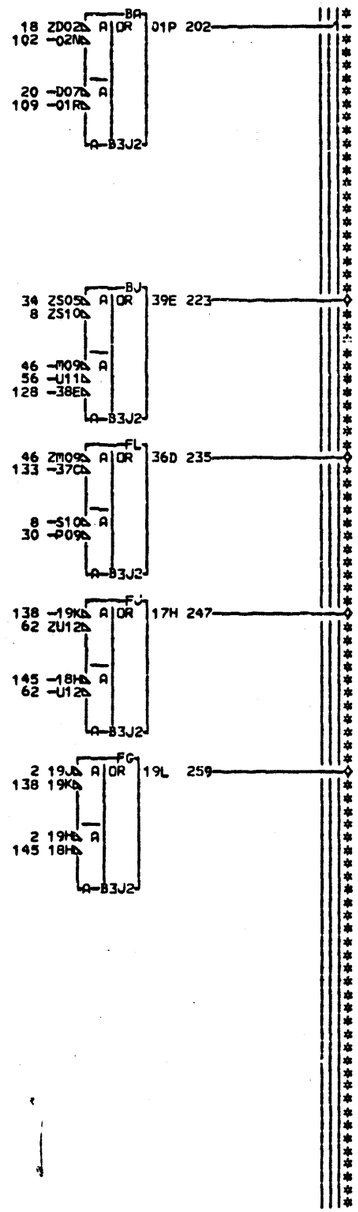
LOC. TYPE
A-B3J2 6815

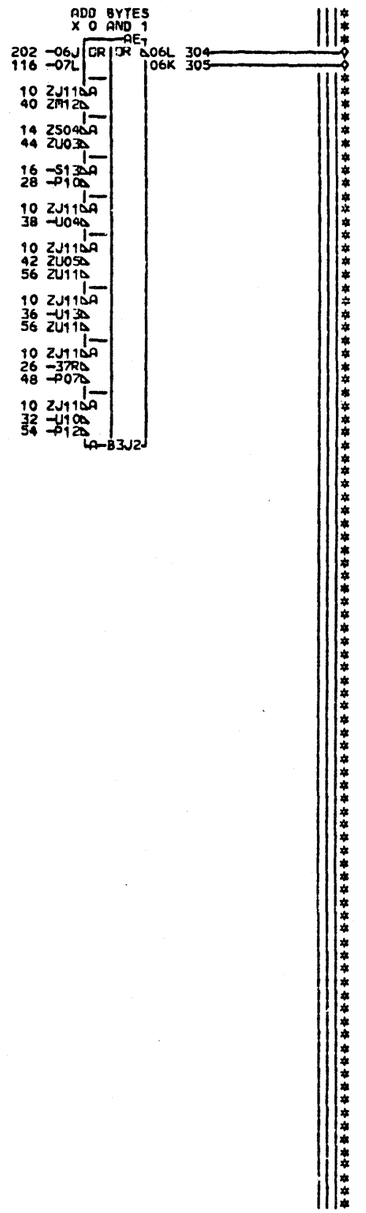
ALU CONTROLS
E.C.-HISTORY — D-MACH.27RNB
31.2922
31.4419
FRAME 01
DATE LAST EC
10-06-76 315053
IBM CORP.SDD — CA001
P.N. 1750143 — 030

CA001
030 SIM TO PN 5997582 EC 31026A









030 CA002

223 + ADD BYTES 0 AND 1 — CA004-BJ2

167 + OP REG BIT 0.1 — CA003-BN2

304 - ADD BYTES X 0 AND 1 — CA004-CC2

305 + ADD BYTES X 0 AND 1 — CA004-CC6

160 + AND BITS X.6-1.7 — CA004-DE2

259 + AND BITS 0.0-0.7 — CA004-FG2

247 + AND BITS 1.0-1.7 — CA004-FJ2

235 + AND BITS 0.0-1.7 — CA004-FL2

THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE
A-B3J2 6815

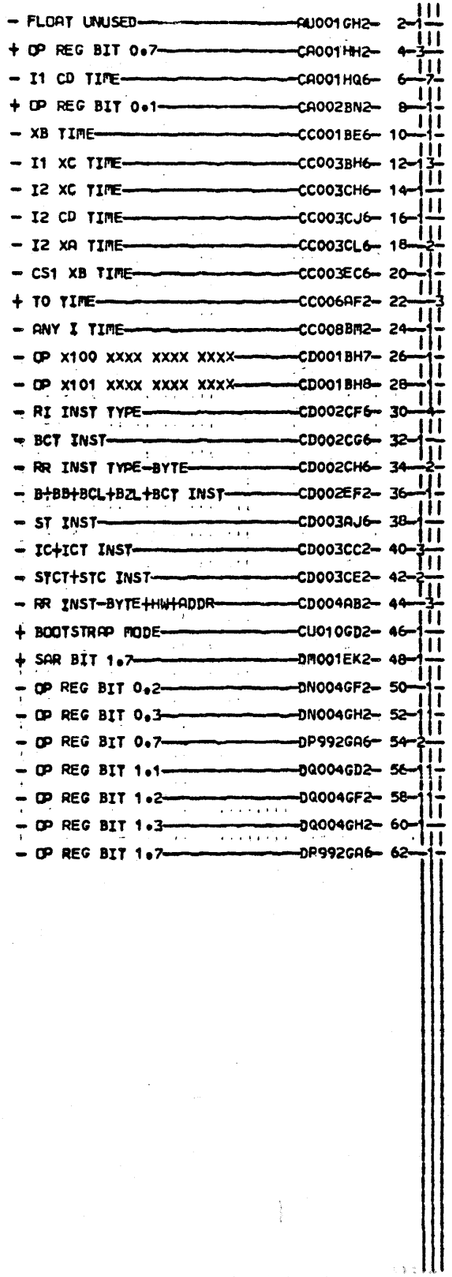
ALU CONTROLS

E.C.-HISTORY — D
312922
314419

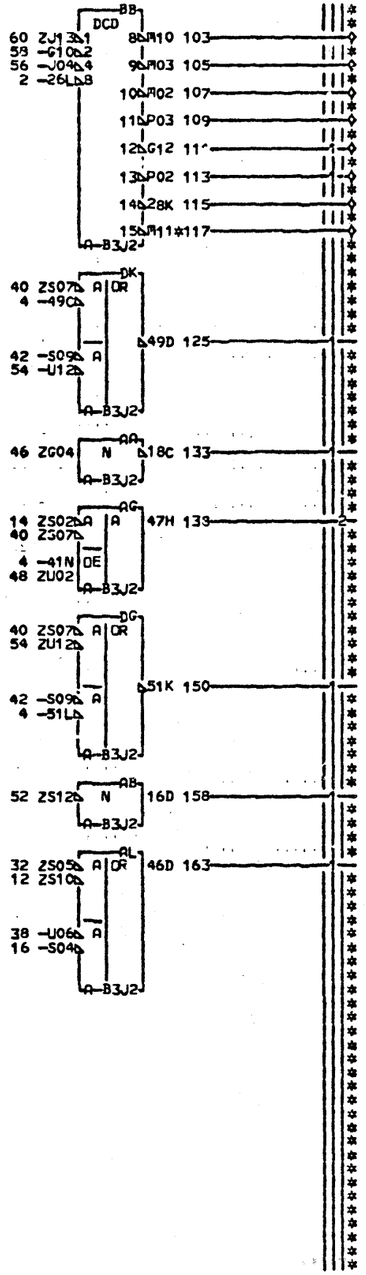
DATE LAST EC
03-06-76 315053

MACH.27 RNB
FRAME 01
IBM CORP. SDD CA002
P.N. 1750144 030

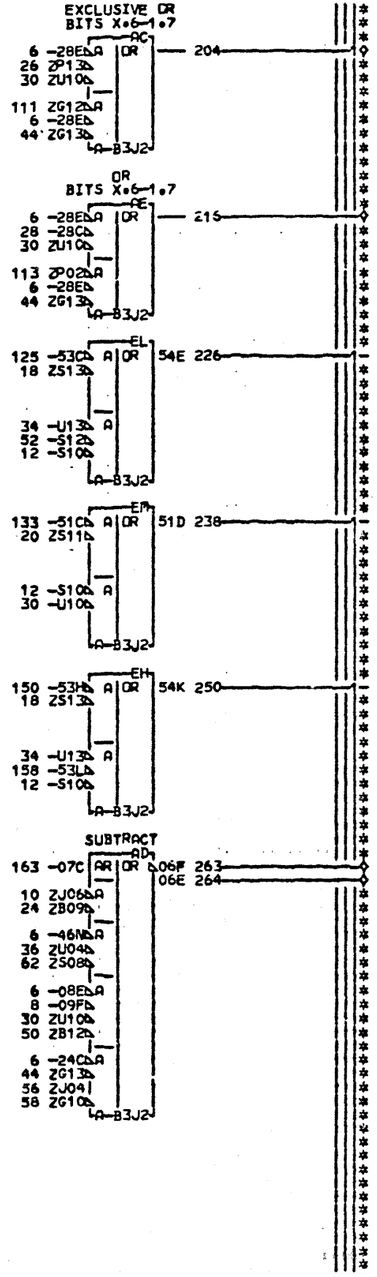
CA002
030 SIM TO PN 5997583 EC 309545



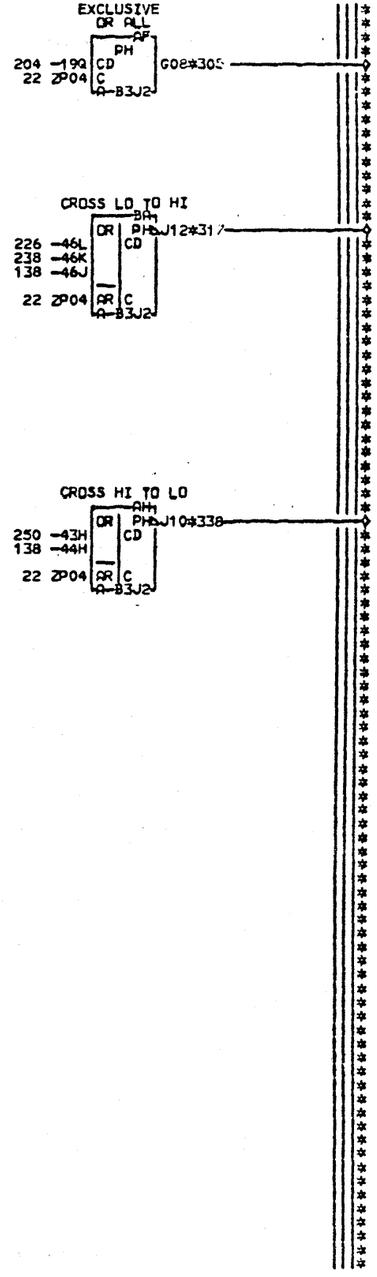
THIS PAGE IS FOR 3705-II ONLY.



EDGE CONN.
117 A-B3P1E11
01A-B4P6E02
305 A-B3H6A02
01A-B4M1A11
317 A-B3H6B02
01A-B4M1B11
338 A-B3H6A04
01A-B4M1A13



LOC. TYPE
A-BJ2 6815



117 - OP XXXX XXXX X111 XXXX — BB0
LCA001 LCF004

103 - OP XXXX XXXX X000 XXXX — BB3
◊CA001 LCD001 LCS003 LCU006
LCX001

105 - OP XXXX XXXX X001 XXXX — BB4
LCS007 LCX001

107 - OP XXXX XXXX X010 XXXX- CS007—BB5

109 - OP XXXX XXXX X011 XXXX — BB6
LCL003 LCS003

111 - OP XXXX XXXX X100 XXXX- CU015—BB7

113 - OP XXXX XXXX X101 XXXX- CU015—BB8

115 - OP XXXX XXXX X110 XXXX- CA002—BB9

204 + EXCLUSIVE OR BITS X.6-1.7 — BD4
LCA004

263 - SUBTRACT — CA004—BJ2

264 + SUBTRACT — CA004—BJ6

216 + OR BITS X.6-1.7 — CA004—CA4

305 + EXCLUSIVE OR ALL — HE6
LDF976 LDG976 LDK976

338 - CROSS HI TO LO — HH2
LDK976 LDL006 LDM006

317 - CROSS LO TO HI — HL2
LDG976 LDH016 LDJ016

ALU CONTROLS
-E.C- HISTORY -D- RACH.27RNB
312922
314419
FRAME 01
DATE LAST EC
10-06-76 315053
IBM CORP. SDD
P.N. 1750145
CA003
030

CA003
030 SIM TO PN 5997594 EC 310268

+ A DIRECT BITS X•6–1•7 ——— CA001DB6 — 2–8
+ A DIRECT BITS 0•0–0•7 ——— CA001DK6 — 8–3
+ A DIRECT BITS 1•0–1•7 ——— CA001DL6 — 14–3
+ ADD BYTES 0 AND 1 ——— CA002BJ2 — 20–2
– ADD BYTES X 0 AND 1 ——— CA002CC2 — 26–1
+ ADD BYTES X 0 AND 1 ——— CA002CC6 — 32–2
+ AND BITS X•6–1•7 ——— CA002DE2 — 38–5
+ AND BITS 0•0–0•7 ——— CA002FG2 — 44–2
+ AND BITS 1•0–1•7 ——— CA002FJ2 — 50–2
+ AND BITS 0•0–1•7 ——— CA002FL2 — 56–4
+ EXCLUSIVE OR BITS X•6–1•7 ——— CA003BD4 — 62–8
– SUBTRACT ——— CA003BJ2 — 68–1
+ SUBTRACT ——— CA003BJ6 — 74–1
+ OR BITS X•6–1•7 ——— CA003CA4 — 80–31
+ TO TIME ——— CC006AF2 — 86–64

THIS PAGE IS FOR 3705–II ONLY•

CA004
030 SIM TO PN 5997585 EC 310268

AB
2 48K 8 18V 38 49J 44 19W 56 18W 62 49L — OR — 19T 102
A–B3J2

AC
2 48K 14 18T 38 49J 50 17T 56 16T 62 49L — OR — 16W 111
A–B3J2

ALU OR CONTROL BYTE 1
BA
2 –49R 14 –14J 62 –49P 80 –48Q — OR — CD — PH — G05#122
86 ZP04 AR C
A–B3J2

ALU OR CONTROL BYTE 0
AH
2 –49R 8 –12H 62 –49P 80 –48Q — OR — CD — PH — G03#134
86 ZP04 AR C
A–B3J2

ALU OR CONTROL BYTE X
AG
62 –49P 80 –48Q — OR — CD — PH — B10#146
86 ZP04 AR C
A–B3J2

ALU AND CONTROL BYTE 1
AF
2 –48K 14 –18T 38 –49J 50 –17T 56 –16T 62 –49L — OR — CD — PH — D13#157
86 ZP04 AR C
A–B3J2

ALU AND CONTROL BYTE 0
AE
2 –48K 8 –18V 38 –49J 44 –19W 56 –18W 62 –49L — OR — CD — PH — U09#171
86 ZP04 AR C
A–B3J2

ALU CTRL REG
AA
2 –48K 38 –49J 62 –49L — OR — CD — PH — D09#184
86 ZP04 AR C
A–B3J2

EDGE CONN•
122 A–B3J6A02
01A–B4J1A11
134 A–B3H6E04
01A–B4H1E13
146 A–B3H6E02
01A–B4H1E11
157 A–B3H6D04
01A–B4H1D13
171 A–B3H6D02
01A–B4H1D11
184 A–B3H6C04
01A–B4H1C13
210 A–B3J6D02
01A–B4J1D11
211 A–B3J6D04
01A–B4J1D13
218 A–B3J6B04
01A–B4J1B13
225 A–B3J6C04
01A–B4J1C13
235 A–B3J6C02
01A–B4J1C11
304 A–B3J6E02
01A–B4J1E11

80 46P 102 19T 111 16W — AD — OR — 47R 202
A–B3J2

SUBTRACT A BUS
BE
68 –33N 86 ZP04 — PH — CD — C — M07#210 — P11#211
A–B3J2

ALU ADD CONTROL BYTE X
BB
26 –31G 86 ZP04 — PH — CD — C — P05#218
A–B3J2

ALU ADD CONTROL BYTE 1
BD
20 –44L 32 –43L — OR — CD — PH — U07#225
86 ZP04 AR C
A–B3J2

ALU ADD CONTROL BYTE 0
BC
20 –44L 32 –43L — OR — CD — PH — J05#235
86 ZP04 AR C
A–B3J2

LOC• TYPE
A–B3J2 6815

COMPLEMENT A BUS
EF
74 22N 62 22H 202 21H — AR — OR — J09* 304
A–B3J2

184 + ALU AND CONTROL BYTE X ——— BA2
↳DF008 ↳DF009

304 – COMPLEMENT A BUS ——— DD2
◇DF975 ↳DG975 ↳DH015 ↳DJ015
↳DK975 ↳DL005 ↳DM005

171 + ALU AND CONTROL BYTE 0 ——— EB6
◇DG008 ↳DG009 ↳DH008 ↳DH009
↳DH010 ↳DJ008 ↳DJ009 ↳DJ010

157 + ALU AND CONTROL BYTE 1 ——— EC6
◇DK008 ↳DK009 ↳DL008 ↳DL009
↳DL010 ↳DM008 ↳DM009 ↳DM010

146 + ALU OR CONTROL BYTE X ——— EE6
↳DF008 ↳DF009

134 + ALU OR CONTROL BYTE 0 ——— EF6
◇DG008 ↳DG009 ↳DH008 ↳DH009
↳DH010 ↳DJ008 ↳DJ009 ↳DJ010

122 + ALU OR CONTROL BYTE 1 ——— EG6
◇DK008 ↳DK009 ↳DL008 ↳DL009
↳DL010 ↳DM008 ↳DM009 ↳DM010

218 + ALU ADD CONTROL BYTE X ——— EH6
↳DF008 ↳DF009 ↳DF974

235 + ALU ADD CONTROL BYTE 0 ——— EJ6
◇DG008 ↳DG009 ↳DG974 ↳DH008
◇DH009 ↳DH010 ↳DH014 ↳DJ008
↳DJ009 ↳DJ010 ↳DJ014

225 + ALU ADD CONTROL BYTE 1 ——— EK6
◇DK008 ↳DK009 ↳DK974 ↳DL004
◇DL008 ↳DL009 ↳DL010 ↳DM004
↳DM008 ↳DM009 ↳DM010

210 – SUBTRACT A BUS ——— EM2
↳DM006 ↳DM007

211 + SUBTRACT A BUS ——— DM006–EM6

ALU CONTROLS

E•C•	HISTORY	D•	MACH•27RNB	
312922				
314419			FRAME 01	
			IBM CORP•SDD	CA004
DATE	LAST EC		P•No• 1750146	030
10–06–76	315053			

+ FET MEMORY INSTALLED ——— AJ001AB4 — 2 — 12

— GATE A TIME ——— CC005CL6 — 12 — 1

+ T0 TIME ——— CC006AF1 — 22 — 3 — 3

+ T2 TIME ——— CC006DF1 — 32 — 3 — 2

— GATE STATUS TO DISPLAY A ——— CU001EH6 — 42

A TIME (AC, PH): 12 24Q CD, 22 21P C, A-B3R2J, U → 24N 104, 24P 105, P04* 106
B TIME (AE, PH): 506 23N CD, 22 21P C, A-B3R2J → 21N 113, B13 114
C TIME (AG, PH): 518 ZB02 CD, 22 16U C, A-B3R2J → 19T 120, J09 121
AA: 2 ZD03 N, A-B3R2J → 126

AX TIME (BA, PH): 106 ZD11 CD, 32 06U C, A-B3R2J → 09T 204, J09U 205, S11 206
BX TIME (BC, PH): 113 08T CD, 32 06U C, A-B3R2J → 07T 213, J06T 214, J04 215
CD TIME (BE, PH): 120 19T CD, 32 11U C, A-B3R2J → 14T 222, G10 223

E TIME (AD, PH): 204 09T CD, 22 21P C, A-B3R2J → 23Q 304
F TIME (AF, PH): 213 07T CD, 22 16U C, A-B3R2J → 16T 311
D TIME (AH, PH): 222 14T CD, 22 16U C, A-B3R2J → 19W 318, J10 319

EB TIME (BB, PH): 304 08W CD, 32 06U C, A-B3R2J → 09W 404, J08V 405
FC TIME (BD, PH): 311 13T CD, 32 11U C, A-B3R2J → 12T 411, J11T 412
CA: 113 -21N OR, 318 -19W → 17N 416; 2 ZD03 A, 311 -23T, A-B3R2J

405 -01T A OR GN, 2 ZD03 → J1V 506
205 -02U A, 126 -01U, A-B3R2J
2 ZD03 A OR BJ, 412 -04T → B02 518
126 -04U A, 214 -03U, A-B3R2J

B+F+D TIME TO DISPLAY A (BH): 42 ZG03 A OR G04 528, 416 -16C, A-B3R2J
DA TIME (BF, PH): 318 19W CD, 32 11U C, A-B3R2J, U → 14W 535, J13V 536, J07 537
MEM STORE NEW TIME (BG): 311 16T, 213 16R, 411 16P OR → J03 544, A-B3R2J
XB TIME (AB): 506 48Q AR → S05* 551, A-B3R2J, U

551 — XB TIME ——— BE6
◇CA002 ↳CA003 ↳CC003 ↳CP001 ◇CS004 ↳CS006 ↳CU003 ↳CU004 ↳CU007 ↳CU014
518 — XC TIME ——— BJ6
↳CC003 ↳CQ001
104 + A TIME ——— CC007-CA2
105 — A TIME ——— CQ002-CA6
113 — B TIME ——— CQ002-CE6
311 — F TIME ——— CC007-CG6
120 + C TIME ——— CQ002-CJ2
318 + D TIME ——— CD002-CL2
204 + AX TIME ——— CC005-DB2
404 + EB TIME ——— CC005-DD2
405 — EB TIME ——— CC005-DD6
213 + BX TIME ——— CC005-DF2
411 + FC TIME ——— CC005-DH2
412 — FC TIME ——— CC005-DH6
222 + CD TIME ——— CC005-DK2
535 + DA TIME ——— CD002-DM2
536 — DA TIME ——— DM6
↳CC005 ↳CS007
206 — AX TIME ——— EC2
◇CC003 ↳CK001 ↳CL005 ↳CS001 ↳CS003 ↳CS006 ↳CS007
114 — B TIME ——— EE2
↳CC002 ↳CK001
215 — BX TIME ——— EG2
◇CC003 ↳CC005 ↳CK003 ↳CS002 ◇CS005 ↳CS006 ↳CS007 ↳CU003 ↳CU004 ↳CU006
223 — CD TIME ——— EJ2
◇CA001 ↳CC003 ↳CK001 ↳CM001 ◇CP002 ↳CP003 ↳CQ001 ↳CS006 ↳CU014
537 — DA TIME ——— EL2
◇CC003 ↳CC008 ↳CS001 ↳CS004 ↳CS006 ↳CU007
106 — A TIME ——— FC2
↳CC002 ↳CC008 ↳CK003 ↳CV001
544 — MEM STORE NEW TIME ——— CM001-FE2
121 — C TIME ——— FG2
◇CC002 ↳CC008 ↳CD003 ↳CL004 ↳CP001 ↳CQ001 ↳CR001 ↳CU010
319 — D TIME ——— FJ2
◇CC002 ↳CL004 ↳CM001 ↳CQ001 ↳CX001 ↳CX011
528 + B+F+D TIME TO DISPLAY A AP015-GF2

THIS PAGE IS FOR 3705-II ONLY.

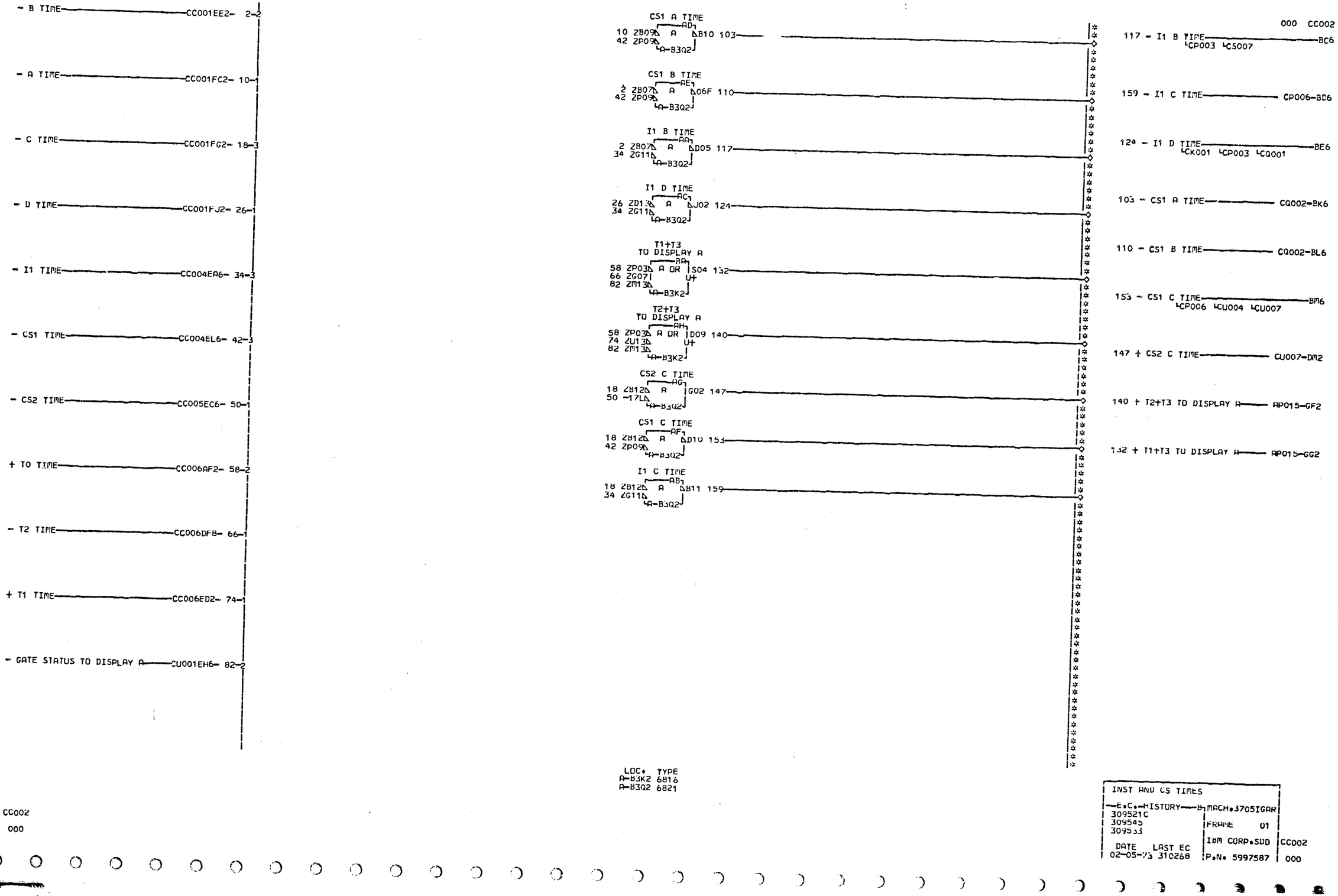
EDGE CONN.
106 A-B3N6A04
01A-B4N1A13
551 RESISTOR
A-B3J2J06

LOC. TYPE
A-B3J2 6815
A-B3R2 AB92

ABCD COUNTER

E.C.-HISTORY	D	MACH.27RNB
312922		
314419		FRAME 01
		IBM CORP.SDD CC001
DATE LAST EC		P.N. 1750147 030
10-06-76 315053		

CC001
030 SIM TO PN 5997586 EC 310268


- B TIME CC001EE2- 2-2
- A TIME CC001FC2- 10-1
- C TIME CC001FG2- 18-3
- D TIME CC001FJ2- 26-1
- I1 TIME CC004EA6- 34-3
- CS1 TIME CC004EL6- 42-3
- CS2 TIME CC005EC6- 50-1
+ TO TIME CC006AF2- 58-2
- T2 TIME CC006DF8- 66-1
+ T1 TIME CC006ED2- 74-1
- GATE STATUS TO DISPLAY A CU001EH6- 82-2
CS1 A TIME
10 ZB09 A AD B10 103
42 ZP09 A-B3Q2
CS1 B TIME
2 ZB07 A AE O6F 110
42 ZP09 A-B3Q2
I1 B TIME
2 ZB07 A AA D05 117
34 ZG11 A-B3Q2
I1 D TIME
26 ZD13 A AC J02 124
34 ZG11 A-B3Q2
T1+T3 TO DISPLAY A
58 ZP03 A OR BA S04 132
66 ZG07 U+
82 ZM13 A-B3K2
T2+T3 TO DISPLAY A
58 ZP03 A OR AH D09 140
74 ZU13 U+
82 ZM13 A-B3K2
CS2 C TIME
18 ZB12 A AG G02 147
50 -17LD A-B3Q2
CS1 C TIME
18 ZB12 A AF D10 153
42 ZP09 A-B3Q2
I1 C TIME
18 ZB12 A AB B11 159
34 ZG11 A-B3Q2
000 CC002
117 - I1 B TIME BC6
CP003 CS007
159 - I1 C TIME CP006-BD6
124 - I1 D TIME BE6
CK001 CP003 CQ001
103 - CS1 A TIME CQ002-BK6
110 - CS1 B TIME CQ002-BL6
153 - CS1 C TIME BM6
CP006 CU004 CU007
147 + CS2 C TIME CU007-DM2
140 + T2+T3 TO DISPLAY A AP015-GF2
132 + T1+T3 TO DISPLAY A AP015-GG2
LOC. TYPE
A-B3K2 6816
A-B3Q2 6821
INST AND CS TIMES
E.C.-HISTORY- BY
309521C MACH.3705IGAR
309545
309533
DATE LAST EC
02-05-73 310268
FRAME 01
IBM CORP.SDD CC002
P.N. 5997587 000
CC002
000

- XB TIME — CC001BE6 — 2
- XC TIME — CC001BJ6 — 7
- AX TIME — CC001EC2 — 12
- BX TIME — CC001EG2 — 17
- CD TIME — CC001EJ2 — 22
- DA TIME — CC001EL2 — 27
- GO SECOND INST CYCLE — CC004CE6 — 32
- GO SECOND CS CYCLE — CC004CN6 — 37
- GO FIRST INSTR CYCLE — CC004DA6 — 42
- GO FIRST CS CYCLE — CC004DL6 — 47
- I1 TIME — CC004EA6 — 52
- I2 TIME — CC004EE6 — 57
- CS1 TIME — CC004EL6 — 62
- I1 TIME + 125 NS — CC004FB6 — 67
- I2 TIME + 125 NS — CC004FF6 — 72
- I3 TIME + 125 NS — CC004FK6 — 77
- CS1 TIME + 125 NS — CC004FM6 — 82
- CS2 TIME — CC005EC6 — 87

37 / 87 — DB *THRU* NOTE 1 — 104

CS1 CD TIME — EJ
22 ZJ05 A — J07 110
82 ZG04 B3Q2

I3 CD TIME — DJ
22 ZJ05 A — B02 117
77 ZD06 B3Q2

I2 CD TIME — CJ
22 ZJ05 A — G13*124
72 ZD02 U B3Q2

I1 CD TIME — BJ
22 ZJ05 A — P04*131
67 ZU03 U B3Q2

ED
17 ZG05 A — G07 137
82 ZG04 B3Q2

I2 AX TIME — CB
12 ZM12 A — G08 145
57 ZG10 B3Q2

I1 AX TIME — BB
12 ZM12 A — J09 152
52 ZG11 B3Q2

CS1 XC TIME — EH
7 ZP05 A — G03 159
82 ZG04 B3Q2

I3 XC TIME — DH
7 ZP05 A — J04 166
77 ZD06 B3Q2

I2 XC TIME — CH
7 ZP05 A — J12*173
72 ZD02 U B3Q2

I1 XC TIME — BH
7 ZP05 A — U05*180
67 ZU03 U B3Q2

CS1 XB TIME — EC
2 ZJ10 A — U11 187
62 ZP09 B3Q2

CS2 XA TIME — EB
104 -33K A — P03*203
27 ZB13 U B3Q2

I1 XB TIME — BC
2 ZJ10 A — G12 209
52 ZG11 B3Q2

CS1 XA TIME — EL
27 ZB13 A — S04*215
47 -29R U B3Q2

I3 DA TIME — DK
27 ZB13 A — D12 221
77 ZD06 B3Q2

I2 XA TIME — CL
27 ZB13 A — M02*227
32 -28M U B3Q2

I2 DA TIME — CK
27 ZB13 A — B04 233
72 ZD02 B3Q2

I1 XA TIME — BL
27 ZB13 A — P12*239
42 -34R U B3Q2

I1 DA TIME — BK
27 ZB13 A — M08 245
67 ZU03 U B3Q2

030 CC003

152 — I1 AX TIME — CL005—BB6

209 — I1 XB TIME — CU004—BC6

180 — I1 XC TIME — BH6
◊CA001 ↳CA002 ↳CA003 ↳CF002
↳CL001 ↳CL002 ↳CL003 ↳CP003

131 — I1 CD TIME — BJ6
◊CA001 ↳CA002 ↳CF003 ↳CL001
◊CL002 ↳CL003 ↳CL005 ↳CQ001
↳CS001

245 — I1 DA TIME — BK6
↳CK001 ↳CL005 ↳CZ002

239 — I1 XA TIME — BL6
◊CA002 ↳CF002 ↳CP002 ↳CP004
↳CS005

145 — I2 AX TIME — CL005—CB6

173 — I2 XC TIME — CH6
◊CA001 ↳CA003 ↳CF003 ↳CL001
↳CL002 ↳CS004

124 — I2 CD TIME — CJ6
◊CA001 ↳CA002 ↳CA003 ↳CF002
◊CF003 ↳CL001 ↳CL002 ↳CL005
↳CZ002

233 — I2 DA TIME — CL005—CK6

227 — I2 XA TIME — CL6
◊CA002 ↳CA003 ↳CF002 ↳CL001
↳CL002 ↳CL003 ↳CS004

166 — I3 XC TIME — DH6
↳CA001 ↳CL001 ↳CL002

117 — I3 CD TIME — DJ6
↳CL005 ↳CZ002

221 — I3 DA TIME — CL005—DK6

203 — CS2 XA TIME — EB6
↳CA002 ↳CF002

187 — CS1 XB TIME — EC6
↳CA003 ↳CA002 ↳CS004

137 UNUSED — ED6

159 — CS1 XC TIME — EH6
↳CA001 ↳CS004

110 — CS1 CD TIME — EJ6
↳CA001 ↳CA002

215 — CS1 XA TIME — EL6
↳CA002 ↳CF002 ↳CS004

THIS PAGE IS FOR 3705-II ONLY.

NOTE 1
THRU BLK VS CARD PN
TOP INPUT TO OUTPUT
CONNECTED PN 8239399
BOTTOM INPUT TO OUTPUT
CC003 CONNECTED PN 5857421

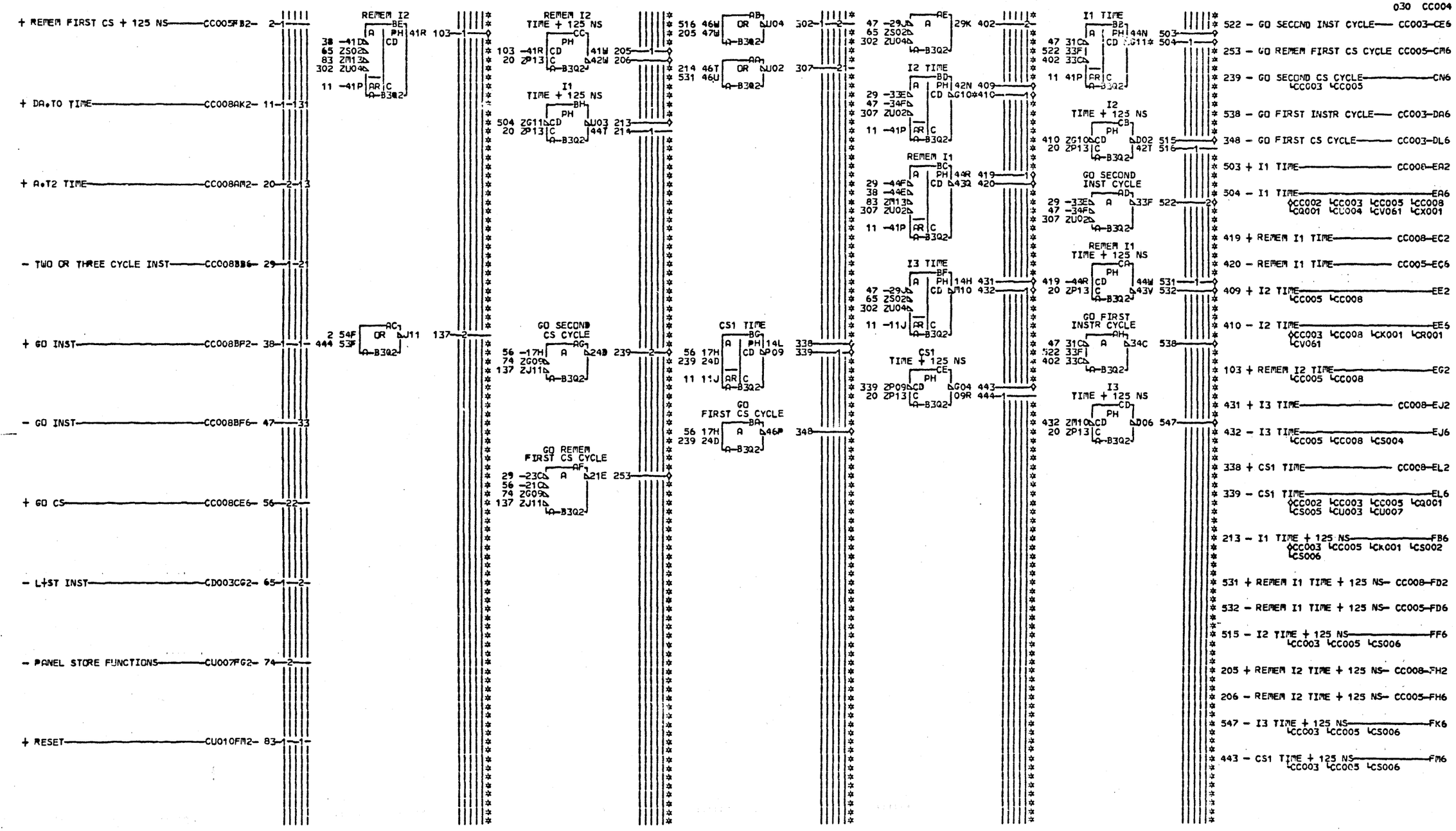
030 SIM TO PN 5997588 EC 312913

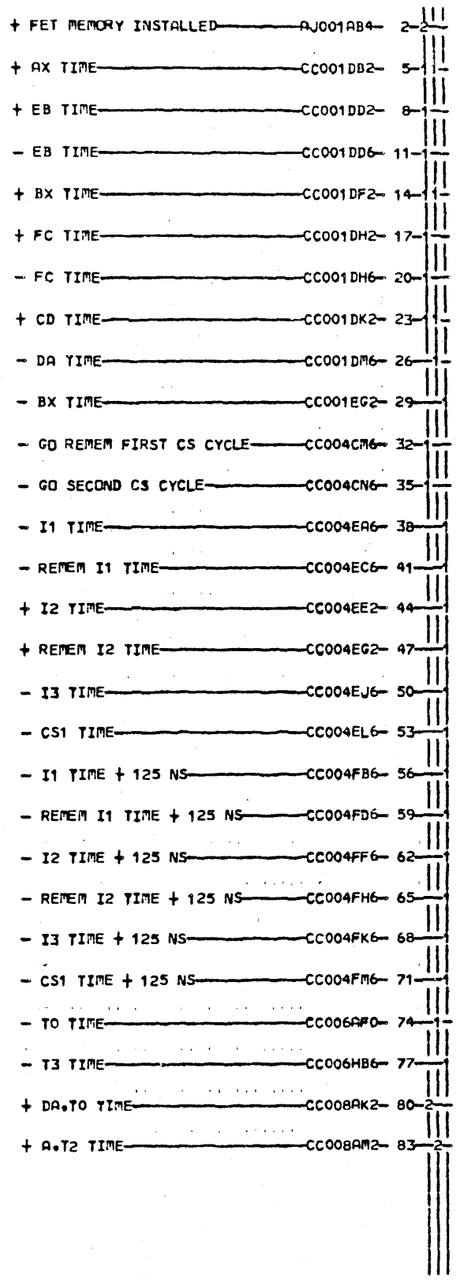
EDGE CONN. A-B4R2G04 A-B4R2G09
124 A-B3V3B09 01A-B3V3B12 01A-B3V3B06
01A-B4V3B09 01A-B4V3B12 01A-B4V3B06
131 A-B3V3B05 215 RESISTOR
01A-B4V3B05 A-B4R2J13
173 A-B3V3B08 01A-B3V3B13
01A-B4V3B08 01A-B4V3B13
180 A-B3V3B04 227 A-B3V3B10
01A-B4V3B04 01A-B3V3B10
203 RESISTOR 239 RESISTOR

LOC. TYPE
A-B3Q2 6821
A-B4R2 6807

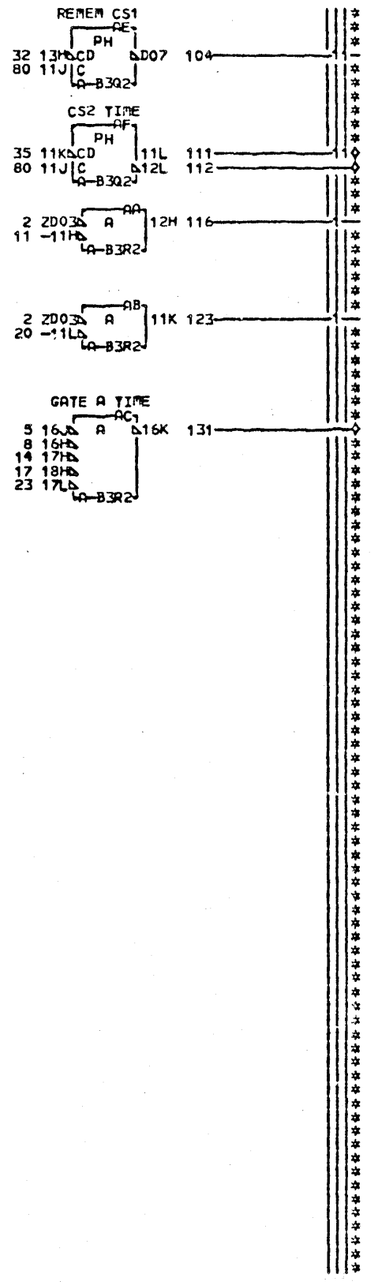
INST AND CS TIMES

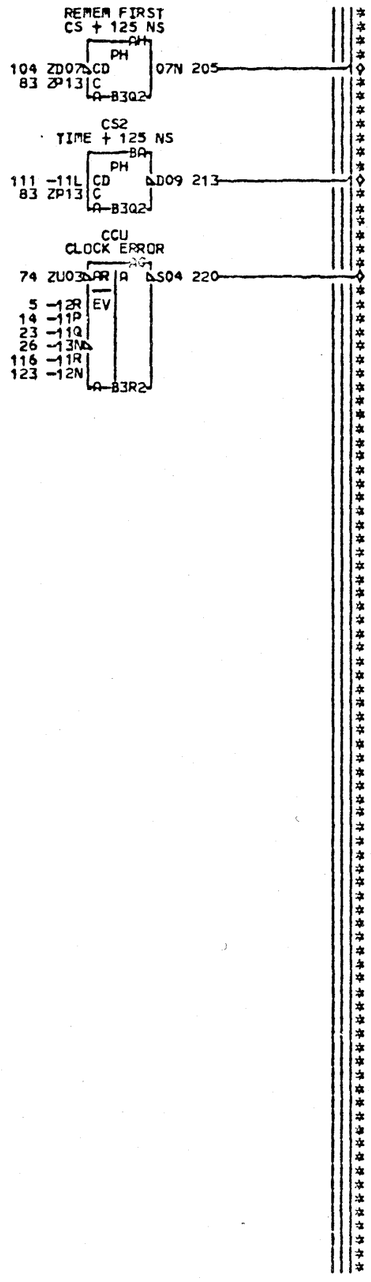
—E.C.—HISTORY—D— MACH.27RNB
312922
314419 FRAME 01
IBM CORP.SDD CC003
DATE LAST EC P.N. 1750148 030
08-31-76 315053

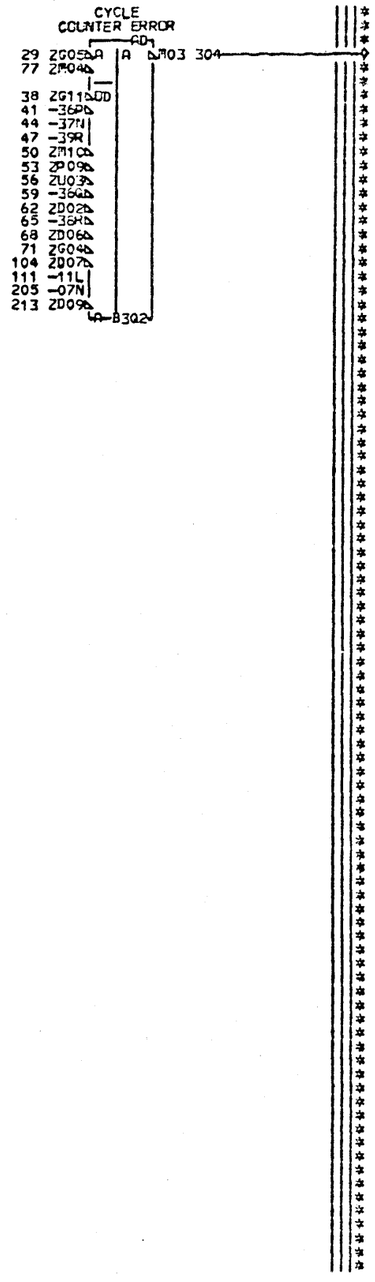

030 CC004
+ REMEM FIRST CS + 125 NS — CC005FB2- 2-1
+ DA.TO TIME — CC008AK2- 11-1-13
+ A.T2 TIME — CC008AM2- 20-2-13
- TWO OR THREE CYCLE INST — CC008BB6- 29-1-21
+ GO INST — CC008BP2- 38-1-1-444 53F
- GO INST — CC008BF6- 47-33
+ GO CS — CC008CE6- 56-22
- LAST INST — CD003CG2- 65-1-2
- PANEL STORE FUNCTIONS — CU007FG2- 74-2
+ RESET — CU010FM2- 83-1-1
REMEM I2
REMEM I2 TIME + 125 NS
I1 TIME + 125 NS
GO SECOND CS CYCLE
GO REMEM FIRST CS CYCLE
CS1 TIME
GO FIRST CS CYCLE
I2 TIME
REMEM I1 TIME + 125 NS
I3 TIME
CS1 TIME + 125 NS
I1 TIME
I2 TIME + 123 NS
GO SECOND INST CYCLE
GO FIRST INSTR CYCLE
I3 TIME + 125 NS
522 - GO SECOND INST CYCLE — CC003-CE6
253 - GO REMEM FIRST CS CYCLE CC005-CM6
239 - GO SECOND CS CYCLE — CN6
CC003 CC005
538 - GO FIRST INSTR CYCLE — CC003-DA6
348 - GO FIRST CS CYCLE — CC003-DL6
503 + I1 TIME — CC008-EA2
504 - I1 TIME — EA6
CC002 CC003 CC005 CC008 CQ001 CC004 CV061 CX001
419 + REMEM I1 TIME — CC008-EC2
420 - REMEM I1 TIME — CC005-EC6
409 + I2 TIME — EE2
CC005 CC008
410 - I2 TIME — EE6
CC003 CC008 CK001 CR001 CV061
103 + REMEM I2 TIME — EG2
CC005 CC008
431 + I3 TIME — CC008-EJ2
432 - I3 TIME — EJ6
CC005 CC008 CS004
338 + CS1 TIME — CC008-EL2
339 - CS1 TIME — EL6
CC002 CC003 CC005 CQ001 CS005 CU003 CU007
213 - I1 TIME + 125 NS — FB6
CC003 CC005 CK001 CS002 CS006
531 + REMEM I1 TIME + 125 NS - CC008-FD2
532 - REMEM I1 TIME + 125 NS - CC005-FD6
515 - I2 TIME + 125 NS — FF6
CC003 CC005 CS006
205 + REMEM I2 TIME + 125 NS - CC008-FH2
206 - REMEM I2 TIME + 125 NS - CC005-FH6
547 - I3 TIME + 125 NS — FK6
CC003 CC005 CS006
443 - CS1 TIME + 125 NS — FM6
CC003 CC005 CS006
EDGE CONN.
410 A-B3V3D03
01A-B4V3D03
504 A-B3V3D02
01A-B4V3D02
LOC. TYPE
A-B3Q2 6821
THIS PAGE IS FOR 3705-II ONLY.
INST AND CS CYCLE COUNTER
E.C.-HISTORY
312922
314419
DATE LAST EC
10-06-76 315053
MACH.27RNB
FRAME 01
IBM CORP.SDD
P.No. 1750149
CC004
030
CC004
030 SIM TO PN 5997589 EC 312913



THIS PAGE IS FOR 3705-II ONLY.







030 CC005

131 - GATE A TIME — CC001-CL6

304 - CYCLE COUNTER ERROR — DG6
CK006 CK007

111 + CS2 TIME — CC008-EC2

112 - CS2 TIME — EC6
CC002 CC003

220 - CCU CLOCK ERROR — EM6
CK006 CK007

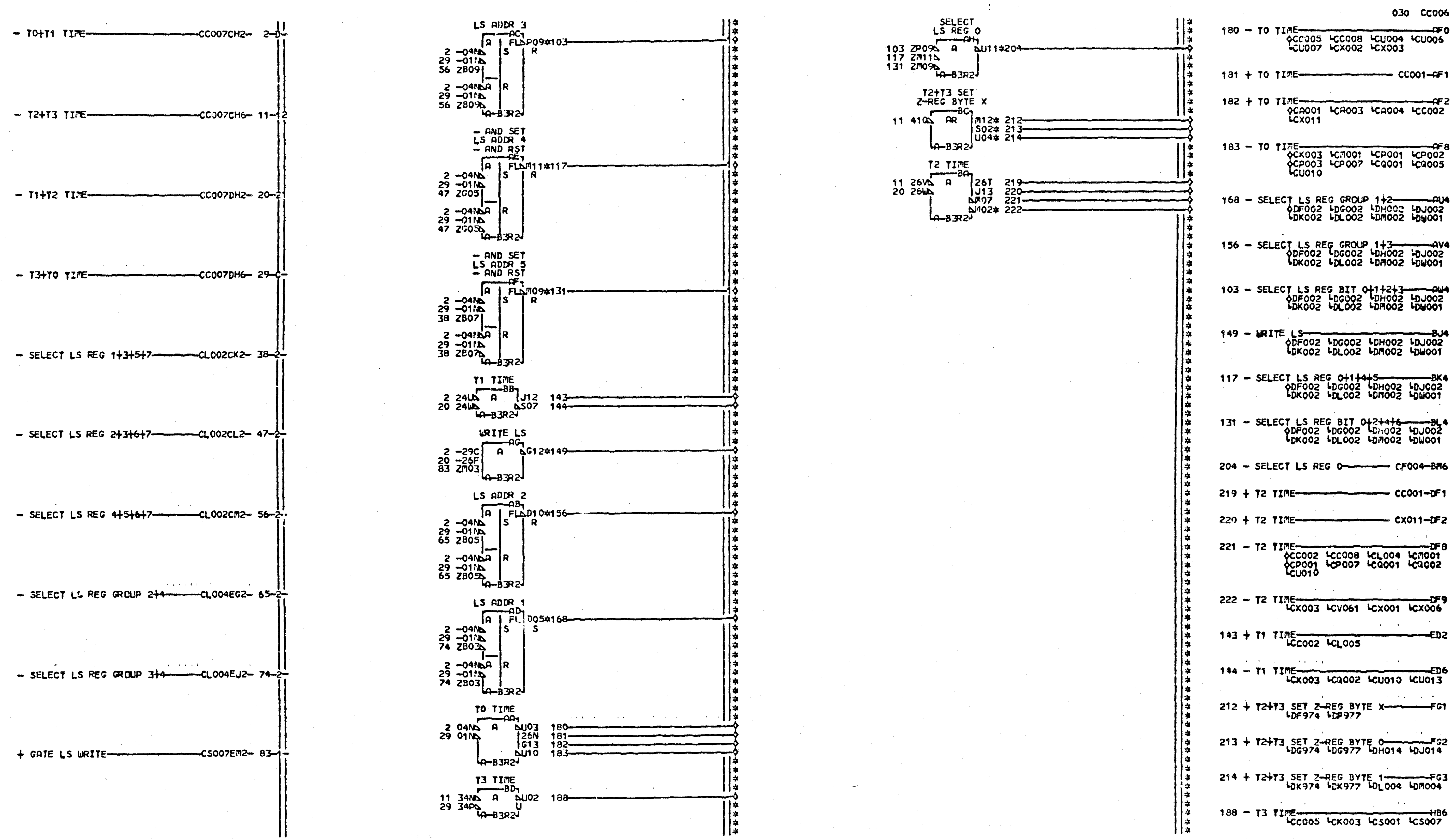
205 + REMEM FIRST CS + 125 NS CC004-FB2

213 - CS2 TIME + 125 NS — FD6
CS003 CS006

LCC. TYPE
A-B3Q2 6821
A-B3R2 AB92

INST AND CS CYCLE COUNTER ERROR DETECTION
E.C.-HISTORY — D MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD CC005
DATE LAST EC
10-06-76 315053
P.N. 1750150 030

CC005
030 SIM TO P/N 5997390 EC 312913

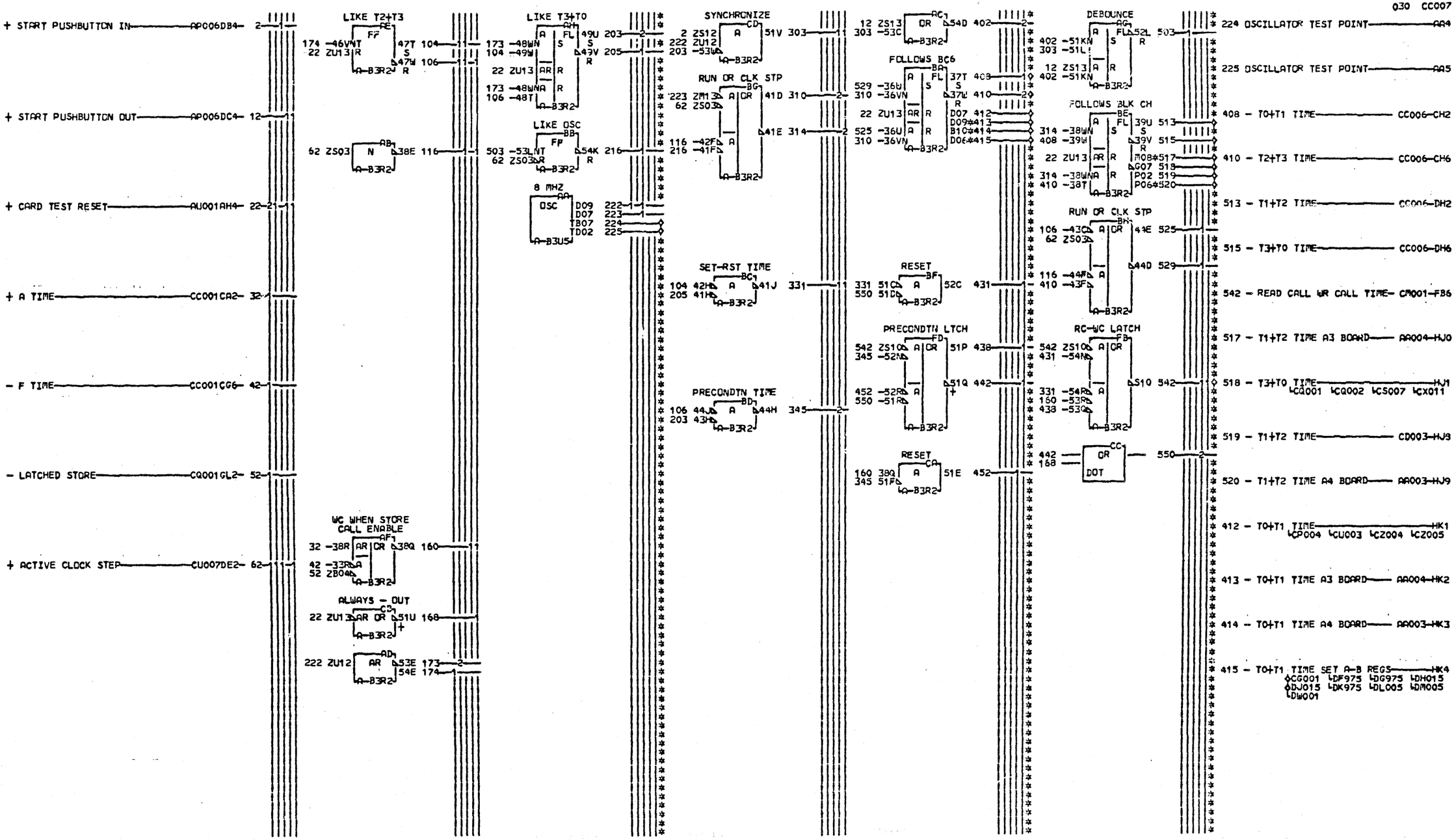


THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
103 A-B3R6B04 01A-B4R1B13
117 A-B3R6C04 01A-B4R1C13
131 A-B3R6D02 01A-B4R1D11
149 A-B3R6C02 01A-B4R1C11
156 A-B3R6B02 01A-B4R1B11
168 A-B3R6A04 01A-B4R1A13
204 A-B3N6B04 01A-B4N1B13
212 A-B3N6D04 01A-B4N1D13
213 A-B3N6E02 01A-B4N1E11
214 A-B3N6E04 01A-B4N1E13
222 A-B3N6B02 01A-B4N1B11

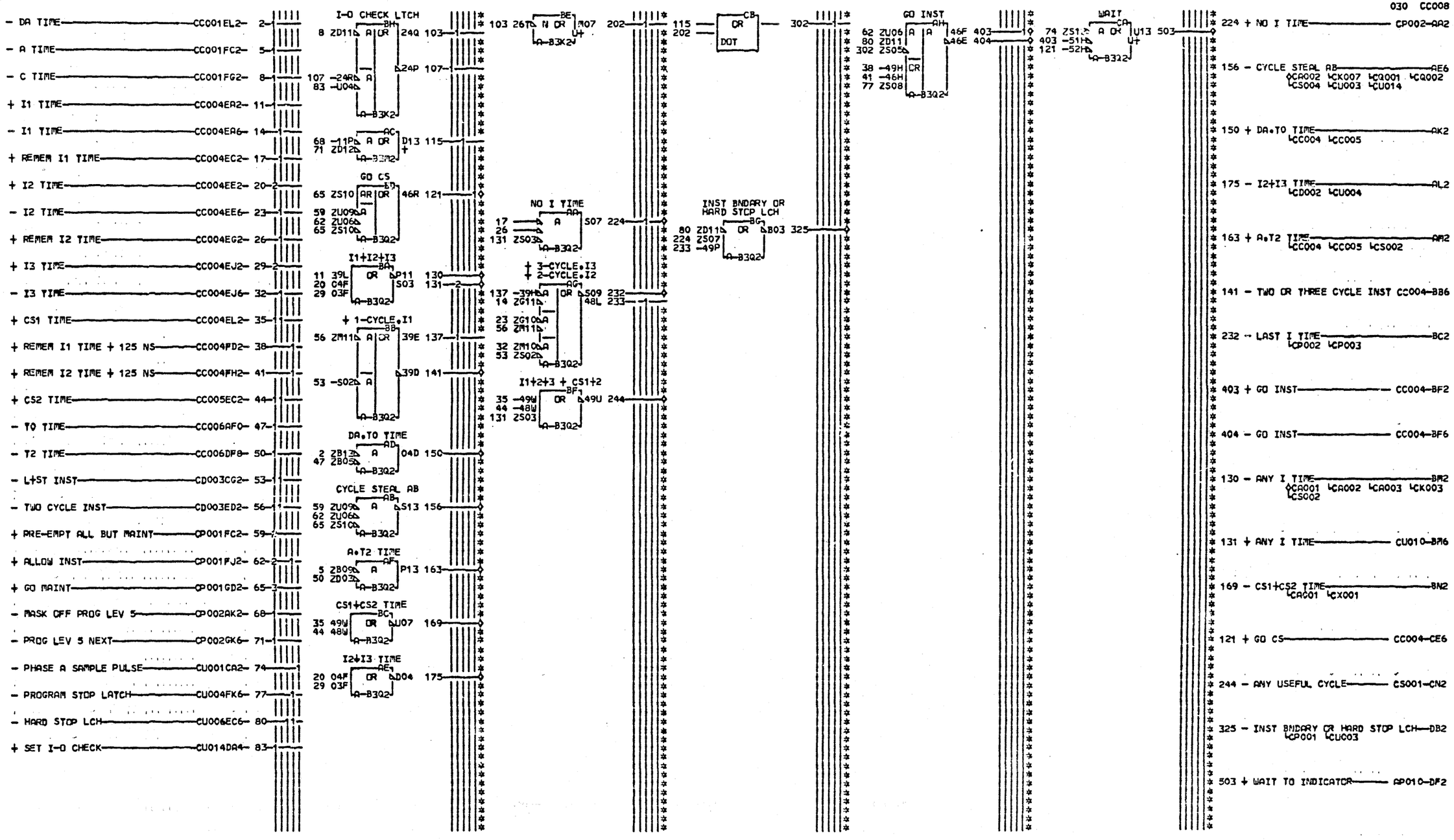
LOC. TYPE
A-B3R2 AB92

LS ADDR REG AND T-TIMES
E.C.-HISTORY 312922 314419
D. MACH.27RNB
FRAME 01
IBM CORP.SDD CC006
DATE 08-31-76 LAST EC 315053
P.N. 1750151 030



CC007

030 SIM TO PN 5997592 EC 310268



030 CC008

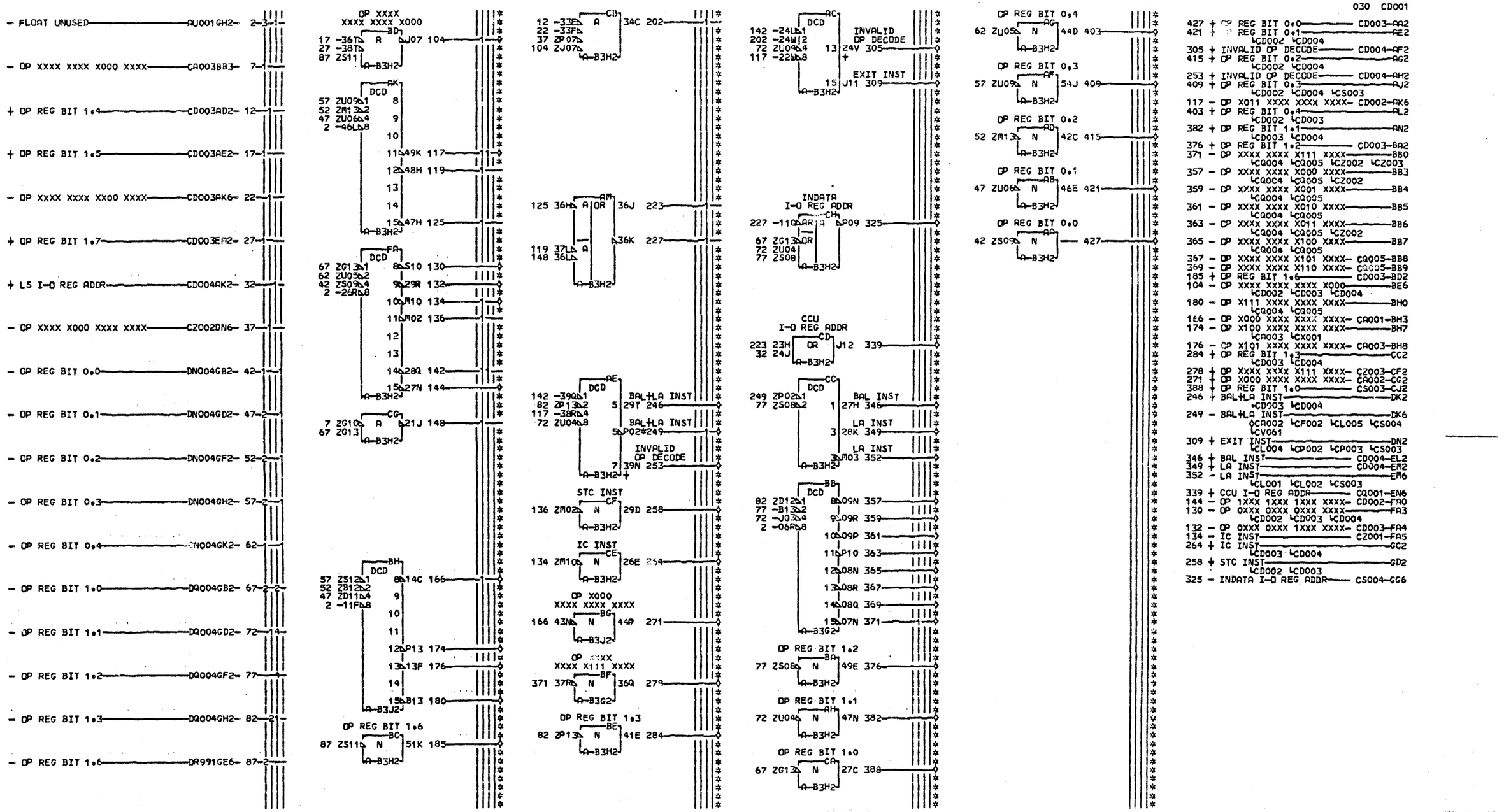
THIS PAGE IS FOR 3705-II ONLY.

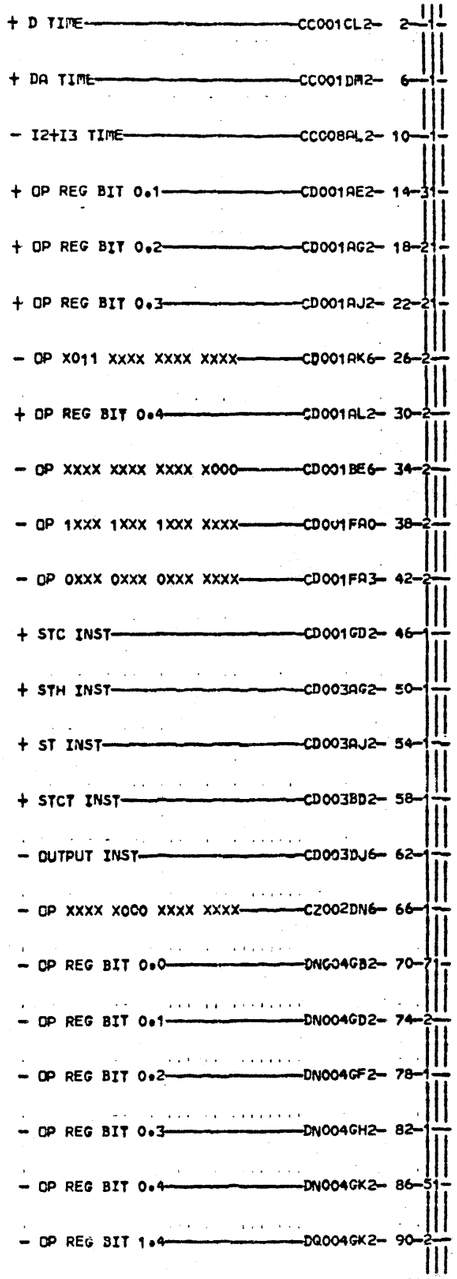
LOC. TYPE
A-B3K2 6816
A-B3M2 6818
A-B3Q2 6821

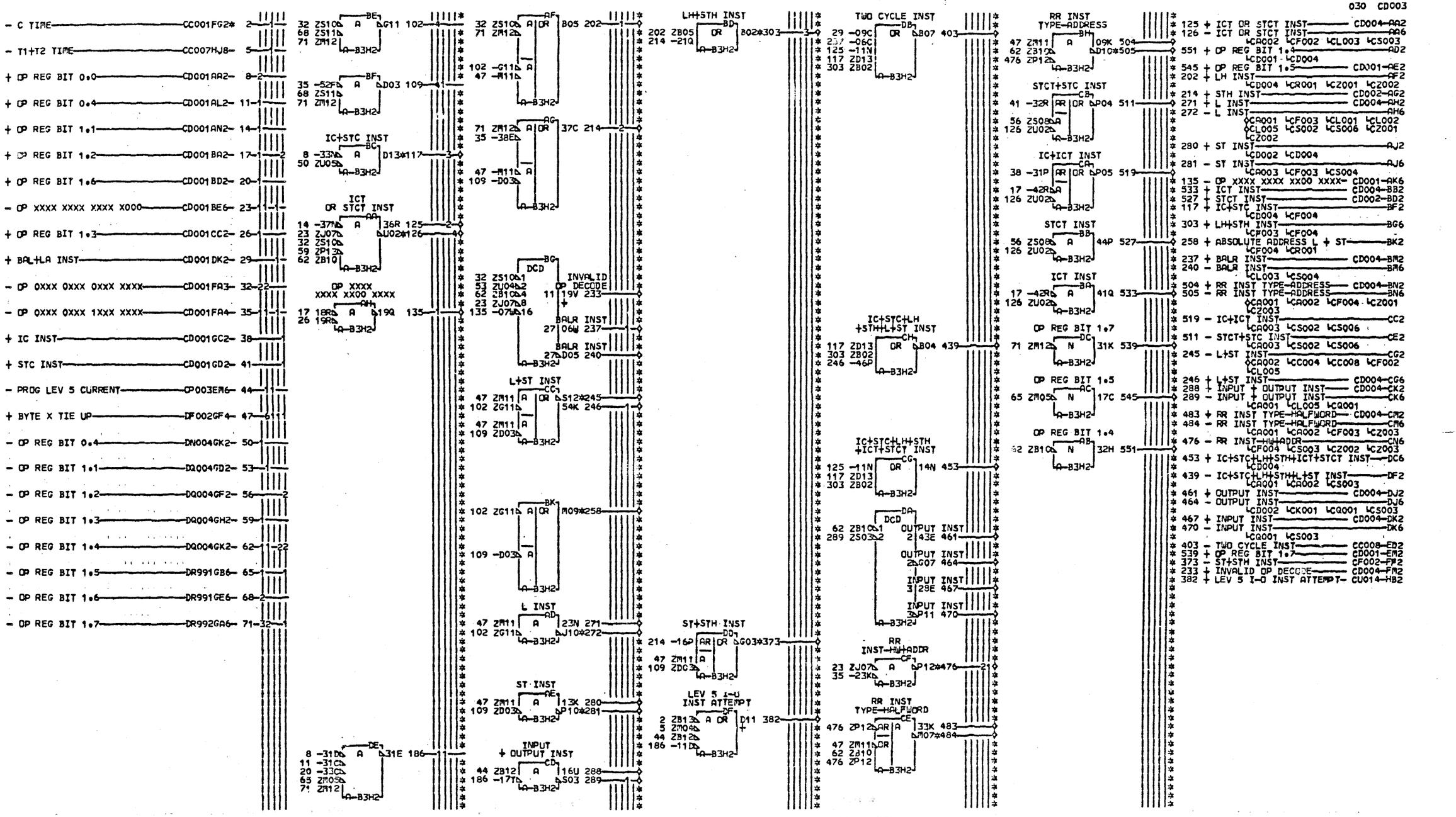
INSTRUCTION STARTS CYCLES STOP

E.C.-HISTORY	D. MACH.27RNB	
312922 314419	FRAME 01	
DATE LAST EC	IBM CORP.SDD	CC008
10-06-76 315053	P.N. 1750153	030

CC008
030 SIM TO PN 5997593 EC 310262


- FLOAT UNUSED AU001GH2- 2
- OP XXXX XXXX X000 XXXX CA003BB3- 7
+ OP REG BIT 1.4 CD003AD2- 12
+ OP REG BIT 1.5 CD003AE2- 17
- OP XXXX XXXX XX00 XXXX CD003AK6- 22
+ OP REG BIT 1.7 CD003EA2- 27
+ LS I-O REG ADDR CD004AK2- 32
- OP XXXX X000 XXXX XXXX CZ002DN6- 37
- OP REG BIT 0.0 DN004GB2- 42
- OP REG BIT 0.1 DN004GD2- 47
- OP REG BIT 0.2 DN004GF2- 52
- OP REG BIT 0.3 DN004GH2- 57
- OP REG BIT 0.4 DN004GK2- 62
- OP REG BIT 1.0 DQ004GB2- 67
- OP REG BIT 1.1 DQ004GD2- 72
- OP REG BIT 1.2 DQ004GF2- 77
- OP REG BIT 1.3 DQ004GH2- 82
- OP REG BIT 1.6 DR991GE6- 87
OP XXXX XXXX XXXX X000
DCD
INVALID OP DECODE
EXIT INST
INDATA I-O REG ADDR
CCU I-O REG ADDR
BAL+LA INST
LA INST
STC INST
IC INST
OP X000 XXXX XXXX XXXX
OP XXXX XXXX X111 XXXX
OP REG BIT 1.6
OP REG BIT 1.4
OP REG BIT 1.5
OP REG BIT 1.7
LS I-O REG ADDR
OP XXXX XXXX X000 XXXX
OP REG BIT 0.0
OP REG BIT 0.1
OP REG BIT 0.2
OP REG BIT 0.3
OP REG BIT 0.4
OP REG BIT 1.0
OP REG BIT 1.1
OP REG BIT 1.2
OP REG BIT 1.3
030 CD001
CD003-AA2
CD003-AE2
CD004-AF2
CD003-AG2
CD004-AH2
CD004-AJ2
CD002-AK6
CD003-AL2
CD003-AN2
CD003-BA2
CD003-BB0
CZ003-BB3
CZ002-BB4
CZ002-BB5
CZ002-BB6
CZ002-BB7
CQ005-BB8
CQ005-BB9
CD003-BD2
CD004-BE6
CD004-BH0
CA001-BH3
CA003-BH7
CA003-BH8
CC2
CD004-CF2
CA002-CG2
CS003-CJ2
CD004-DK2
CD004-DK6
CD004-EL2
CD004-EM2
CD004-EM6
CQ001-EN6
CD002-FA0
CD003-FA3
CD003-FA4
CZ001-FA5
CD004-GD2
CS004-GG6
EDGE CONN.
249 A-B3P1E13
01A-B4P6E04
LOC. TYPE
A-B3G2 Y702
A-B3H2 AB91
A-B3J2 6815
THIS PAGE IS FOR 3705-II ONLY.
INSTRUCTION DECODE
E.C.-HISTORY-D
312922
314419
DATE LAST EC
10-06-76 315053
MACH.27RNB
FRAME 01
IBM CORP.SDD
P.N. 1750154
CD001
030
CD001
030 SIM TO PN 5997594 EC 310268


+ D TIME CC001CL2- 2-1
+ DA TIME CC001DM2- 6-1
- I2+I3 TIME CC008AL2- 10-1
+ OP REG BIT 0.1 CD001AE2- 14-3
+ OP REG BIT 0.2 CD001AG2- 18-2
+ OP REG BIT 0.3 CD001AJ2- 22-2
- OP X011 XXXX XXXX XXXX CD001AK6- 26-2
+ OP REG BIT 0.4 CD001AL2- 30-2
- OP XXXX XXXX XXXX X000 CD001BE6- 34-2
- OP 1XXX 1XXX 1XXX XXXX CD001FA0- 38-2
- OP 0XXX 0XXX 0XXX XXXX CD001FA3- 42-2
+ STC INST CD001GD2- 46-1
+ STH INST CD003AG2- 50-1
+ ST INST CD003AJ2- 54-1
+ STCT INST CD003BD2- 58-1
- OUTPUT INST CD003DJ6- 62-1
- OP XXXX X000 XXXX XXXX CZ002DN6- 66-1
- OP REG BIT 0.0 DN004GB2- 70-7
- OP REG BIT 0.1 DN004GD2- 74-2
- OP REG BIT 0.2 DN004GF2- 78-1
- OP REG BIT 0.3 DN004GH2- 82-1
- OP REG BIT 0.4 DN004GK2- 86-5
- OP REG BIT 1.4 DQ004GK2- 90-2
B INST
AA
14 -36W A U13*103
22 -41H
70 ZS09
78 ZM13
86 ZU05
A-B3H2
BCL INST
AB
14 -36W A U12*112
18 -53E 54D 113
70 ZS09
82 ZU09
86 ZU05
A-B3H2
BB+BCT INST
BB
26 -44R OR P06*121
38 -43R 34Q 122
70 ZS09
74 ZU06
86 ZU05
A-B3H2
BZL INST
AC
14 -36W A U03 131
18 -43K
22 -41H
70 ZS09
86 ZU05
A-B3H2
STORE INST TYPE
AH
46 14E OR M08 141
50 14F
54 13E
58 13F
A-B3H2
BCT INST
AF
26 44R A 43Q 149
38 43R S04 150
A-B3H2
OUTPUT IAR INST
BF
62 ZG07 A S02 156
66 ZP07
A-B3H2
RR INST TYPE-BYTE
AG
34 ZJ07 A 09E 163
42 ZS1C B09 164
90 ZB10
A-B3H2
RI+RR-BYTE INST TYPE
BC
30 -24Q OR U13*171
70 ZS09
34 ZJ07
42 ZS1C
90 ZB10
A-B3H2
RI INST TYPE
AE
30 -24Q A 23R 181
70 ZS09 G08 182
A-B3H2
BB INST
AD
70 ZS09 A 49Q 187
74 ZU06 U07 188
86 ZU05
A-B3H2
B+BCL BZL INST
BA
103 ZU13 OR CR S13*203
113 -54W 53W 204
14 -36W A
18 -43K
22 -41H
70 ZS09
86 ZU05
A-B3H2
INST LATCHED STORE COND
BD
2 -18L A 19H 232
6 -18K
10 ZJ06
141 ZJ05
A-B3R2
B+BB+BC L+BZL+BCT INST
BE
204 28M OR P03 304
122 29M
A-B3H2
030 CD002
103 + B INST CB2
CF003 CL005
112 - BCL INST CC6
CF003 CL005
131 - BZL INST CL005-CD6
187 + BB INST CD004-CE2
188 - BB INST CA002-CE6
181 + RI INST TYPE CD004-CF2
182 - RI INST TYPE CF6
CA001 CA002 CA003 CL003
CS002 CZ001 CZ002
149 + BCT INST CD004-CG2
150 - BCT INST CG6
CA002 CA003 CL004 CL005
CZ001
163 + RR INST TYPE-BYTE CD004-CH2
164 - RR INST TYPE-BYTE CH6
CA001 CA002 CA003 CZ001
CZ002 CZ003
141 - STORE INST TYPE CN2
CR003 CU004
203 - B+BCL BZL INST CF003-DA2
121 - BB+BCT INST DF2
CF002 CL005
171 - RI+RR-BYTE INST TYPE DH2
CF003 CZ002
232 + INST LATCHED STORE COND CQ001-DM2
304 - B+BB+BCL+BZL+BCT INST EF2
CA002 CA003
156 + OUTPUT IAR INST CA001-EK2
THIS PAGE IS FOR 3705-II ONLY.
CD002
030 SIM TO PN 5097595 EC 310268
EDGE CONN. 01A-B4Q6C02
103 A-B3Q1A11
01A-B4Q6A02
112 A-B3Q1B13
01A-B4Q6B04
121 A-B3Q1C13
01A-B4Q6C04
171 A-B3Q1D11
01A-B4Q6D02
203 A-B3Q1C11
LOC. TYPE
A-B3H2 AB91
A-B3R2 AB92
INSTRUCTION DECODE
E.C.-HISTORY
312922
314419
DATE LAST EC
10-06-76 315053
D MACH.27 RNB
FRAME 01
IBM CORP. SDD
P.N. 1750155
CD002
030



THIS PAGE IS FOR 3705-II ONLY.

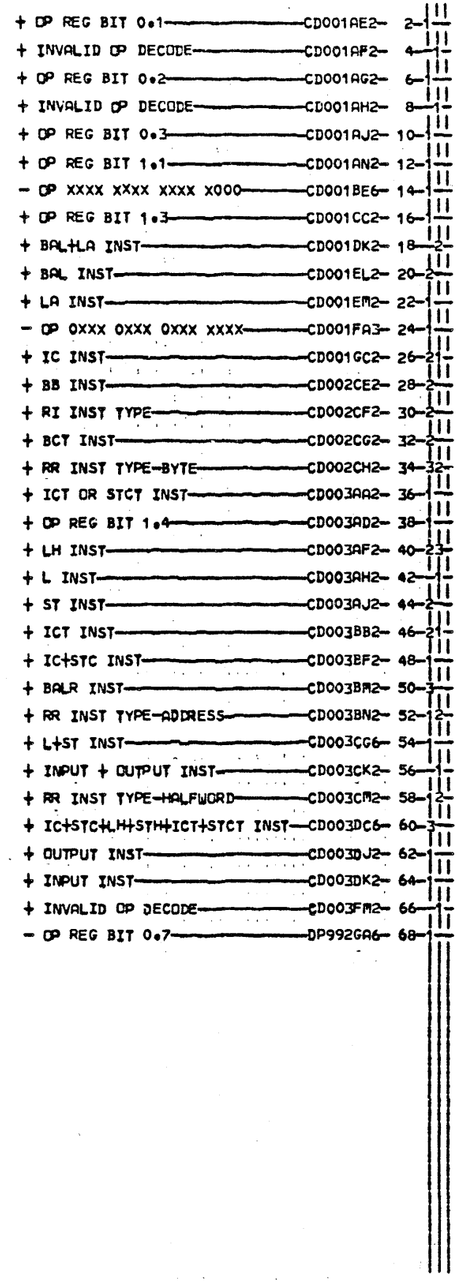
EDGE CONN. 2 RESISTOR
01A-B4R6C02 272 A-B3Q1D13 01A-B4S6A02 484 A-B3R1E13 A-B3H2B13 01A-B4Q6D04 01A-B4R6E04 117 A-B3R1A13 281 A-B3Q1E11 505 A-B3R1C13 01A-B4R6A04 01A-B4Q6E02 01A-B4R6C04 126 A-B3R1B13 303 A-B3R1B11 01A-B4R6B04 01A-B4R6B02 245 A-B3R1D11 373 A-B3S1A13 01A-B4R6D02 01A-B4S6A04 258 A-B3R1C11 476 A-B3S1A11

LOC. TYPE
A-B3H2 AB91

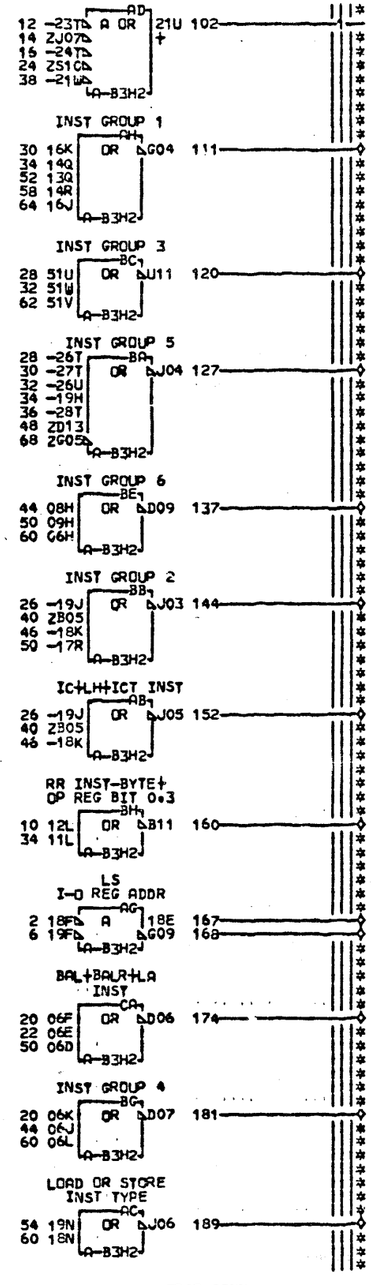
CYCLE AND INSTRUCTION DECODE

E.C.-HISTORY	D	MACH.27RNB	
312922			
314419		FRAME 01	
		IBM CORP.SDD	CD003
DATE LAST EC		P.N. 1750156	030
10-06-76 315053			

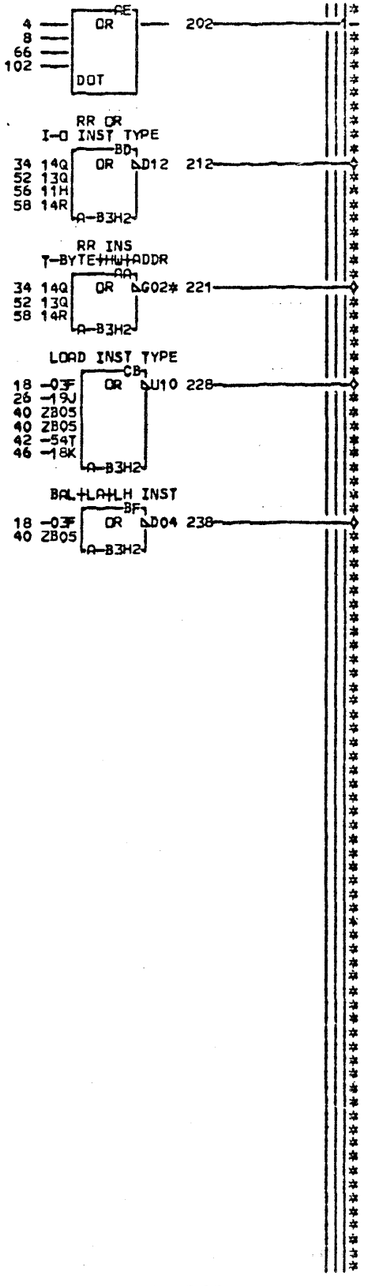
CD003
030 SIM TO PN 5997596 EC 310268



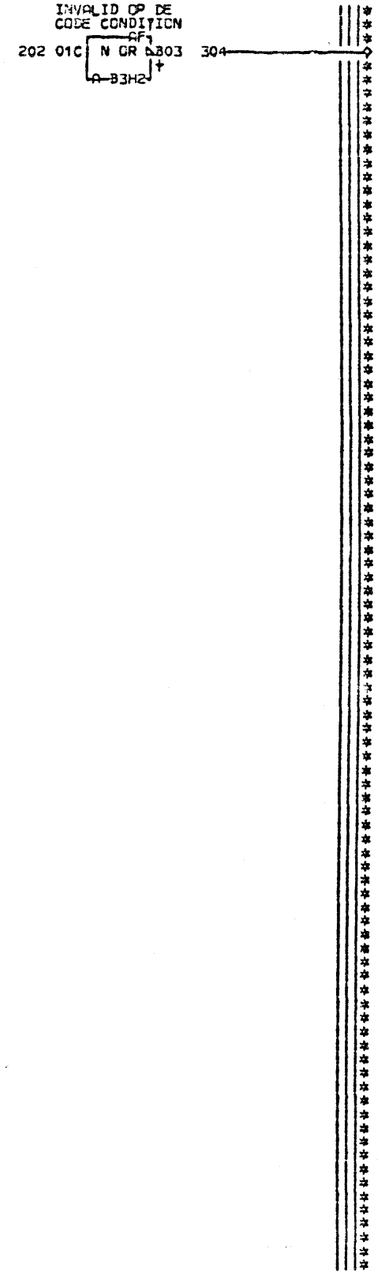
THIS PAGE IS FOR 3705-II ONLY.



EDGE CONN.
221 A-B3V3D05
01A-B4V3D05



LOC. TYPE
A-B3H2 AB91



030 CD004

221 - RR INST-BYTE+HW+ADDR AB2
CA003 CF004 CL003

152 - IC+LH+ICT INST CA001-AD2

189 - LOAD OR STORE INST TYPE CL003-AE2

304 - INVALID OP DECODE CONDITION AH2
CK007

167 + LS I-O REG ADDR CD001-AK2

168 - LS I-O REG ADDR AK6
CA001 CL004 CL005 CS003

111 - INST GROUP 1 BB2
CL001 CL002

127 - INST GROUP 5 CL002-BC2

144 - INST GROUP 2 BD2
CL001 CL002 CL005

120 - INST GROUP 3 BF2
CL001 CL002

212 - RR OR I-O INST TYPE CS002-BG2

137 - INST GROUP 6 CS002-BH2

238 - BAL+LA+LH INST CS002-BK2

181 - INST GROUP 4 BL2
CL001 CL002

160 - RR INST-BYTE+OP REG BIT 0.3 CK2
CL003

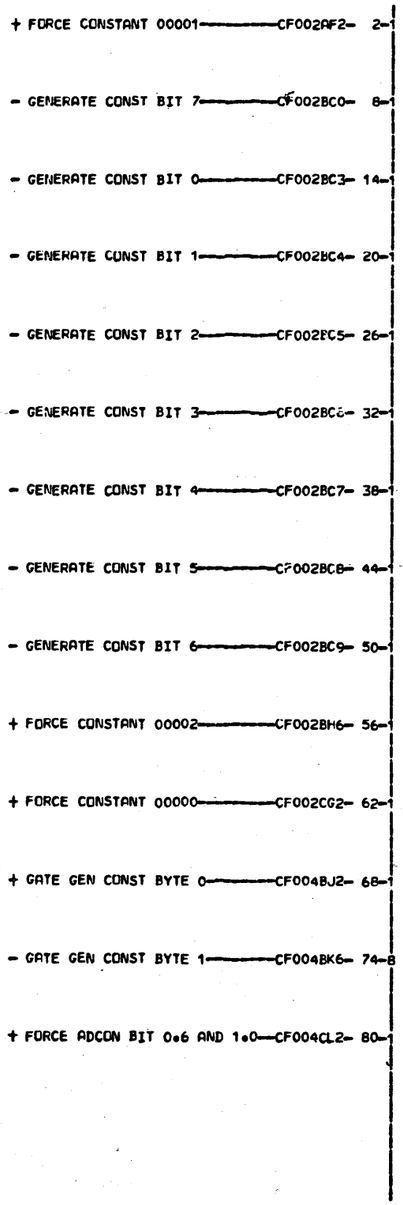
174 - BAL+BALR+LA INST CA001-DM2

228 - LOAD INST TYPE CU004-DN2

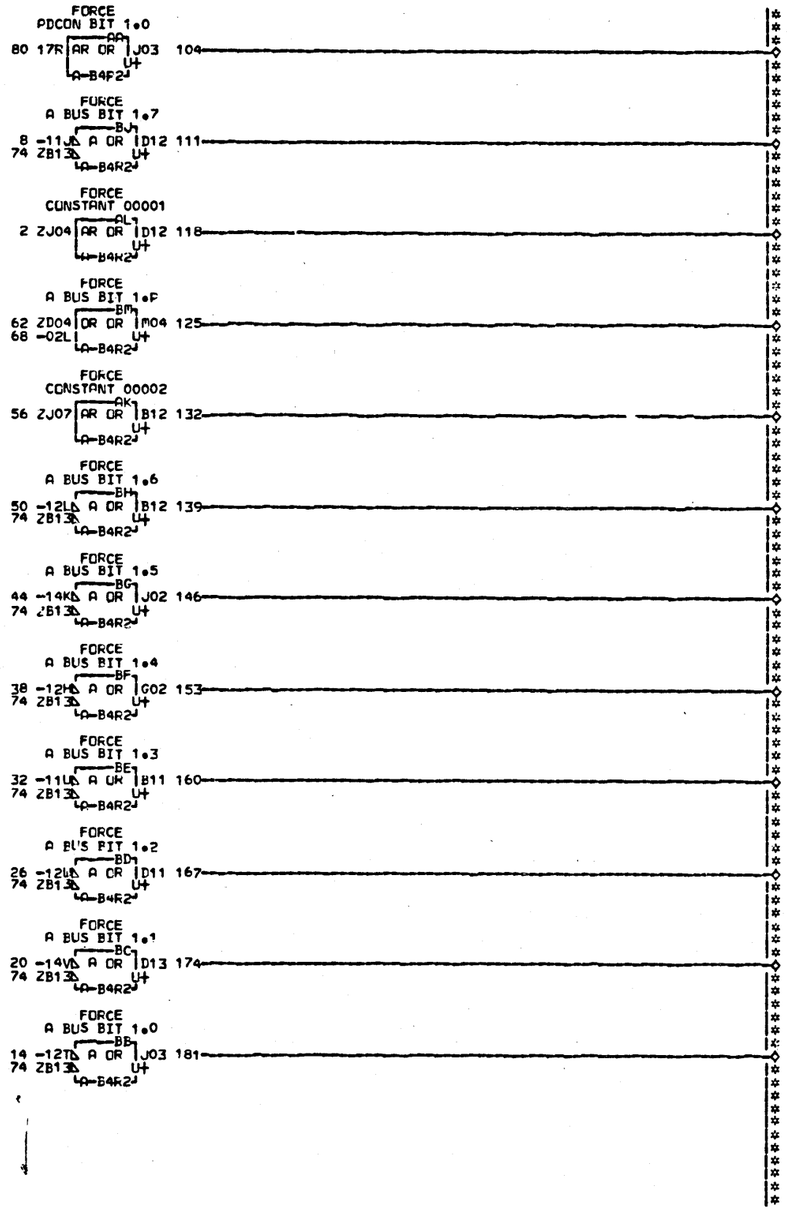
INSTRUCTION DECODE

E.C.-HISTORY-D.	MACH.27RNB	
312922		
314419	FRAME 01	
	IBM CORP.SDD	CD004
DATE LAST EC	P.N. 1750157	030
10-06-75 315053		

CD004
030 SIM TO PN 5997597 EC 310268



CF001
000



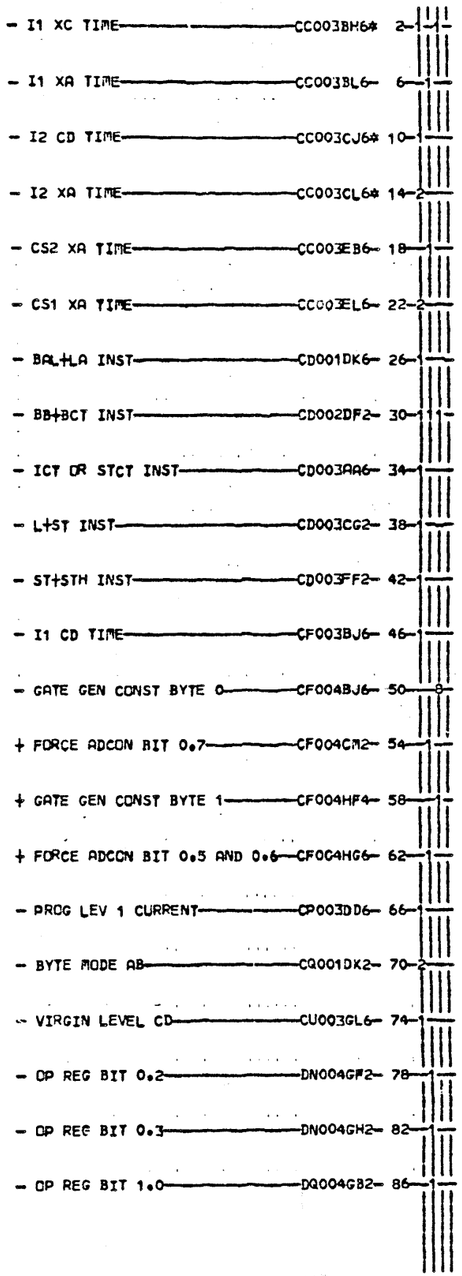
LOC. TYPE
A-B4R2 6807

000 CF001

104 + FORCE ADCON BIT 1.0 — DK975-AA6
132 + FORCE CONSTANT 00002 — DM005-AK6
118 + FORCE CONSTANT 00001 — DM005-AL6
181 + FORCE A BUS BIT 1.0 — DK975-BB2
174 + FORCE A BUS BIT 1.1 — DK975-BC2
167 + FORCE A BUS BIT 1.2 — DL005-BD2
160 + FORCE A BUS BIT 1.3 — DL005-BE2
153 + FORCE A BUS BIT 1.4 — DL005-BF2
146 + FORCE A BUS BIT 1.5 — DM005-BG2
139 + FORCE A BUS BIT 1.6 — DM005-BH2
111 + FORCE A BUS BIT 1.7 — DM005-BJ2
125 + FORCE A BUS BIT 1.P — DK975-BM6

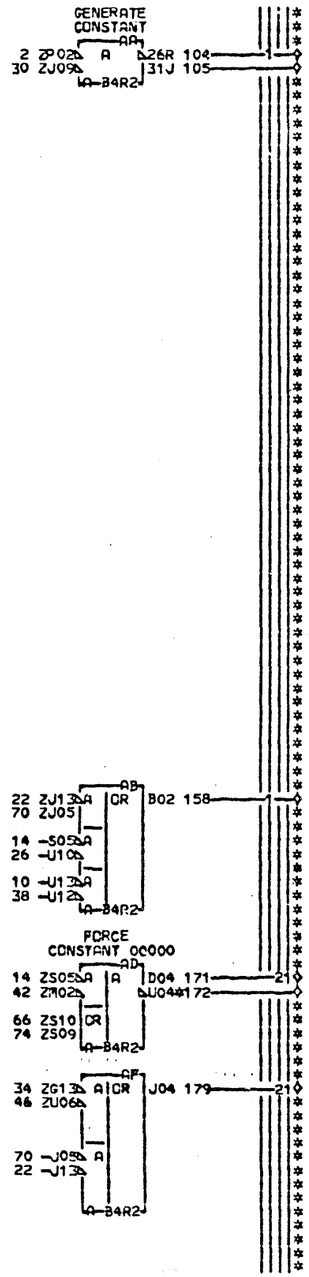
FORCE CONSTANTS

E.C.-HISTORY	BY	MACH.27RNB	
309521C		FRAME 01	
		IBM CORP.SDD	CF001
DATE LAST EC		P.N. 5997598	000
04-19-72 309545			

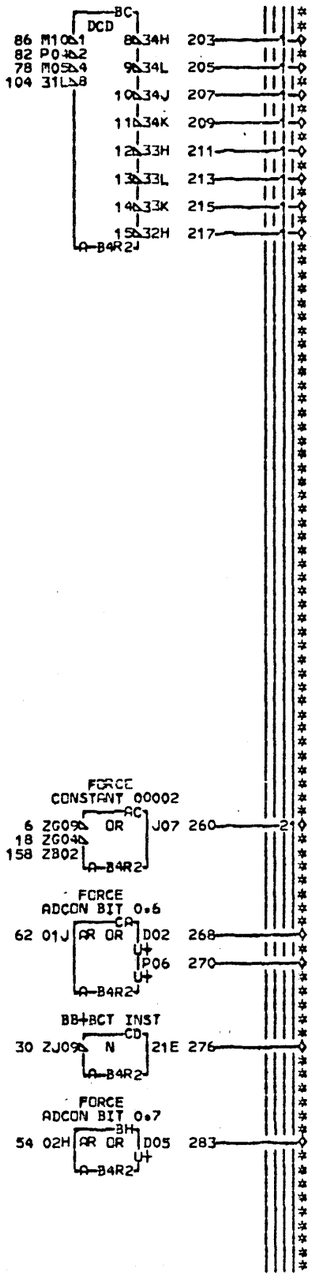


THIS PAGE IS FOR 3705-II ONLY.

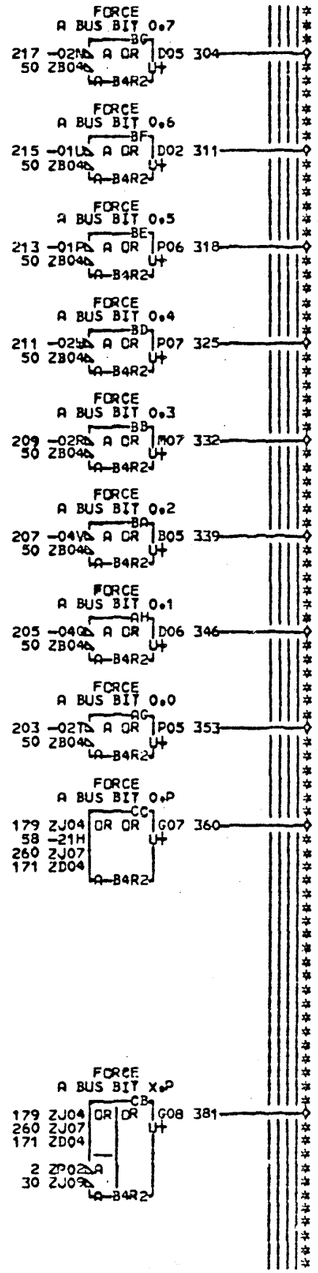
CF002
030 SIM TO PN 5997599 EC 309545

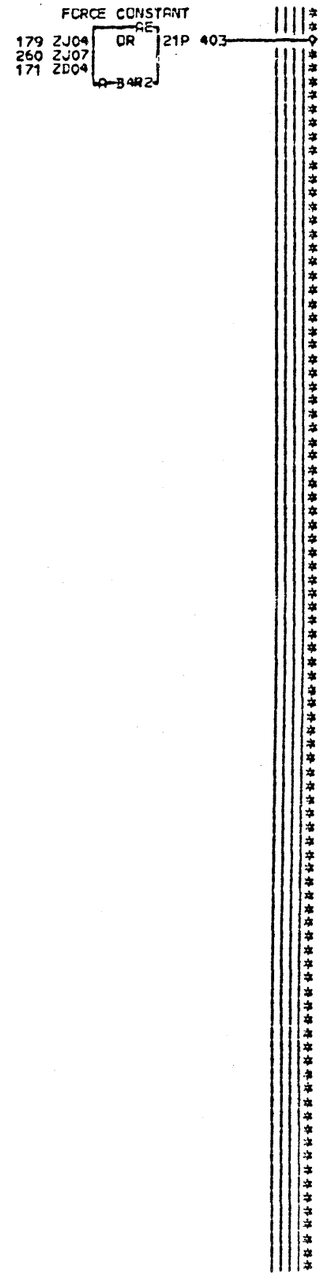


EDGE CONN.
2 RESISTOR
A-B4R2P02
10 RESISTOR
A-B4R2U13
14 RESISTOR
A-B4R2S05
172 A-B3V3D06
01A-B4V3D06



LOC. TYPE
A-B4R2 6807





030 CF002

104 - GENERATE CONSTANT — CF004-AB6
179 + FORCE CONSTANT 00001 — CF001-AF2
158 + TEST PIN — AH2
217 - GENERATE CONST BIT 7 — CF001-BC0
203 - GENERATE CONST BIT 0 — CF001-BC3
205 - GENERATE CONST BIT 1 — CF001-BC4
207 - GENERATE CONST BIT 2 — CF001-BC5
209 - GENERATE CONST BIT 3 — CF001-BC6
211 - GENERATE CONST BIT 4 — CF001-BC7
213 - GENERATE CONST BIT 5 — CF001-BC8
215 - GENERATE CONST BIT 6 — CF001-BC9
105 + GENERATE CONSTANT — CF003-BD2
260 + FORCE CONSTANT 00002 — CF001-BH6
171 + FORCE CONSTANT 00000 — CF001-CG2
403 + FORCE CONSTANT — CF003-CJ6
172 - PRE FORCE CONSTANT 00000 — CM6
(CA001)
353 + FORCE A BUS BIT 0.0 — DG975-DC2
346 + FORCE A BUS BIT 0.1 — DG975-DD2
339 + FORCE A BUS BIT 0.2 — DH015-DE2
332 + FORCE A BUS BIT 0.3 — DH015-DF2
325 + FORCE A BUS BIT 0.4 — DH015-DG2
318 + FORCE A BUS BIT 0.5 — DJ015-DH2
311 + FORCE A BUS BIT 0.6 — DJ015-DJ2
304 + FORCE A BUS BIT 0.7 — DJ015-DK2
283 + FORCE ADCON BIT 0.7 — DJ015-DL6
268 + FORCE ADCON BIT 0.6 — DJ015-DM6
270 + FORCE ADCON BIT 0.5 — DJ015-DN6
381 + FORCE A BUS BIT X.P — DF975-FE6
360 + FORCE A BUS BIT 0.P — DG975-FF6
276 + BB+BCT INST — CF003-HD2

FORCE CONSTANTS

E.C.-HISTORY	MACH.27RNB
312922	
314419	FRAME 01
	IBM CORP.SDD
DATE LAST EC	P.N. 1750158
10-06-76 315053	

CF002
030

030 CF003

- I1 CD TIME — CC003BJ6* 2
- I2 XC TIME — CC003CH6* 5
- I2 CD TIME — CC003CJ6 8
+ B INST — CD002CB2 11-1
- BCL INST — CD002CC6 14
- B+BCL BZL INST — CD002DA2 17-22
- RI+RR-BYTE INST TYPE — CD002DH2 20-11
- L INST — CD003AH6 23-3-2
- ST INST — CD003AJ6 26
+ LH+STH INST — CD003BG6 29-1
- RR INST TYPE-HALFWORD — CD003CM6 32
+ GENERATE CONSTANT — CF002BD2 35
+ FORCE CONSTANT — CF002CJ6 38
+ BB+BCT INST — CF002HD2 41
+ OP REG BIT 0.7 — CF004AH2 44
- IC+STC INST — CF004AL2 47-2
+ SHIFT RIGHT — CF004CF2 50
+ FORCE ADCON BIT 0.6 AND 1.0 — CF004CL2 53
- FORCE ADCON BITS 0.6 AND 1.0 — CF004CL6 56-2
+ FORCE ADCON BIT 0.5 AND 0.6 — CF004HG6 59
- GATE SAR TO A BUS — CS004BK6 62
- OP REG BIT 0.7 — DP992GA6 65
+ GENERATED SDR BIT ODD 0.P — DP993GV4 68
- OP REG BIT 1.0 — DQ004GB2 71-2
- OP REG BIT 1.7 — DR992GA6 74-1
+ GENERATED SDR BIT ODD 1.P — DR993GV4 77

I1 CD TIME
AD
2 ZJ10 AR U06 103 — B4R2

BLOCK COMPLETE SDR
AA
35 -49V
38 -48W
50 -49W
62 ZD03
DR D09 111 — B4R2

CC
11 ZB03 N 101D 123 — B4R2

BC
56 -36C
71 ZM10
74 ZM11
OD 139D 144 — B4R2

AE
56 -36C
71 ZM10
OE 136D 151 — B4R2

103 ZU06 AR A AC 139T 202
26 ZJ06 DR
29 ZG11
41 -19H — B4R2

103 ZU06 A DR CM
123 -29C
14 -P03 A 129D 220
103 -UC6 — B4R2

20 ZB07 A AB B10 230
44 -16K
103 ZU06 — B4R2

GATE SDR 0.
5-0.7 TO A BUS
AG
111 ZD09 A A S07 304
202 -37N
23 ZB09 DR
103 ZU06
47 -53C DR
103 ZU06
20 ZB07 DR
65 ZD07
103 ZU06 — B4R2

GATE SDR 1.
1-1.5 TO A BUS
BE
111 ZD09 A A S04 332
230 ZB10 — B4R2

BG
202 -39T A DR 38K 337
144 -37L
17 ZG12
74 ZM11
103 ZU06
23 ZB09
53 -39J
103 ZU06
47 -36K
103 -U06
151 -36H — B4R2

CD
PWR
-0.75V
41P 365 — B4R2

GATE SDR X.
6-X.7 TO A BUS
BD
230 ZB10 A A P12 460
304 ZS07
8 ZU13
355 -54J
32 -S11
103 -U06
5 -S12
29 ZG11
17 ZG12
103 ZU06 — B4R2

GATE SDR
1.7 TO A BUS
BB
111 ZD09 A A M09 381
230 ZB10
202 -36T
23 ZB09 DR
103 ZU06
17 ZG12 DR
103 ZU06 — B4R2

GATE SDR 0.
0-0.4 TO A BUS
AF
304 ZS07 AR A M08 479
17 ZG12 DR
103 ZU06 — B4R2

304 ZS07 AR DR U07N 402
220 -54T OE
68 ZM03 — B4R2

332 ZS04 AR DR U46P 430
77 ZP11 OE
337 -51T — B4R2

SDR BIT
0.P TO A BUS
CD
59 -49P A DR C07 504
111 ZD09 U+
402 -49N — B4R2

SDR BIT
1.P TO A BUS
BF
111 ZD09 A DR M04 532
430 -46N U+ — B4R2

GATE SDR
1.6 TO A BUS
BA
111 ZD09 A A D10 539
230 ZB10
23 ZB09 DR
103 ZU06 — B4R2

GATE SDR
1.0 TO A BUS
AH
111 ZD09 A A U05 549
230 ZB10
202 -46T
23 ZB09 DR
103 ZU06
47 -53C DR
103 ZU06 — B4R2

111 + BLOCK COMPLETE SDR — AG6
LDP994 LDR994

103 - I1 CD TIME — BJ6
LCF002 LCF004

479 - GATE SDR 0.0-0.4 TO A BUS — DB6
LDN003

304 - GATE SDR 0.5-0.7 TO A BUS — DE6
LDP994

549 - GATE SDR 1.0 TO A BUS — DQ003-DF6

539 - GATE SDR 1.6 TO A BUS — DR994-DG6

381 - GATE SDR 1.7 TO A BUS — DR994-DJ6

460 - GATE SDR X.6-X.7 TO A BUS — EA6
LDR994

332 - GATE SDR 1.1-1.5 TO A BUS — EH6
LDQ003 LDR994

532 + SDR BIT 1.P TO A BUS — DK975-FL2

504 + SDR BIT 0.P TO A BUS — DG975-GM2

THIS PAGE IS FOR 3705-II ONLY.

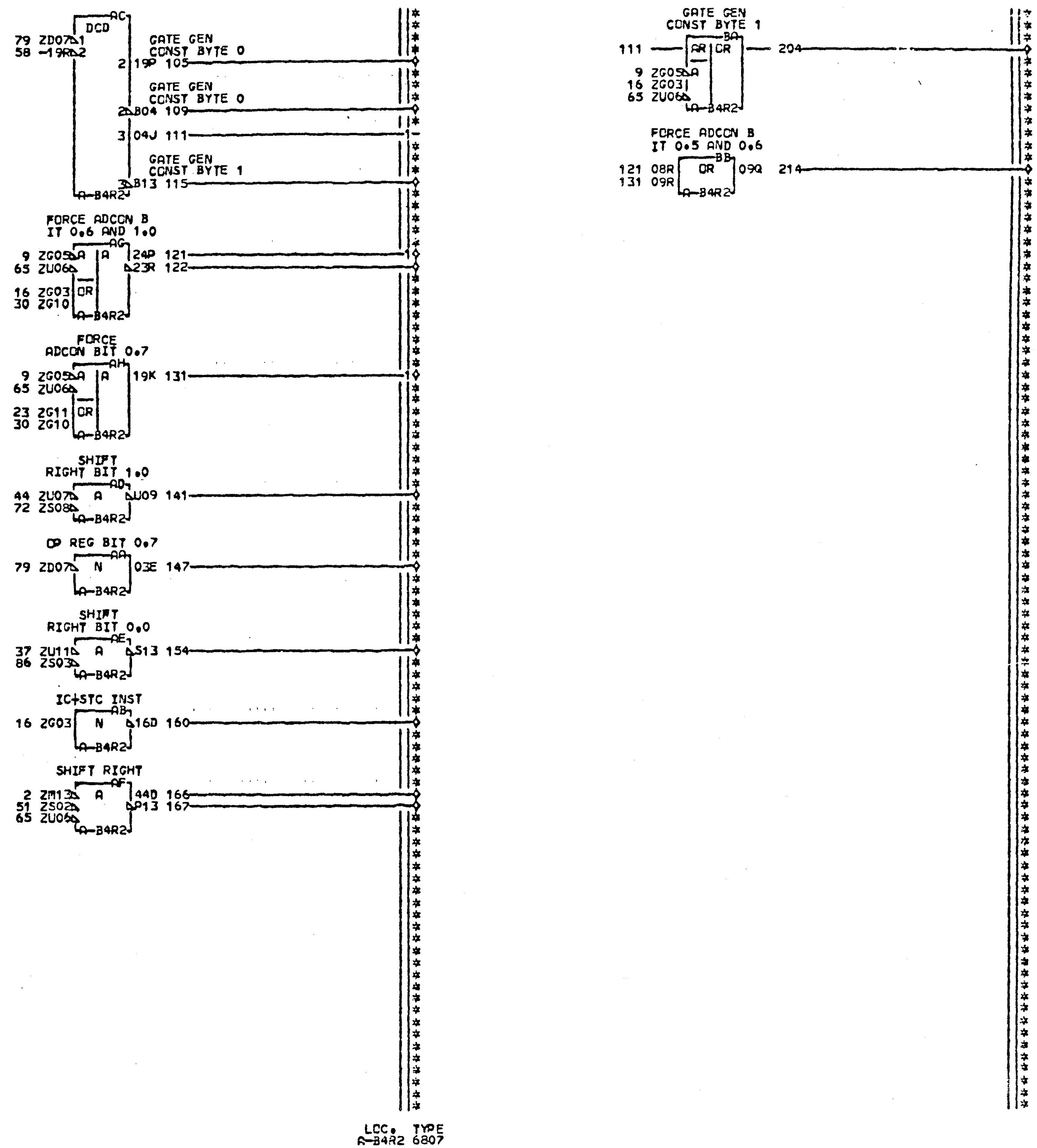
EDGE CONN.
2 RESISTOR
A-B4R2J10
5 RESISTOR
A-B4R2S12

LOC. TYPE
A-B4R2 6807

CF003
030 SIM TO PN 5997600 EC 310268

BIT FILTER AND PARITY GENERATI
E.C.-HISTORY — D MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD CF003
DATE LAST EC
10-06-76 315053
P.N. 1750159 030

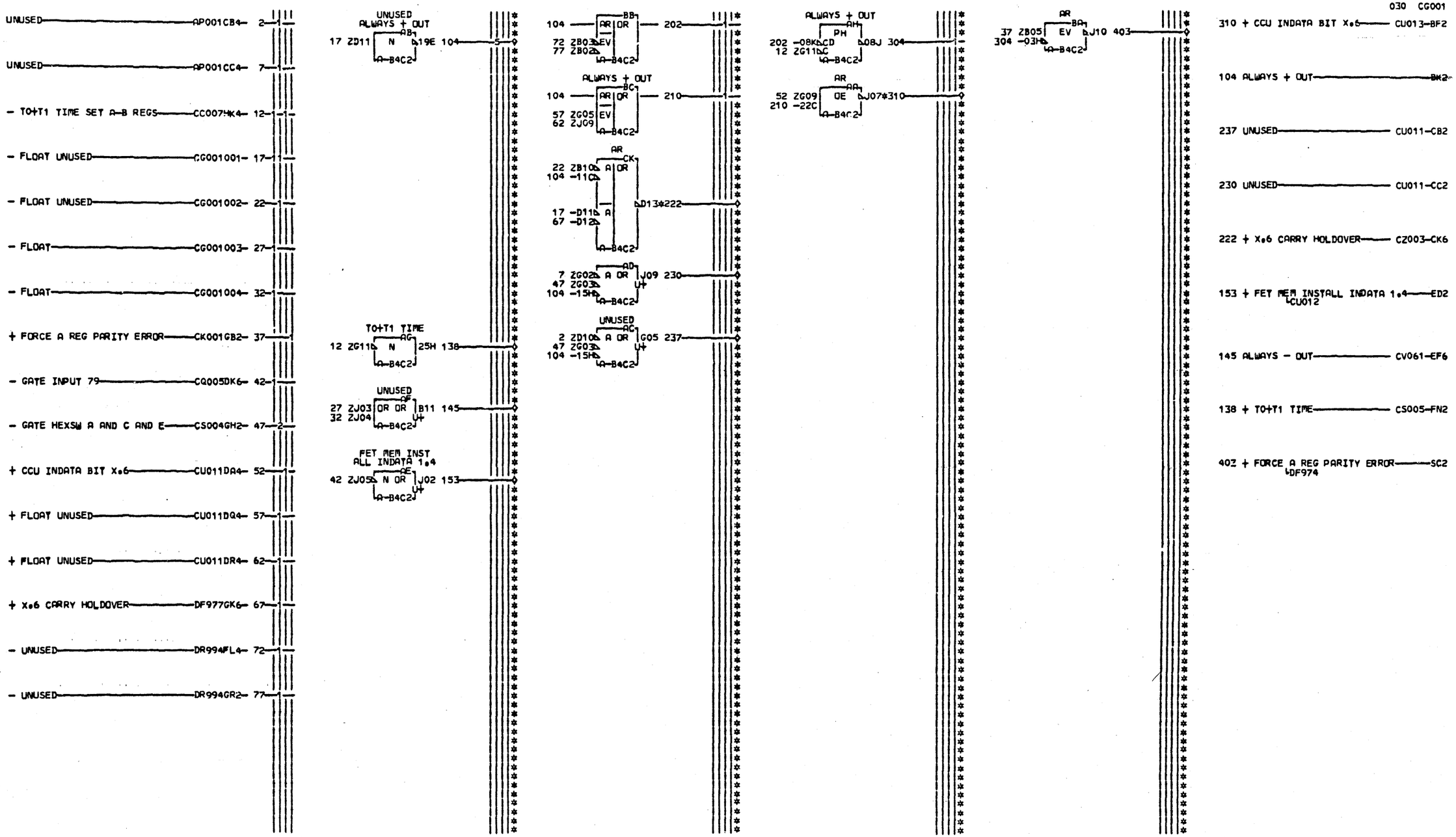
- DP XXXX XXXX X111 XXXX——CA003BB0— 2
- SELECT LS REG 0——CC006BM6— 9
+ IC+STC INST——CD003BF2— 16
+ LH+STH INST——CD003BG6— 23
+ ABSOLUTE ADDRESS L + ST——CD003BK2— 30
- RR INST TYPE-ADDRESS——CD003BN6— 37
- RR INST-HW+ADDR——CD003CN6— 44
- RR INST-BYTE+HW+ADDR——CD004AB2— 51
- GENERATE CONSTANT——CF002AB6— 58
- I1 CD TIME——CF003BJ6— 65
- SDR BIT 0.7——DP992ED2— 72
- DP REG BIT 0.7——DP992GA6— 79
- SDR BIT X.7 OR UNUSED——DR992EG2— 86

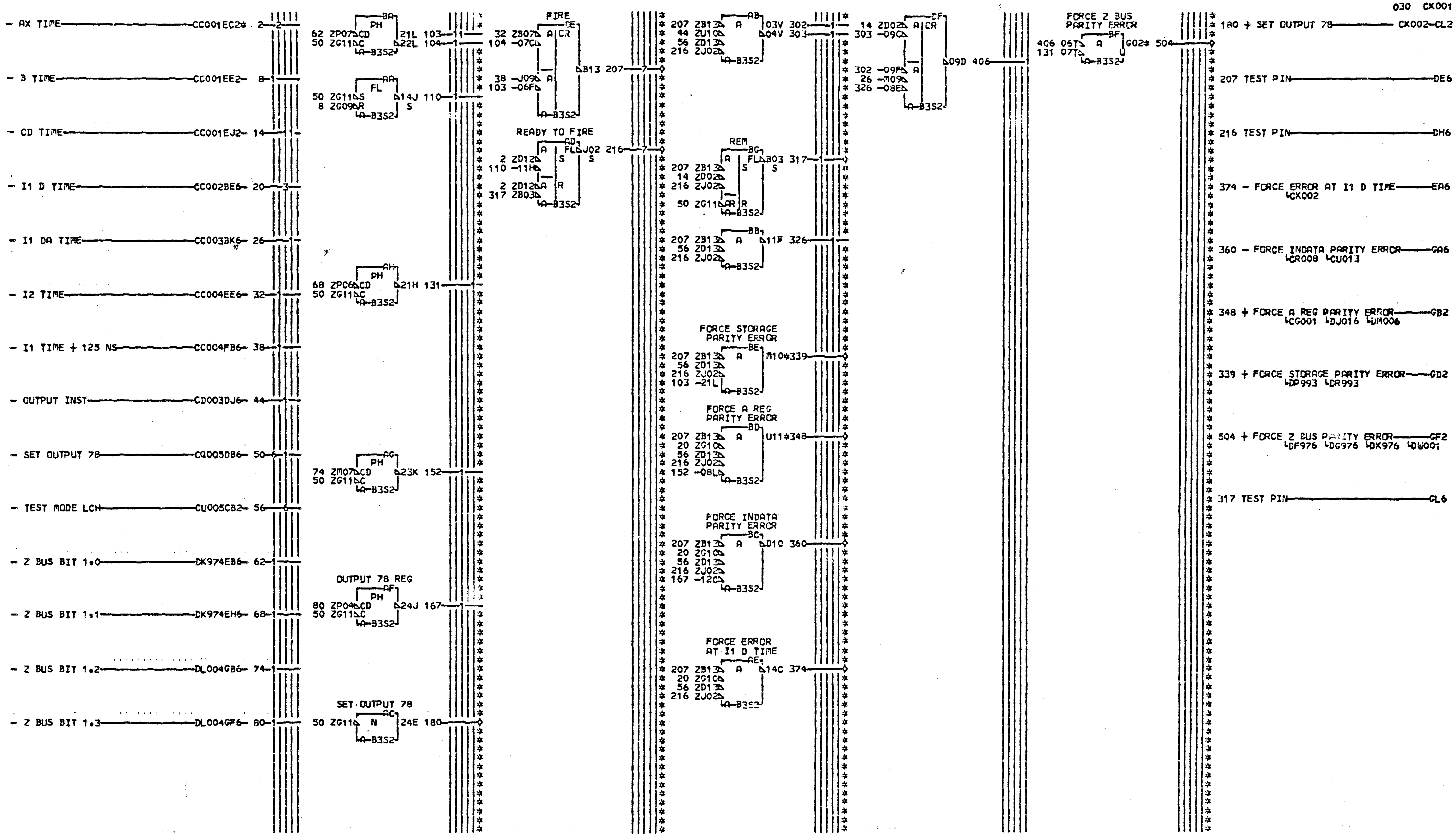


147 + DP REG BIT 0.7——CF003-AH2
160 - IC+STC INST——CF003-AL2
105 + GATE GEN CONST BYTE 0— CF001-BJ2
109 - GATE GEN CONST BYTE 0— CF002-BJ6
115 - GATE GEN CONST BYTE 1— CF001-BK6
141 - SHIFT RIGHT BIT 1.0——CC6 (DQ003, DR992)
154 - SHIFT RIGHT BIT 0.0——CE6 (DN003, DP992)
166 + SHIFT RIGHT——CF003-CF2
167 - SHIFT RIGHT——CF6 (DN003, DP994, DQ003, DR994)
121 + FORCE ADCON BIT 0.6 AND 1.0——CL2 (CF001, CF003)
122 - FORCE ADCON BITS 0.6 AND 1.0——CL6 (CF003)
131 + FORCE ADCON BIT 0.7— CF002-CM2
204 + GATE GEN CONST BYTE 1— CF002-HF4
214 + FORCE ADCON BIT 0.5 AND 0.6——HG6 (CF002, CF003)

THIS PAGE IS FOR 3705-II ONLY.

SHIFT RIGHT CONTROLS AND FORCE ADCONST
E.C. HISTORY D MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD CF004
DATE 10-06-76 LAST EC 315053 P.N. 1750150 030


UNUSED — AP001CB4 — 2
UNUSED — AP001CC4 — 7
- TO+T1 TIME SET A-B REGS — CC007HK4 — 12
- FLOAT UNUSED — CG001001 — 17
- FLOAT UNUSED — CG001002 — 22
- FLOAT — CG001003 — 27
- FLOAT — CG001004 — 32
+ FORCE A REG PARITY ERROR — CK001GB2 — 37
- GATE INPUT 79 — CQ005DK6 — 42
- GATE HEXSW A AND C AND E — CS004GH2 — 47
+ CCU INDATA BIT X.6 — CU011DA4 — 52
+ FLOAT UNUSED — CU011DQ4 — 57
+ FLOAT UNUSED — CU011DR4 — 62
+ X.6 CARRY HOLDOVER — DF977GK6 — 67
- UNUSED — DR994FL4 — 72
- UNUSED — DR994GR2 — 77
UNUSED
ALWAYS + OUT
17 ZD11 N AB 19E 104
TO+T1 TIME
12 ZG11 N AG 25H 138
UNUSED
27 ZJ03 OR OR AF B11 145
32 ZJ04
FET MEM INST ALL INDATA 1.4
42 ZJ05 N OR AE J02 153
104 AR OR BB 202
72 ZB03 EV
77 ZB02
ALWAYS + OUT
104 AR OR BC 210
57 ZG05 EV
62 ZJ09
AR
22 ZB10 A OR CK
104 -11C
17 -D11 A D13*222
67 -D12
7 ZG02 A OR AD J09 230
47 ZG03
104 -15H
UNUSED
2 ZD10 A OR AC G05 237
47 ZG03
104 -15H
ALWAYS + OUT
202 -08K CD PH AH J08J 304
12 ZG11 C
AR
52 ZG09 OE AA J07*310
210 -22C
AR
37 ZB05 EV BA J10 403
304 -03H
030 CG001
310 + CCU INDATA BIT X.6 — CU013-BF2
104 ALWAYS + OUT — BK2
237 UNUSED — CU011-CB2
230 UNUSED — CU011-CC2
222 + X.6 CARRY HOLDOVER — CZ003-CK6
153 + FET MEM INSTALL INDATA 1.4 — ED2
CU012
145 ALWAYS - OUT — CV061-EF6
138 + TO+T1 TIME — CS005-FN2
402 + FORCE A REG PARITY ERROR — SC2
DF974
THIS PAGE IS FOR 3705-II ONLY.
EDGE CONN.
222 A-B4K1A13
01A-B3K6A04
310 A-B4E6D02
01A-B3E1D11
LOC. TYPE
A-B4C2 WB93
GATE HEXSWITCH OR GATE INDATA X.4-X.5 SELECT C AND Z LATCH
E.C.-HISTORY — D-MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD
P.N. 1750161
DATE LAST EC
08-31-76 315053
CG001
030
CG001
030


- AX TIME CC001EC2* 2
- B TIME CC001EE2- 8
- CD TIME CC001EJ2- 14
- I1 D TIME CC002BE6- 20
- I1 DA TIME CC003BK6- 26
- I2 TIME CC004EE6- 32
- I1 TIME + 125 NS CC004FB6- 38
- OUTPUT INST CD003DJ6- 44
- SET OUTPUT 78 CQ005DB6- 50
- TEST MODE LCH CU005CB2- 56
- Z BUS BIT 1.0 DK974EB6- 62
- Z BUS BIT 1.1 DK974EH6- 68
- Z BUS BIT 1.2 DL004GB6- 74
- Z BUS BIT 1.3 DL004GF6- 80
PH
FL
OUTPUT 78 REG
SET OUTPUT 78
FIRE
READY TO FIRE
REM
FORCE STORAGE PARITY ERROR
FORCE A REG PARITY ERROR
FORCE INDATA PARITY ERROR
FORCE ERROR AT I1 D TIME
FORCE Z BUS PARITY ERROR
180 + SET OUTPUT 78 CK002-CL2
207 TEST PIN DE6
216 TEST PIN DH6
374 - FORCE ERROR AT I1 D TIME EA6 CK002
360 - FORCE INDATA PARITY ERROR GA6 CR008 CU013
348 + FORCE A REG PARITY ERROR GB2 CG001 DJ016 DM006
339 + FORCE STORAGE PARITY ERROR GD2 DP993 DR993
504 + FORCE Z BUS PARITY ERROR GF2 DF976 DG976 DK976 DW001
317 TEST PIN GL6

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
2 RESISTOR
A-B3S2D12
339 A-B3M1A13
01A-B4M6A04
348 A-B3L1D13
01A-B4L6D04
504 A-B3L1E11
01A-B4L6E02

LOC. TYPE
A-B3S2 Y703

CC CONTROL

E.C.-HISTORY	MACH.27RNB	
312922 314419	FRAME 01	
DATE 08-31-76 LAST EC 315053	IBM CORP. P.N. 1750162	CK001 030

+ SET OUTPUT 78 — CK001CL2 — 2-8

- FORCE ERROR AT I1 D TIME — CK001ER6 — 11-8

- Z BUS BIT 0.0 — DG974EB6 — 20-1

- Z BUS BIT 0.1 — DG974EH6 — 29-1

- Z BUS BIT 0.2 — DH014GB6 — 38-1

- Z BUS BIT 0.3 — DH014GF6 — 47-1

- Z BUS BIT 0.4 — DH014GK6 — 56-1

- Z BUS BIT 0.5 — DJ014GB6 — 65-1

- Z BUS BIT 0.6 — DJ014GF6 — 74-1

- Z BUS BIT 0.7 — DJ014GK6 — 83-1

THIS PAGE IS FOR 3705-II ONLY.

CK002
030 SIM TO PN 1857256 EC 310268

20 ZM02 CD (CL PH) 2 -21P C A-B3S2 — 22R 103

29 ZJ10 CD (CK PH) 2 -21P C A-B3S2 — 21N 110

38 ZP02 CD (CJ PH) 2 -21P C A-B3S2 — 23Q 117

OUTPUT 78 REG

47 ZJ13 CD (CH PH) 2 -21P C A-B3S2 — 24P 125

56 ZB02 CD (CD PH) 2 -01J C A-B3S2 — 02L 131

65 ZB04 CD (CC PH) 2 -01J C A-B3S2 — 01H 138

74 ZD04 CD (CB PH) 2 -01J C A-B3S2 — 03K 145

OUTPUT 78 REG

83 ZB05 CD (CA PH) 2 -01J C A-B3S2 — 04J 153

EDGE CONN.
204 A-B3N1A13 — 01A-B4M6C04
01A-B4N6A04 — 01A-B4M5C02
211 A-B3N1A11 — 246 A-B3M1B13
01A-B3N6A02 — 01A-B4M6B04
218 A-B3M1E13 — 253 A-B3M1B11
01A-B4M6E04 — 01A-B4M6B02
225 A-B3M1D11
01A-B3M6D02
232 A-B3M1C13

FORCE ERROR IN BIT 0
11 19 A (DL) G08* 204
103 19 A-B3S2

FORCE ERROR IN BIT 1
11 19 A (DK) G13* 211
110 17 A-B3S2

FORCE ERROR IN BIT 2
11 16 A (DJ) P03* 218
117 16 A-B3S2

FORCE ERROR IN BIT 3
11 16 A (DH) P05* 225
125 17 A-B3S2

FORCE ERROR IN BIT 4
11 09 A (DD) D11* 232
131 09 A-B3S2

FORCE ERROR IN BIT 5
11 09 A (DC) M03* 239
138 07 A-B3S2

FORCE ERROR IN BIT 6
11 06 A (DB) M05* 246
145 06 A-B3S2

FORCE ERROR IN BIT 7
11 06 A (DA) M08* 253
153 07 A-B3S2

LOC. TYPE
A-B3S2 Y703

253 + FORCE ERROR IN BIT 7 — DA2
└DF002 └DJ002 └DM002

246 + FORCE ERROR IN BIT 6 — DB2
└DF002 └DJ002 └DM002

239 + FORCE ERROR IN BIT 5 — DC2
└DJ002 └DM002

232 + FORCE ERROR IN BIT 4 — DD2
└DH002 └DL002

225 + FORCE ERROR IN BIT 3 — DH2
└DH002 └DL002

218 + FORCE ERROR IN BIT 2 — DJ2
└DH002 └DL002

211 + FORCE ERROR IN BIT 1 — DK2
└DG002 └DK002

204 + FORCE ERROR IN BIT 0 — DL2
└DG002 └DK002

CC CONTROL

E.C.-HISTORY		MACH.27RNB
312922		
314419		FRAME 01
DATE	LAST EC	IBM CORP.
10-06-76	315033	P.N. 1750163

CK002
030

- INHIBIT B REG BYTE X ERROR—AJ001BG4— 2
+ POWER ON RESET—AP008DB4— 5
+ OUTPUT IAR INHIBIT B REG CHK—CA001FA2— 8
- BX TIME—CC001EG2— 11
- A TIME—CC001FC2— 14
- TO TIME—CC006AF8— 17
- T2 TIME—CC006DF9— 20
- T1 TIME—CC006ED6— 23
- T3 TIME—CC006HB6— 26
- ANY I TIME—CC008BM2— 29
- UNUSED—CK003001— 32
- GATE INPUT 72—CQ004DL6— 35
+ IPL INHIBIT SDR CK—CR003AE4— 38
- I-O INHIBIT B REG MACH CHK—CS004EF2— 41
- SAR BYTE 0 EVEN PARITY—CV001FG2— 44
- SAR BYTE X EVEN PARITY—CV001HE4— 47
+ B REG BYTE X EVEN PARITY—DF974GG2— 50
+ ALU+AREG+ZBUS BYTE X ERROR—DF976EJ6— 53
- B REG BYTE 0 EVEN PARITY—DG974GG2— 56
+ ALU+AREG+ZBUS BYTE 0 ERROR—DG976EJ6— 59
- B REG BYTE 1 EVEN PARITY—DK974GG2— 62
+ ALU+AREG+ZBUS BYTE 1 ERROR—DK976EJ6— 65
- OP REG BYTE O•P EVEN PARITY—DP992GL2— 68
- SDR BYTE 0 EVEN PARITY—DP993SA4— 71
- OP REG BYTE N EVEN PARITY—DR992GL2— 74
- SDR BYTE 1 OR X EVEN PARITY—DR993SA4— 77
- SAR BYTE 1 PARITY ERROR—DS001EL2— 80

CB: 32, 53 → DOT → 102
CA: 17 ZB10, 11 ZB04, 29 ZB05 → AR A OR → 19C 109 (A-B3N2)
AE: 2, 50 → DOT → 116
BB: 41 ZG12, 35 -G13 → A OR A → 26D 123, 26E 127 (A-B3N2)
AG: 8 ZU02, 26 ZP13, 11 ZB04, 29 ZB05 → A A OR → 41D 135 (A-B3N2)

OP REG BYTE 1 PARITY ERROR
BG: 11 ZB04, 23 ZG11, 74 ZD07 → A → 06F 153 (A-B3N2)

OP REG BYTE 0 PARITY ERROR
BD: 11 ZB04, 23 ZG11, 68 ZB09 → A → 09C 161 (A-B3N2)

SAR EVEN PARITY
BH: 44 -M11, 47 ZM12, 80 -P11 → OR → P12*169 (A-B3N2)

SAR BYTE 1 PARITY ERROR
BE: 14 ZP06, 20 ZM04, 80 ZP11 → A → 31L 177 (A-B3N2)

SAR BYTE 0 PARITY ERROR
BA: 14 ZP06, 20 ZM04, 44 ZM11 → A → 34H 185 (A-B3N2)

AB: 5 ZS12, 102 ZG08, 109 -21Q → PH S → 22N 203 (A-B3N2)
AF: 116 ZJ09 → N → 21E 216 (A-B3N2)
AA: 5 ZS12, 123 -24L, 17 ZB10, 127 -23H, 17 ZB10 → AR FL S A S A R → 24J 223 (A-B3N2)
AD: 5 ZS12, 65 ZJ11, 109 -21K → PH S → 22H 238 (A-B3N2)
AC: 5 ZS12, 59 ZG09, 109 -23R → PH S → 24P 245 (A-B3N2)

SAR BYTE X PARITY ERROR
AH: 14 ZP06, 20 ZM04, 47 ZM12 → A → 31J 253 (A-B3N2)

SDR BYTE 1 PARITY ERROR
BF: 20 ZM04, 38 ZS02, 77 ZM13 → A → 44C 261 (A-B3N2)

SDR BYTE 0 PARITY ERROR
BC: 20 ZM04, 38 ZS02, 71 ZS02 → A → 43F 269 (A-B3N2)

FC: 203 -38H, 20 ZM04, 216 -39L, 223 -38L, 135 -38K → A OR A → 39K 302, 39J 306 (A-B3N2)
FL: 238 -43H, 20 ZM04, 62 -U03, 223 -43L, 135 -43K → A OR A → 44K 337, 44J 341 (A-B3N2)
FE: 245 -28C, 20 ZM04, 56 -M03, 223 -28F, 135 -28E → A OR A → 29E 349, 29D 353 (A-B3N2)

253 — SAR BYTE X PARITY ERROR—EA6 (CK006 CK007)
185 — SAR BYTE 0 PARITY ERROR—ED6 (CK006 CK007)
269 — SDR BYTE 0 PARITY ERROR—EF6 (CK006 CK007)
161 — OP REG BYTE 0 PARITY ERROR—EG6 (CK006 CK007)
177 — SAR BYTE 1 PARITY ERROR—EJ6 (CK006 CK007)
261 — SDR BYTE 1 PARITY ERROR—EK6 (CK006 CK007)
153 — OP REG BYTE 1 PARITY ERROR—EM6 (CK006 CK007)
302 + ALU CR B REG BYTE X ERROR—FC2 (CK007)
306 — ALU CR B REG BYTE X ERROR—FC6 (CK006)
349 + ALU CR B REG BYTE 0 ERROR—FE2 (CK007)
353 — ALU CR B REG BYTE 0 ERROR—FE6 (CK006)
169 + SAR EVEN PARITY—FF2 (CM002 CR003 CS002)
337 + ALU CR B REG BYTE 1 ERROR—FL2 (CK007)
341 — ALU CR B REG BYTE 1 ERROR—FL6 (CK006)

THIS PAGE IS FOR 3705-II ONLY.

CK003
030 SIM TO PN 5997604 EC 310268

EDGE CONN.
102 RESISTOR
A-B3N2G08
01A-B4K6802
C*A-B3K1811
116 A-B3J1D13
01A-B4J6D04
169 A-B3N1311
01A-B4N6802

LOC. TYPE
A-B3N2 6819

ERROR DETECTION
E.C.-HISTORY-D
312922
314419
DATE LAST EC
08-31-76 313053
MACH.27RNB
FRAME 01
IBM CORP.SDD CK003
P.N. 1750154 030

- BYTE X ERROR ——— CK006GA6 — 2-2
- INDATA PARITY ERROR ——— CK006GD6 — 12-2
- BYTE 0 ERROR ——— CK006GE2 — 22-1
- SAR PARITY ERROR ——— CK006GH6 — 32-2
- BYTE 1 ERROR ——— CK006GJ2 — 42-2
- OP REG PARITY ERROR ——— CK006GK6 — 52-2
- SDR PARITY ERROR ——— CK006GL6 — 62-2
- GATE INPUT 7D ——— CQ005FK6 — 72-7
- GATE STATUS TO DISPLAY A ——— CU001EH6 — 82-6

MACH CK TO INDATA BIT 0.7
12 -14N A CR |S08 104
72 ZD09 U4
A-B3N2

INDATA PARITY CK TO DISPLY A
12 -11Q A CR |B11 111
82 ZU13 U4
A-B3N2

MACH CK TO INDATA BIT 0.0
2 -09R A OR |U06 118
72 ZD09 U4
A-B3N2

BYTE X MACH CK TO DISPLAY A
2 -09P A CR |D10 125
82 ZU13 U4
A-B3N2

MACH CK TO INDATA BIT 0.5
62 -49W A OR |S07 132
72 ZD09 U4
A-B3N2

SDR PARITY CK TO DISPLAY A
62 -49T A OR |U11 139
82 ZU13 U4
A-B3N2

MACH CK TO INDATA BIT 0.6
52 -46T A OR |U09 146
72 ZD09 U4
A-B3N2

OP REG PARITY CK TO DISPLY A
52 -46V A OR |P10 153
82 ZU13 U4
A-B3N2

MACH CK TO INDATA BIT 0.2
42 -54L A OR |S04 160
72 ZD09 U4
A-B3N2

BYTE 1 MACH CK TO DISPLAY A
42 -54H A OR |S17 167
82 ZU13 U4
A-B3N2

MACH CK TO INDATA BIT 0.4
32 -51H A OR |M08 174
72 ZD09 U4
A-B3N2

SAR PARITY CK TO DISPLAY A
32 -51L A OR |U10 181
82 ZU13 U4
A-B3N2

MACH CK TO INDATA BIT 0.1
22 -06N A OR |L05 188
72 ZD09 U4
A-B3N2

LOC. TYPE
A-B3N2 6819

BYTE 0 MACH CK TO DISPLAY A
22 -06R A CR |B07 204
82 ZU13 U4
A-B3N2

125 + BYTE X MACH CK TO DISPLAY A — DA2
↳AP011

204 + BYTE 0 MACH CK TO DISPLAY A — DC2
↳AP012

167 + BYTE 1 MACH CK TO DISPLAY A — DE2
↳AP012

181 + SAR PARITY CK TO DISPLAY A — DG2
↳AP012

139 + SDR PARITY CK TO DISPLAY A — DJ2
↳AP013

153 + OP REG PARITY CK TO DISPLY A — DL2
↳AP013

111 + INDATA PARITY CK TO DISPLY A — DN2
↳AP012

118 + MACH CK TO INDATA BIT 0.0 — EB2
↳CU011

188 + MACH CK TO INDATA BIT 0.1 — EC2
↳CU011

160 + MACH CK TO INDATA BIT 0.2 — ED2
↳CU011

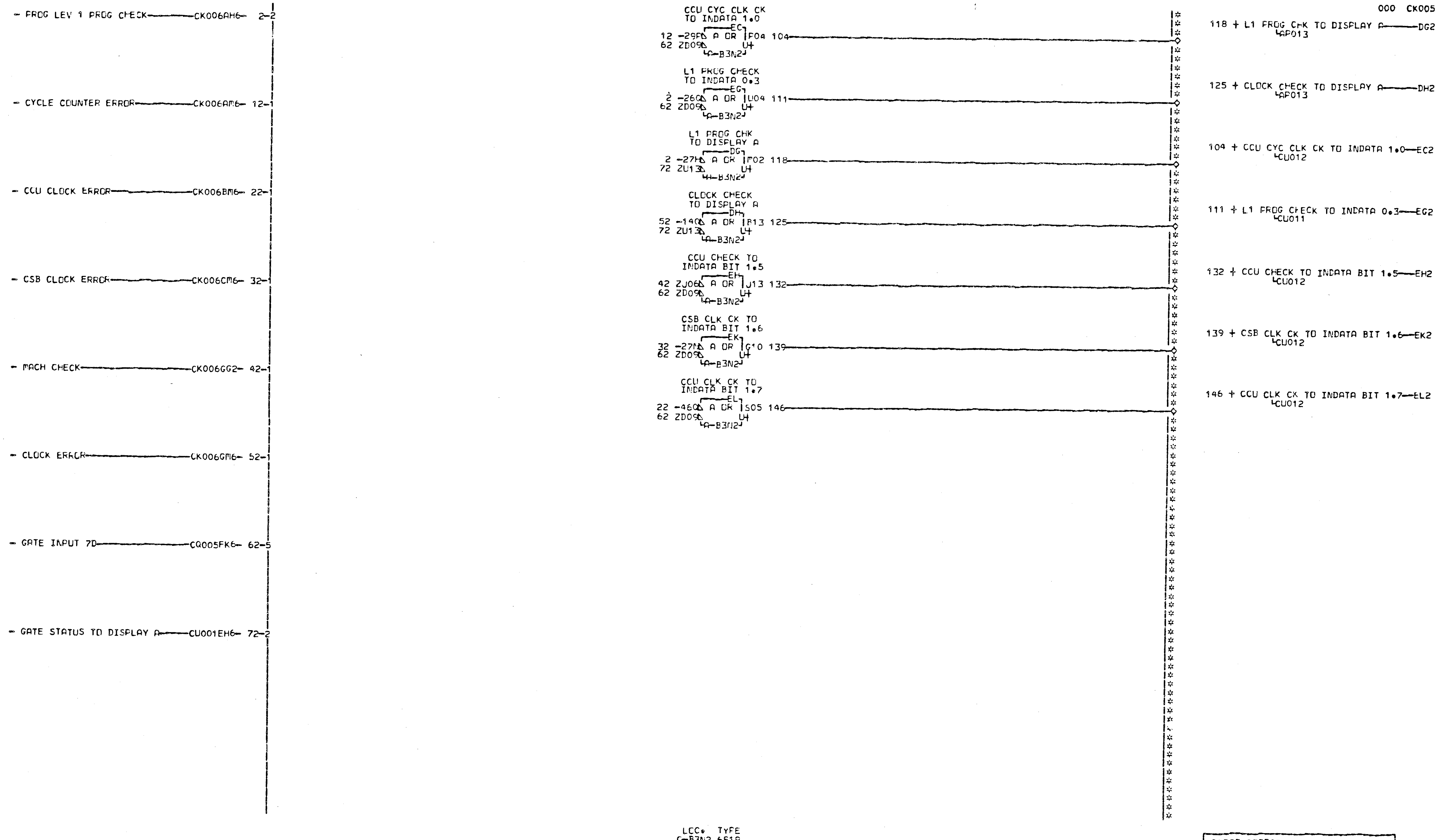
174 + MACH CK TO INDATA BIT 0.4 — EF2
↳CU011

132 + MACH CK TO INDATA BIT 0.5 — EG2
↳CL011

146 + MACH CK TO INDATA BIT 0.6 — EH2
↳CL011

104 + MACH CK TO INDATA BIT 0.7 — EJ2
↳CU011

ERROR DETECTION
E.C.-HISTORY — B
309521C
MACH.27RNB
FRAME 01
IBM CORP.SDD | CK004
DATE LAST EC
04-19-72 309545
P.N. 5997605 | 000



- PROG LEV 1 PROG CHECK CK006AH6 2-2
- CYCLE COUNTER ERROR CK006AM6 12-1
- CCU CLOCK ERROR CK006BM6 22-1
- CSB CLOCK ERROR CK006CM6 32-1
- MACH CHECK CK006GG2 42-1
- CLOCK ERROR CK006GM6 52-1
- GATE INPUT 7D CQ005FK6 62-5
- GATE STATUS TO DISPLAY A CU001EH6 72-2
CCU CYC CLK CK TO INDATA 1.0
EC
12 -29F A OR F04 104
62 ZD09 U4
A-B3N2
L1 PROG CHECK TO INDATA 0.3
EG
2 -26C A OR U04 111
62 ZD09 U4
A-B3N2
L1 PROG CHK TO DISPLAY A
DG
2 -27H A OR M02 118
72 ZU13 U4
H-B3N2
CLOCK CHECK TO DISPLAY A
DH
52 -14C A OR B13 125
72 ZU13 U4
A-B3N2
CCU CHECK TO INDATA BIT 1.5
EH
42 ZJ06 A OR J13 132
62 ZD09 U4
A-B3N2
CSB CLK CK TO INDATA BIT 1.6
EK
32 -27N A OR G10 139
62 ZD09 U4
A-B3N2
CCU CLK CK TO INDATA BIT 1.7
EL
22 -46C A OR S05 146
62 ZD09 U4
A-B3N2
118 + L1 PROG CHK TO DISPLAY A DG2
AP013
125 + CLOCK CHECK TO DISPLAY A DH2
AP013
104 + CCU CYC CLK CK TO INDATA 1.0 EC2
CU012
111 + L1 PROG CHECK TO INDATA 0.3 EG2
CU011
132 + CCU CHECK TO INDATA BIT 1.5 EH2
CU012
139 + CSB CLK CK TO INDATA BIT 1.6 EK2
CU012
146 + CCU CLK CK TO INDATA BIT 1.7 EL2
CU012
LOC. TYPE
A-B3N2 6819

CK005
000

ERROR DETECTION		
E.C.-HISTORY B 309521C	MACH.27RNB	
	FRAME 01	
DATE 04-19-72 LAST EC 309545	IBM CORP.SDD	CK005
	P.N. 5997606	000

030 CK006

- CYCLE COUNTER ERROR — CC005DG6 — 2
- CCU CLOCK ERROR — CC005EM6 — 7
- SAR BYTE X PARITY ERROR — CK003EA6 — 12
- SAR BYTE 0 PARITY ERROR — CK003ED6 — 17
- SDR BYTE 0 PARITY ERROR — CK003EF6 — 22
- OP REG BYTE 0 PARITY ERROR — CK003EG6 — 27
- SAR BYTE 1 PARITY ERROR — CK003EJ6 — 32
- SDR BYTE 1 PARITY ERROR — CK003EK6 — 37
- OP REG BYTE 1 PARITY ERROR — CK003EM6 — 42
- ALU OR B REG BYTE X ERROR — CK003FC6 — 47
- ALU OR B REG BYTE 0 ERROR — CK003FE6 — 52
- ALU OR B REG BYTE 1 ERROR — CK003FL6 — 57
- SET PROG LEV 1 PROG CHK ERR — CK007BB6 — 62
- CSB CLOCK ERROR — CK007BM2 — 67
+ RESET MACHINE CHECK — CU006CB6 — 72 — A42
- INDATA ERROR BYTE X — CU013DD6 — 77
- INDATA ERROR BYTE 0 — CU013DF6 — 82
- INDATA ERROR BYTE 1 — CU013DJ6 — 87

CYCLE CNTR ERR — AE — FLA36R — 103
MACH CHECK — CE — 103 530A OR J06 — 203 — A42 — 16U
CCU CLOCK ERR — BC — FLA46L — 312
CSB CLOCK ERR — CA — FLA41R — 321
SAR PARITY ERROR — CF — OR A51P — 125
INDATA PARITY ERROR — CC — OR A23T — 133
ALU BYTE 1 — BD — FLA14U — 303
ALU BYTE X — AA — FLA39U — 330
SAR BYTE X — AB — FLA39T — 339
INDATA BYTE X — AC — FLA36W — 348
ALU BYTE 0 — AF — FLA29U — 357
INDATA BYTE 0 — AH — FLA26W — 366
SAR BYTE 0 — AG — FLA29T — 375
OP REG BYTE 0 — BA — FLA49J — 384
BYTE X ERROR — CB — OR A21U — 424
BYTE 0 ERROR — CD — OR A03E — 545
SDR BYTE 0 — BB — FLA49H — 457
L1 PROG CHK — AD — FLA39P — 466
SAR BYTE 1 — BE — FLA14T — 475
INDATA BYTE 1 — BF — FLA11W — 484
BYTE 1 ERROR — CG — OR A53E — 503
OP REG BYTE 1 — BG — FLA44P — 553
SDR BYTE 1+X — BH — FLA44N — 562
CLOCK ERROR — CH — OR A53N — 571

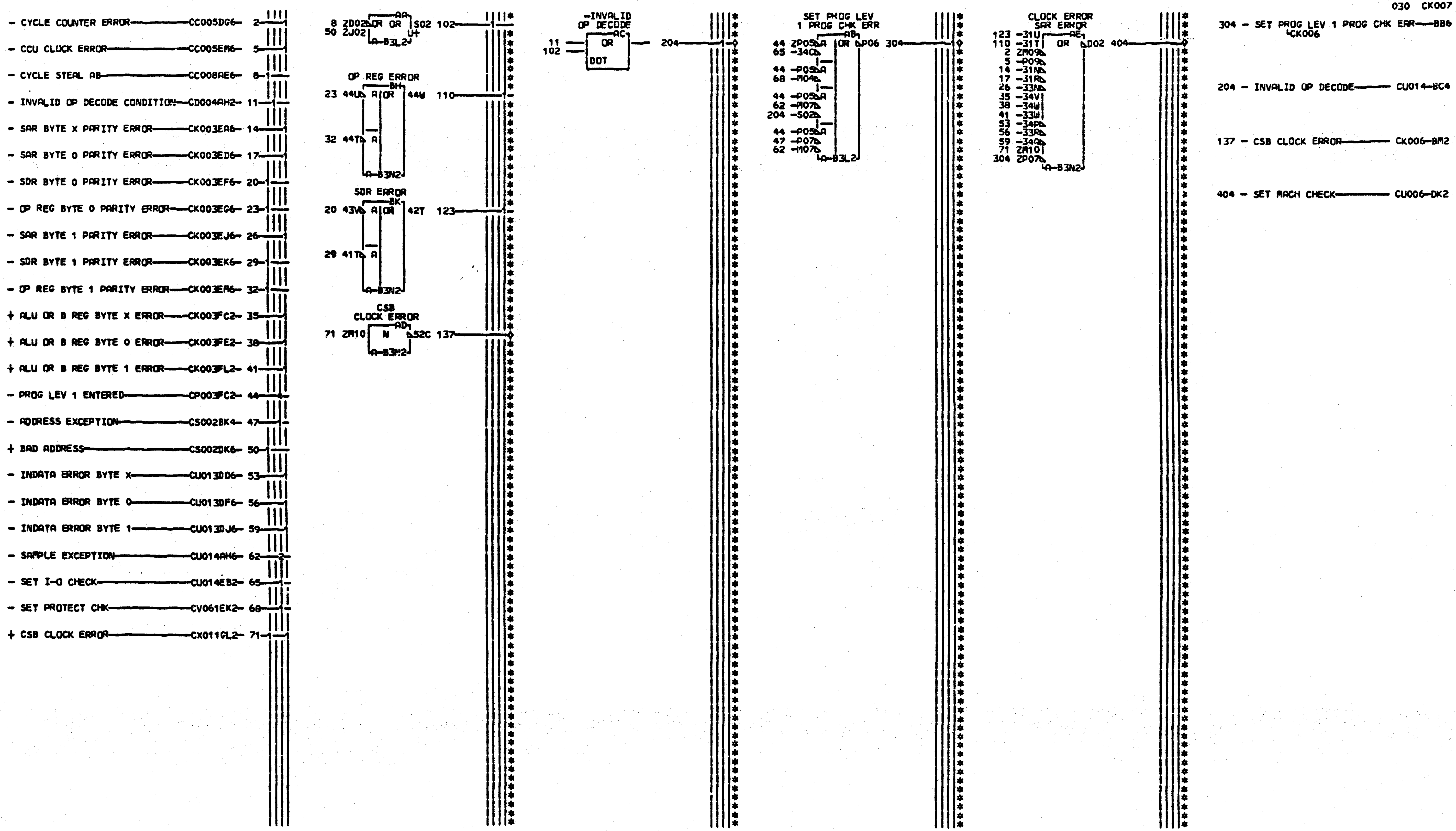
- 466 — PROG LEV 1 PROG CHECK — CK005-AH6
- 103 — CYCLE COUNTER ERROR — CK005-AM6
- 312 — CCU CLOCK ERROR — CK005-BM6
- 321 — CSB CLOCK ERROR — CK005-CM6
- 424 — BYTE X ERROR — CK004-GA6
- 133 — INDATA PARITY ERROR — CK004-GD6
- 545 — BYTE 0 ERROR — CK004-GE2
- 203 — MACH CHECK — CK005 CU001 CU006 — GG2
- 125 — SAR PARITY ERROR — CK004-GH6
- 503 — BYTE 1 ERROR — CK004-GJ2
- 115 — OP REG PARITY ERROR — CK004-GK6
- 581 — SDR PARITY ERROR — CK004-GL6
- 571 — CLOCK ERROR — CK005-GM6

LOC. TYPE
A-B3N2 6819

THIS PAGE IS FOR 3705-II ONLY.

ERROR REGISTER		
E.C.-HISTORY-D	7ACH.27RNB	
312922 314419	FRAME 01	
DATE LAST EC 10-06-76 315053	IBM CORP.SDD P.N. 1750165	CK006 030

CK006
030 SIM TO PN 5997607 EC 309545

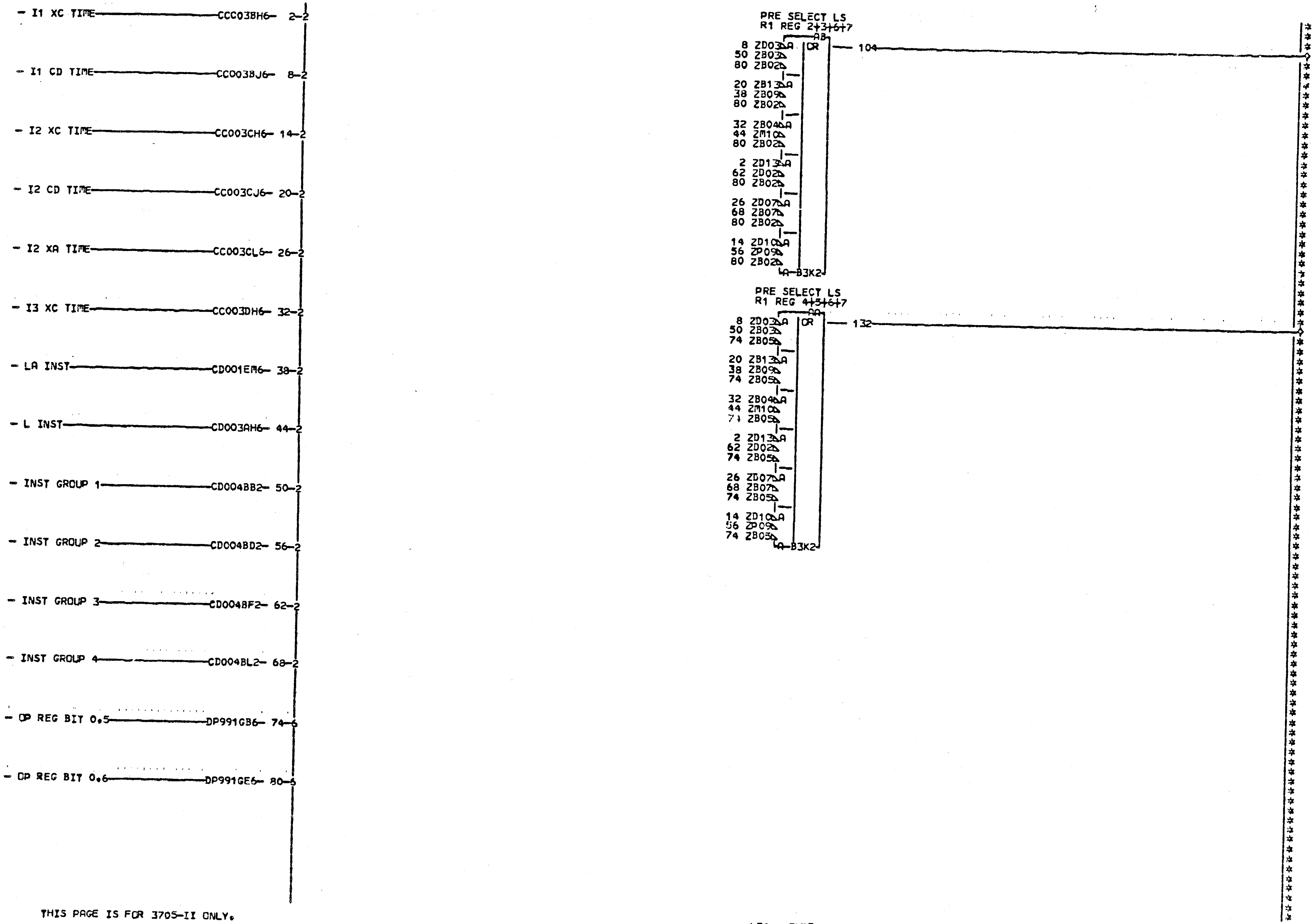


- CYCLE COUNTER ERROR——CC005DG6- 2
- CCU CLOCK ERROR——CC005EM6- 5
- CYCLE STEAL AB——CC008AE6- 8
- INVALID OP DECODE CONDITION——CD004AH2- 11
- SAR BYTE X PARITY ERROR——CK003EA6- 14
- SAR BYTE 0 PARITY ERROR——CK003ED6- 17
- SDR BYTE 0 PARITY ERROR——CK003EF6- 20
- OP REG BYTE 0 PARITY ERROR——CK003EG6- 23
- SAR BYTE 1 PARITY ERROR——CK003EJ6- 26
- SDR BYTE 1 PARITY ERROR——CK003EK6- 29
- OP REG BYTE 1 PARITY ERROR——CK003EM6- 32
+ ALU OR B REG BYTE X ERROR——CK003FC2- 35
+ ALU OR B REG BYTE 0 ERROR——CK003FE2- 38
+ ALU OR B REG BYTE 1 ERROR——CK003FL2- 41
- PROG LEV 1 ENTERED——CP003FC2- 44
- ADDRESS EXCEPTION——CS002BK4- 47
+ BAD ADDRESS——CS002DK6- 50
- INDATA ERROR BYTE X——CU013DD6- 53
- INDATA ERROR BYTE 0——CU013DF6- 56
- INDATA ERROR BYTE 1——CU013DJ6- 59
- SAMPLE EXCEPTION——CU014AH6- 62
- SET I-O CHECK——CU014EB2- 65
- SET PROTECT CHK——CV061EK2- 68
+ CSB CLOCK ERROR——CX011GL2- 71
OP REG ERROR
SDR ERROR
CSB CLOCK ERROR
-INVALID OP DECODE
SET PROG LEV 1 PROG CHK ERR
CLOCK ERROR SAR ERROR
304 - SET PROG LEV 1 PROG CHK ERR——BB6 CK006
204 - INVALID OP DECODE——CU014-BC4
137 - CSB CLOCK ERROR——CK006-BM2
404 - SET MACH CHECK——CU006-DK2

THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE
A-B3L2 6823
A-B3N2 6819

SECOND ERROR DETECTION AND PROG LEV 1 PROG CHK DETECTION	
E.C.-HISTORY-0 312922 314419	MACH.27RNB
	FRAME 01
DATE 10-06-76 LAST EC 315053	IBM CORP.SDD
	P.N. 1750166


- I1 XC TIME CC003BH6- 2-2
- I1 CD TIME CC003BJ6- 8-2
- I2 XC TIME CC003CH6- 14-2
- I2 CD TIME CC003CJ6- 20-2
- I2 XA TIME CC003CL6- 26-2
- I3 XC TIME CC003DH6- 32-2
- LA INST CD001EM6- 38-2
- L INST CD003AH6- 44-2
- INST GROUP 1 CD004BB2- 50-2
- INST GROUP 2 CD004BD2- 56-2
- INST GROUP 3 CD004BF2- 62-2
- INST GROUP 4 CD004BL2- 68-2
- OP REG BIT 0.5 DP991GB6- 74-6
- OP REG BIT 0.6 DP991GE6- 80-6
PRE SELECT LS
R1 REG 2+3+6+7
AB
8 ZD03 A
50 ZB03
80 ZB02
20 ZB13 A
38 ZB09
80 ZB02
32 ZB04 A
44 ZM10
80 ZB02
2 ZD13 A
62 ZD02
80 ZB02
26 ZD07 A
68 ZB07
80 ZB02
14 ZD10 A
56 ZP09
80 ZB02
OR
104
A-B3K2
PRE SELECT LS
R1 REG 4+5+6+7
AA
8 ZD03 A
50 ZB03
74 ZB05
20 ZB13 A
38 ZB09
74 ZB05
32 ZB04 A
44 ZM10
74 ZB05
2 ZD13 A
62 ZD02
74 ZB05
26 ZD07 A
68 ZB07
74 ZB05
14 ZD10 A
56 ZP09
74 ZB05
OR
132
A-B3K2

132 + PRE SELECT LS R1 REG 4+5+6+7—DE4
CL002

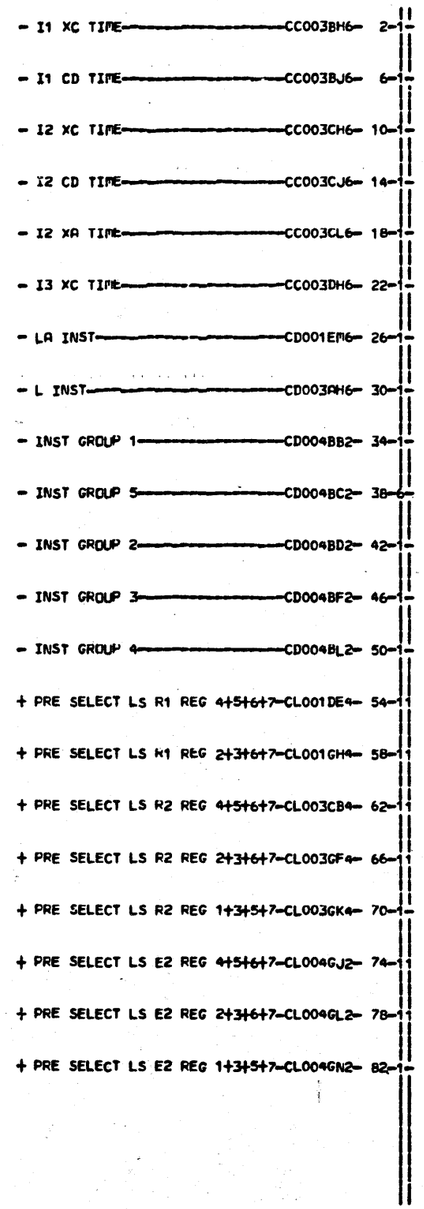
104 + PRE SELECT LS R1 REG 2+3+6+7—GH4
CL002

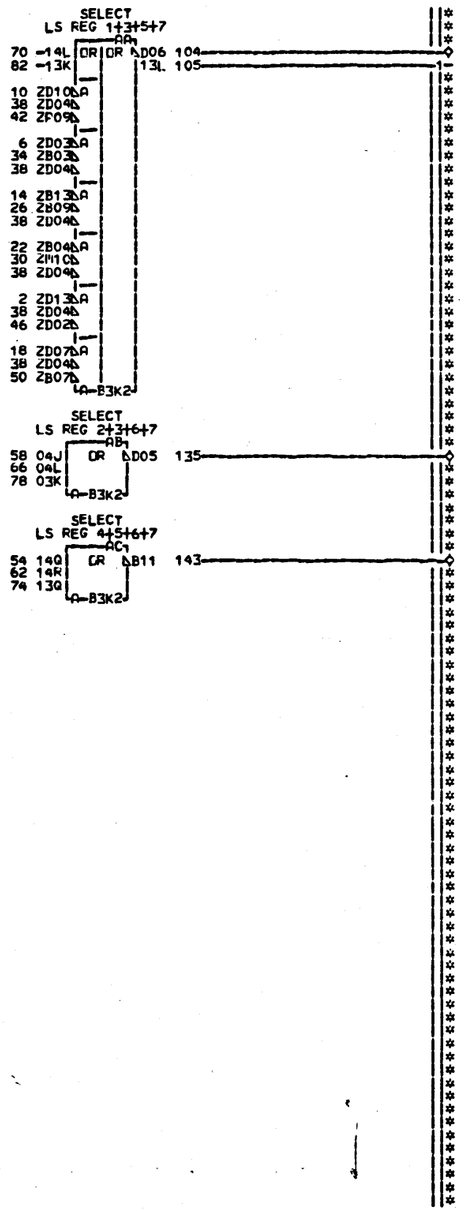
THIS PAGE IS FOR 3705-II ONLY.

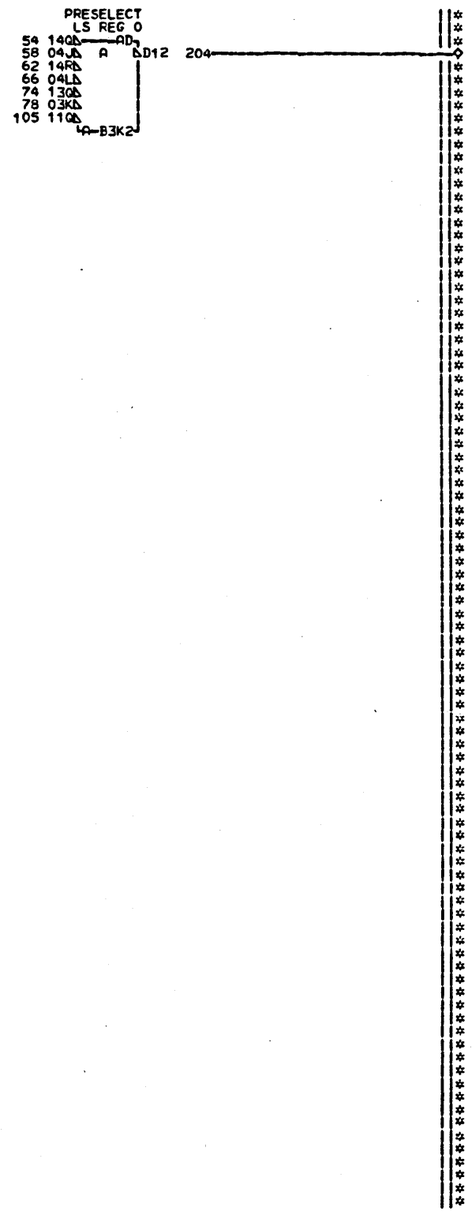
LOC. TYPE
A-B3K2 6816

CL001
030 SIM TO PN 5997609 EC 309545

LOCAL STORE CONTROL R1 SELECT GENERATION —E.C.-HISTORY—D	MACH.27RNB
312922 314419	FRAME 01
DATE LAST EC 10-06-76 315053	IBM CORP.SDD P.N. 1750167







000 CL002

104 - SELECT LS REG 1+3+5+7 — CC006-CK2

135 - SELECT LS REG 2+3+6+7 — CC006-CL2

143 - SELECT LS REG 4+5+6+7 — CC006-CM2

204 - PRESELECT LS REG 0 — CH001-FH6

LOC. TYPE
A-B3K2 6816

LOCAL STORE CONTROL
R2 SEL GENERATION AND REG SEL
—E.C.—HISTORY— B FACH.27RNB
309521C

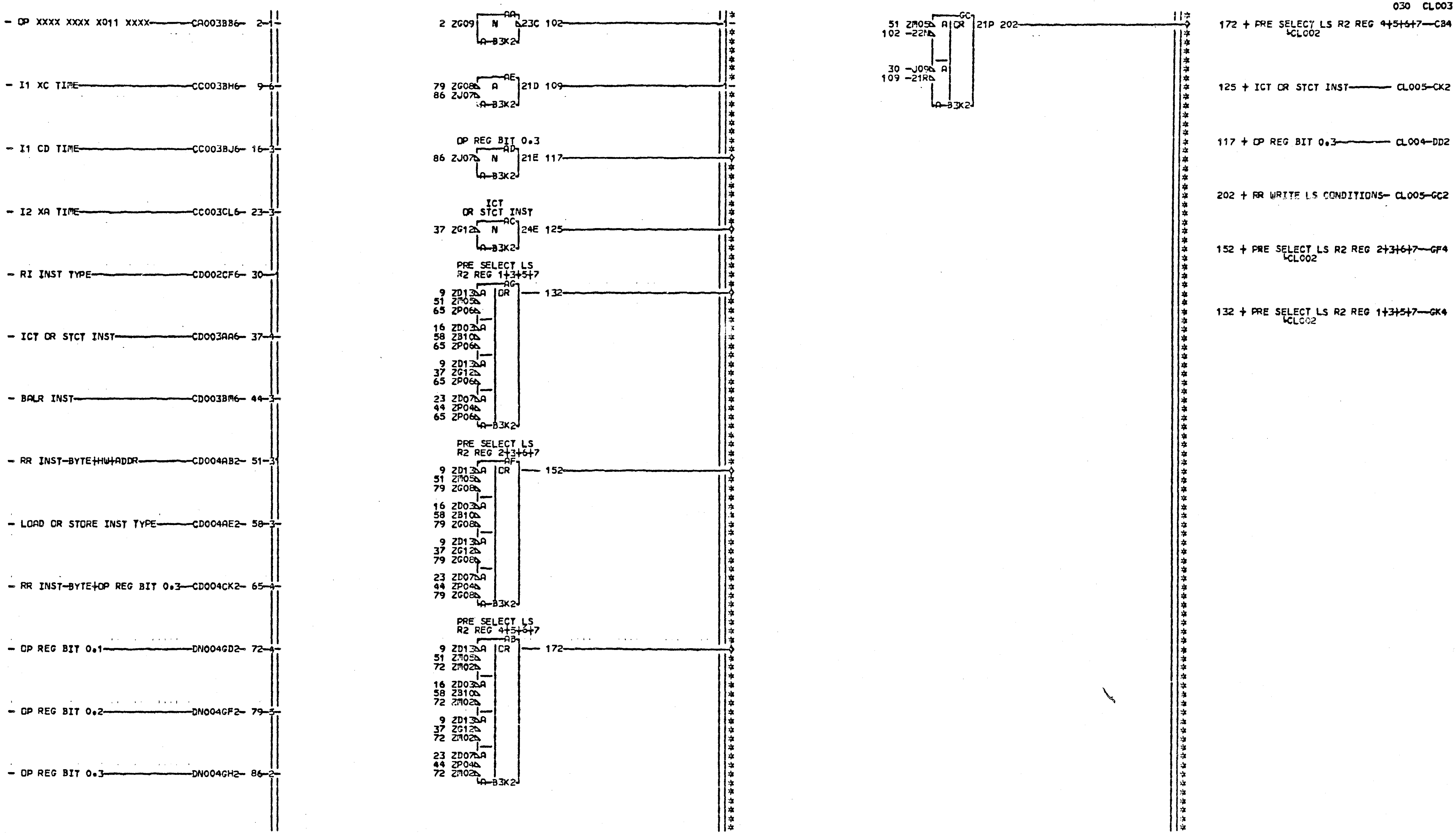
FRAME 01

IBM CORP.SDD CL002

DATE LAST EC
04-19-72 309545

P.N. 5997610 000

CL002
000



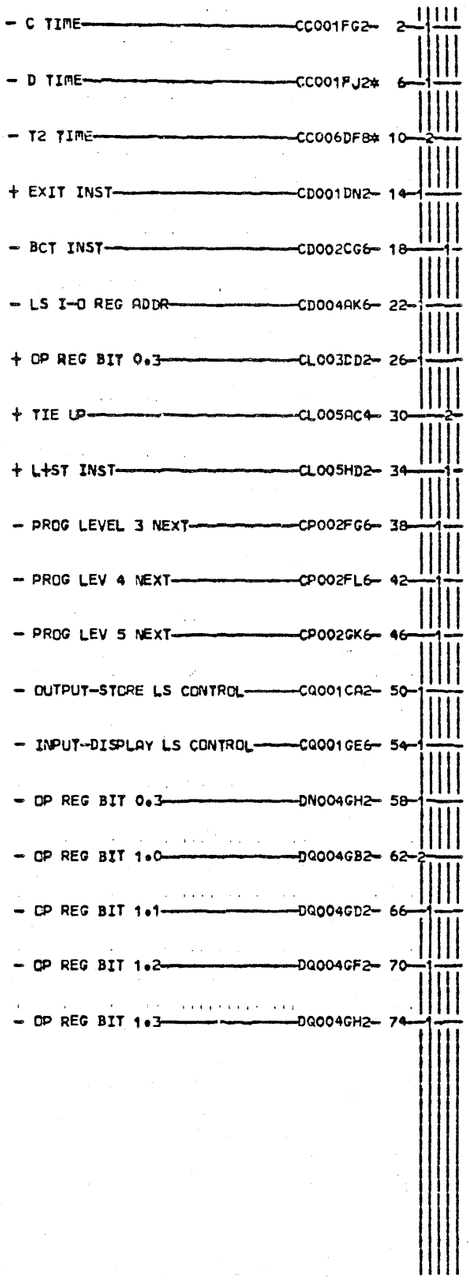
- OP XXXX XXXX X011 XXXX—CA003BB6— 2-1-
- I1 XC TIME—CC003BH6— 9-6-
- I1 CD TIME—CC003BJ6— 16-3-
- I2 XA TIME—CC003CL6— 23-3-
- RI INST TYPE—CD002CF6— 30-
- ICT OR STCT INST—CD003AA6— 37-4-
- BALR INST—CD003BM6— 44-3-
- RR INST-BYTE+HW+ADDR—CD004AB2— 51-3
- LOAD OR STORE INST TYPE—CD004AE2— 58-3-
- RR INST-BYTE+OP REG BIT 0.3—CD004CK2— 65-4-
- OP REG BIT 0.1—DN004GD2— 72-4-
- OP REG BIT 0.2—DN004GF2— 79-3-
- OP REG BIT 0.3—DN004GH2— 86-2-
2 ZG09 N AA 23C 102 A-B3K2
79 ZG08 86 ZJ07 A AE 21D 109 A-B3K2
OP REG BIT 0.3
86 ZJ07 N AD 21E 117 A-B3K2
ICT OR STCT INST
37 ZG12 N AC 24E 125 A-B3K2
PRE SELECT LS R2 REG 1+3+5+7
9 ZD13 51 ZM05 65 ZP06 16 ZD03 58 ZB10 65 ZP06 9 ZD13 37 ZG12 65 ZP06 23 ZD07 44 ZP04 65 ZP06 A OR AG 132 A-B3K2
PRE SELECT LS R2 REG 2+3+6+7
9 ZD13 51 ZM05 79 ZG08 16 ZD03 58 ZB10 79 ZG08 9 ZD13 37 ZG12 79 ZG08 23 ZD07 44 ZP04 79 ZG08 A OR AF 152 A-B3K2
PRE SELECT LS R2 REG 4+5+6+7
9 ZD13 51 ZM05 72 ZM02 16 ZD03 58 ZB10 72 ZM02 9 ZD13 37 ZG12 72 ZM02 23 ZD07 44 ZP04 72 ZM02 A OR AB 172 A-B3K2
51 ZM05 102 -22N 30 -J09 109 -21R A OR GC 21P 202 A-B3K2
172 + PRE SELECT LS R2 REG 4+5+6+7—CB4 CL002
125 + ICT OR STCT INST— CL005-CK2
117 + OP REG BIT 0.3— CL004-DD2
202 + RR WRITE LS CONDITIONS— CL005-GC2
152 + PRE SELECT LS R2 REG 2+3+6+7—GF4 CL002
132 + PRE SELECT LS R2 REG 1+3+5+7—GK4 CL002

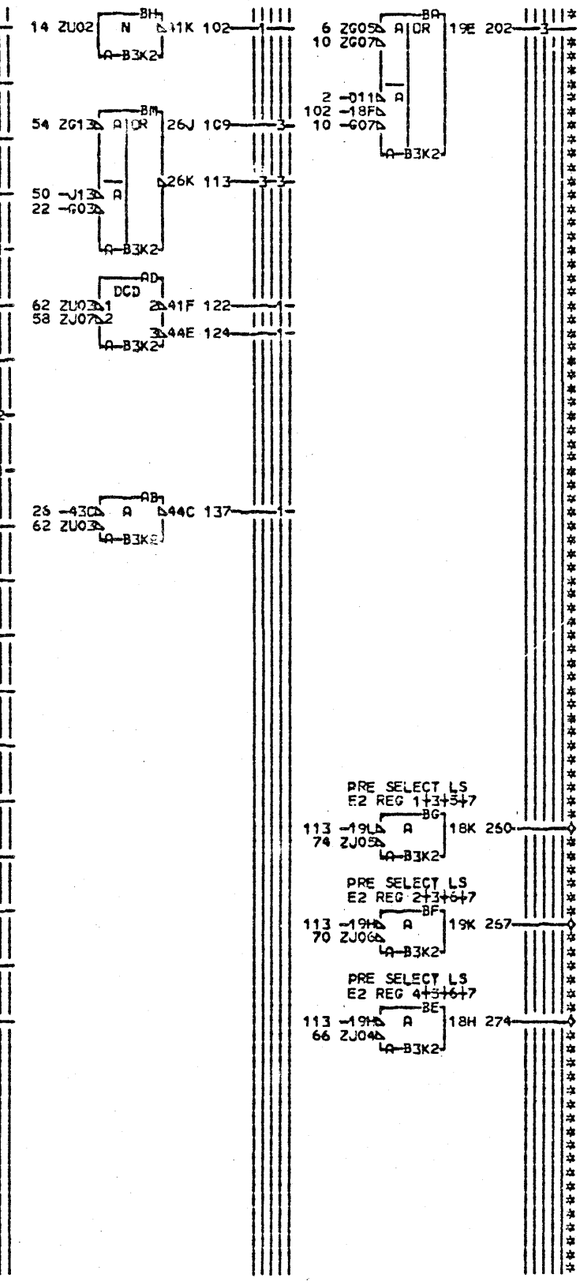
THIS PAGE IS FOR 3705-II ONLY.

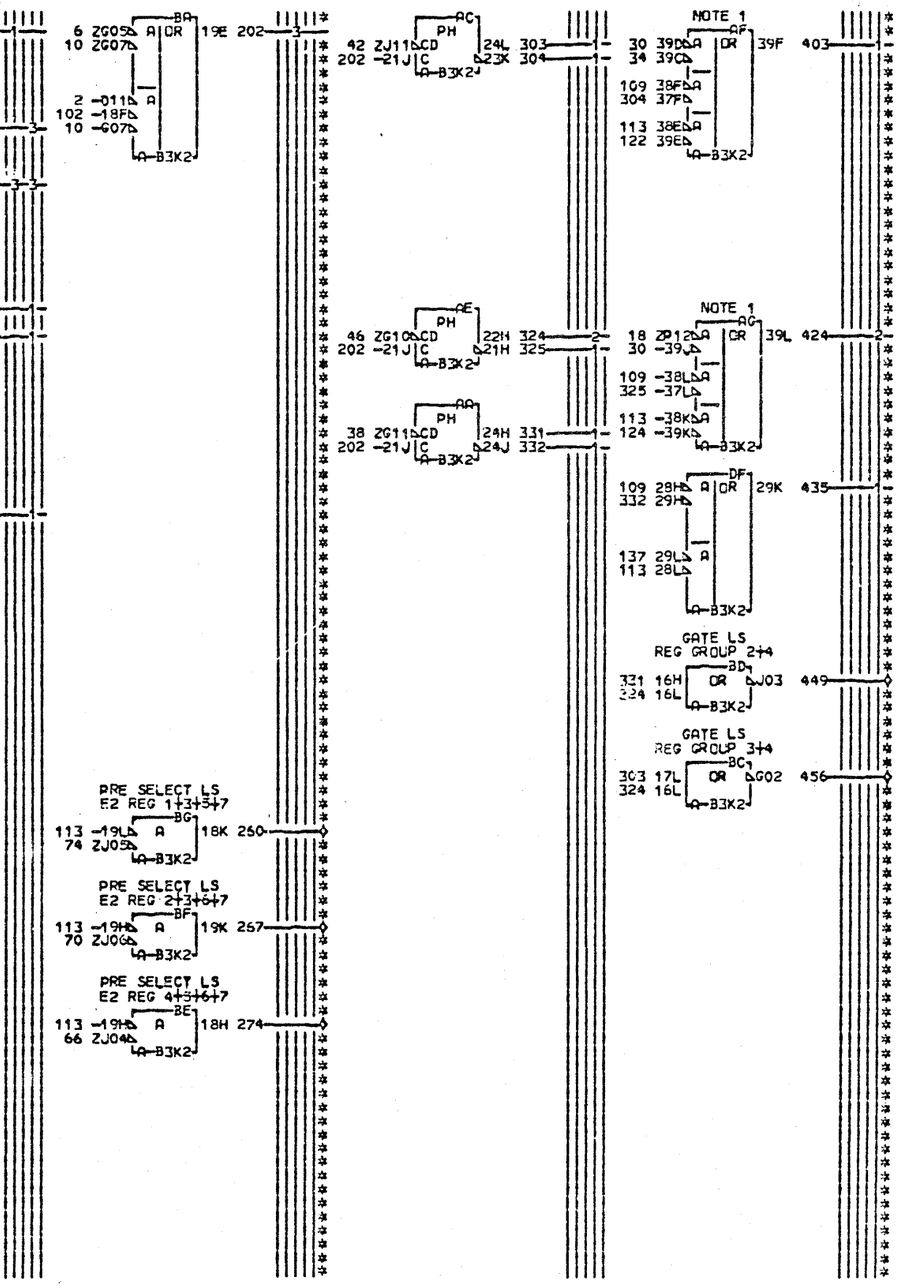
LOC. TYPE
A-B3K2 6816

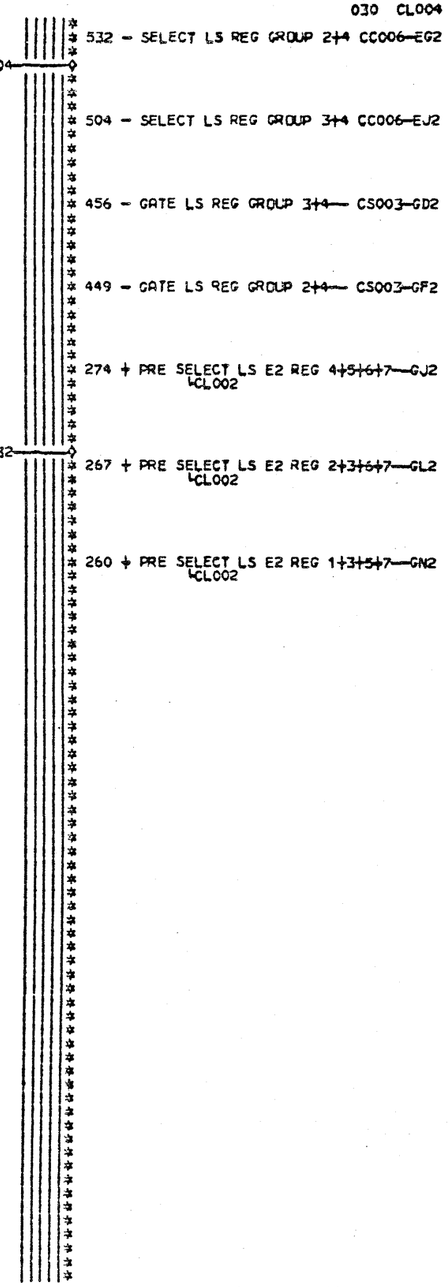
LOCAL STORE CONTROL
R2 SELECT GENERATION

E.C. HISTORY	
312922	MACH.27RNB
314419	FRAME 01
DATE 10-06-76 LAST EC 315033	IBM CORP.SDD
	P.N. 1750168









THIS PAGE IS FOR 3705-II ONLY.

NOTE 1 THIS 3A0 IS USED AS A 2A0

EDGE CONN.
6 RESISTOR A-B3K2205
10 RESISTOR A-B3K2607

LOC. TYPE
A-B3K2 6816

LOCAL STORE CONTROL
E2 SEL GENERATION AND ZONE SEL

E.C.-HISTORY-D	MACH.27RN8	
312922		
314419	FRAME 01	
	IBM CORP.SDD	CL004
DATE LAST EC	P.N. 1750169	030
10-06-76 315053		

CL004
030 SIM TO PN 5997612 EC 309545

- AX TIME — CC001EC2 — 2
- I1 AX TIME — CC003BB6 — 4
- I1 CD TIME — CC003BJ6 — 6
- I1 DA TIME — CC003BK6 — 8
- I2 AX TIME — CC003CB6 — 10
- I2 CD TIME — CC003CJ6 — 12
- I2 DA TIME — CC003CK6 — 14
- I3 CD TIME — CC003DJ6 — 16
- I3 DA TIME — CC003DK6 — 18
+ T1 TIME — CC006ED2 — 20
- BAL+LR INST — CD001DK6 — 22
+ B INST — CD002CB2 — 24
- BCL INST — CD002CC6 — 26
- BZL INST — CD002CD6 — 28
- BCT INST — CD002CG6 — 30
- BB+BCT INST — CD002DF2 — 32
- L INST — CD003AH6 — 34
- L+ST INST — CD003CG2 — 36
- INPUT + OUTPUT INST — CD003CK6 — 38
- LS I-O REG ADDR — CD004AK6 — 40
- INST GROUP 2 — CD004BD2 — 42
+ ICT OR STCT INST — CL003CK2 — 44
+ RR WRITE LS CONDITIONS — CL003GC2 — 46
- PROG LEV 1 NEXT — CP002EA2 — 48
- VIRGIN LEVEL — CP003BL6 — 50
- COND LS REG WRITE — CS003EF6 — 52
- GATE STATUS TO DISPLAY B — CU001EJ6 — 54
+ RESET — CU010FM2 — 56
- Z BUS BYTE 0 OR 0 AND 1 ZERO — CZ001ED6 — 58
- Z COND — CZ004EB2 — 60
- C COND — CZ005DC2 — 62

20 ZU13 AB N 53H 102 — A-B3K2

TIE UP
AA FWR XVHLM 36P 117 — A-B3K2

48 ZS13 AG A 53K 123
50 ZU12 — A-B3K2

Z COND TO IND
54 ZU05 BA A OR U07 131
60 ZU10 U+ — A-B3K2

C COND TO IND
54 ZU05 AH A OR U06 138
62 ZS11 U+ — A-B3K2

L+ST INST
36 ZM12 BC N 27W 145 — A-B3K2

NOTE 1
117 54D AC OR 54F 203
203 54C
58 U11
102 52F
20 U13
316 53E — A-B3K2

BR COND MET
24 ZB12 AR AD OR P05 303
26 ZS10
62 ZS11
28 -S09
60 -U10
32 -S12
203 -54C — A-B3K2

203 54J AE N 54K 316 — A-B3K2

38 ZU09 AF CR 34N 402
44 -33Q
46 -34Q
303 ZF05 — A-B3K2

COND LS WRITE
18 ZF13 BB AR CR 12L 503
8 ZF10
402 -36H
10 -F07
22 -M08
6 -D03
30 -P12
40 ZG03
52 ZG04
4 -J02
123 -17F
2 ZF11
56 ZM04
16 ZM09
34 ZM10
12 -B13
42 -P09
14 -M11
36 ZM12 — A-B3K2

117 + TIE UP — CL004-AC4
303 + BR COND MET — CS003-CC6
138 + C COND TO IND — AF014-EA2
131 + Z COND TO IND — AF014-EB2
503 - COND LS WRITE — CS007-FH2
145 + L+ST INST — CL004-HD2

NOTE 1 THIS 3AD IS USED AS A POLARITY HOLD.

LOC. TYPE
A-B3K2 6816

LOCAL STORE CONTROL
WRITE LS GENERATION
E.C.-HISTORY — B — MACH.27ANB
309321C
309538
FRAME 01
IBM CORP. SDD
DATE LAST EC
04-19-72 309545
P.N. 5997613
CL005
000

- CD TIME ——— CC001EJ2 - 2
- MEM STORE NEW TIME ——— CC001FE2 - 12
- D TIME ——— CC001FJ2 - 22
- T0 TIME ——— CC006AF8* 32
- T2 TIME ——— CC006DF8 - 42
- READ CALL WR CALL TIME ——— CC007FB6 - 52
- LATCHED STORE ——— CQ001GL2 - 62
+ BAD ADDRESS ——— CS002DK6 - 72
+ RESET ——— CU010FR2 - 82

RESET
AB
22 ZD10 A
42 ZB11
82 ZD11
A-B3T4
J03 103

SET MEM RESET
AA
A FL D09*203
2 ZG11 S S
32 ZJ10 14K 205
R
103 ZJ03 AR R
A-B3T4

READ CALL WR
ITE CALL PULSE
AD
52 ZB07 A B09*304
205 -05C
A-B3T4

MEM STORE NEW
AC
12 ZD02 A B05*310
62 ZD07
72 ZB04
205 -04E
A-B3T4

030 CM001

203 + MEM RESET ——— BB6
LCM003 LDB101

310 + MEM STORE NEW ——— DD2
LCM003 LDB101

304 - READ CALL WRITE CALL PULSE ——— DF6
LCM003

THIS PAGE IS FOR 3705-II ONLY.

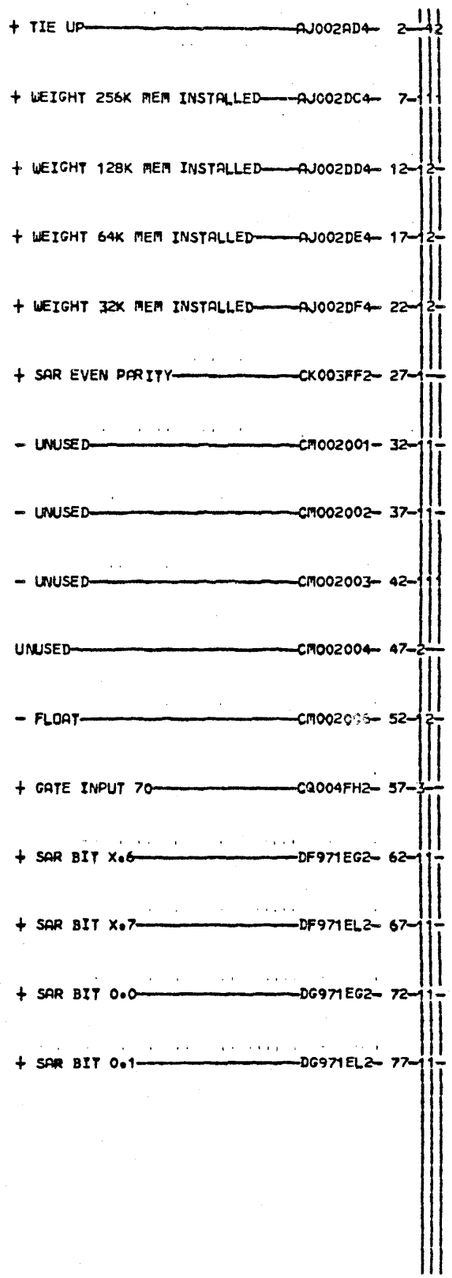
EDGE CONN.
32 RESISTOR
A-B3T4J10
203 A-B3F1D11
01A-B4F6D02
304 A-B3G1A11
01A-B4G6A02
310 A-B3F1E13
01A-B4F6E04

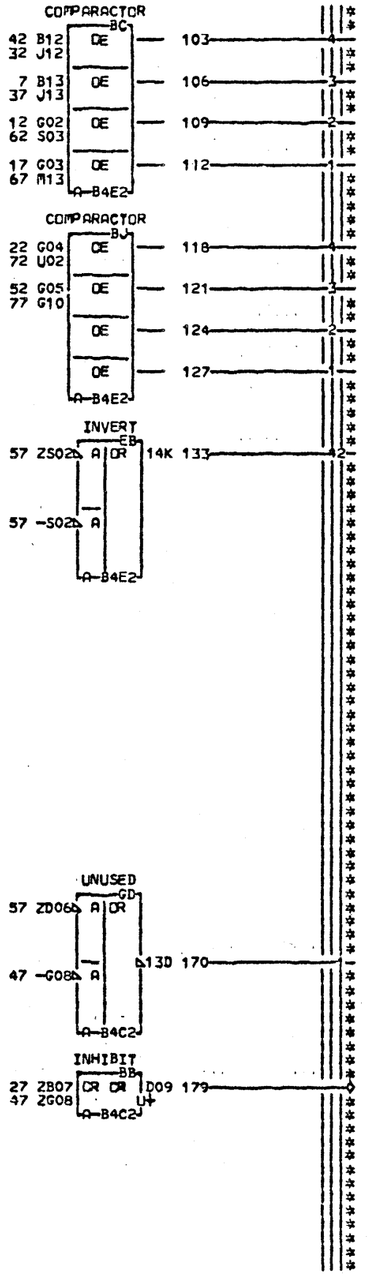
LOC. TYPE
A-B3T4 6824

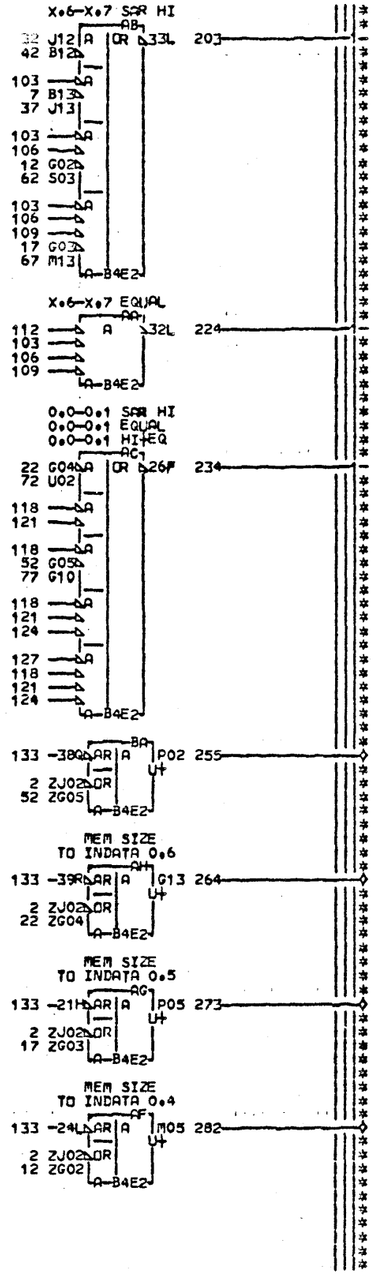
MEMORY CONTROLS CCU

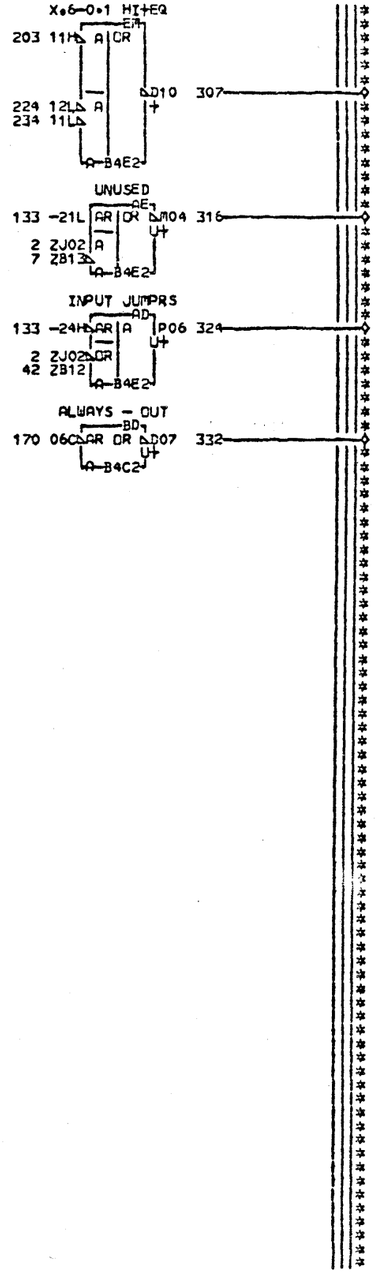
E.C.-HISTORY-D	MACH.27RNB
312922	
314419	FRAME 01
	IBM CORP.SDD CM001
DATE LAST EC	P.N. 1750170 030
10-06-76 315053	

CM001
030 SIM TO PN 5997614 EC 310268









030 CM002

307 - ADDRESS EXCEPTION CONDITION UCS002 — EM6

324 - UNUSED — CU011-FC2

316 - UNUSED — CU011-FD2

282 + MEM SIZE TO INDATA 0•4 — CU011-FE2

273 + MEM SIZE TO INDATA 0•5 — CU011-FF2

264 + MEM SIZE TO INDATA 0•6 — CU011-FG2

255 ALWAYS MINUS — CU011-FH2

179 + INHIBIT ADDR EXCEPT — CS002-GA6

332 - UNUSED — CU011-GC6

THIS PAGE IS FOR 3705-II ONLY•

LOC• TYPE
A-B4C2 AB93
A-B4E2 AB89

CC CONTROL
ADDR EXCPT AND GATE MEM SIZE
—E•C•-HISTORY— D• MACH•27RN8
312922
314419

FRAME 01

IBM CORP•SDD CM002

DATE LAST EC
09-29-76 315053

P•N• 1750171 030

CM002
030 SIM TO PN 5997615 EC 310268

+ MEM RESET — CM001BB6 — 2

+ MEM STORE NEW — CM001DD2 — 11 — 2

- READ CALL WRITE CALL PULSE — CM001DF6 — 20 — 1

- UNUSED — CM003001 — 29 — 2

- UNUSED — CM003002 — 38 — 2

- SET OUTPUT 79 — CQ005DC6 — 47 — 1

- TEST MODE LCH — CU005CB2 — 56 — 1

+ RESET — CU010FM2 — 65 — 1

+ SAR BIT X.P — DF971EC2 — 74 — 1

- Z BUS BIT 0.3 — DH014GF6 — 83 — 1

RESET

AD
OR
56 ZJ10
65 ZG12
A-B4E2
23R 103
P03 104

SET

AA
A
47 ZG11
83 ZJ09
A-B4E2
38N 110

ALWAYS + OUT

AR
AC
EV
29 ZB04
38 ZB07
74 ZD07
A-B4E2
WJ07 118

AB
DCD
20 ZJ11
29 ZB04
38 ZB07
A-B4E2
1 B05 125
3 B02 128
5 D02 131
FRAME 1 READ CALL WR CALL
7 D04 136

SEE NOTE

AE
AR
FL
S
R
103 -28V
2 ZG08
11 ZJ06N
11 ZJ06N
110 -26T
A-B4E2
M02 203

030 CM003

125 - UNUSED — DB2

131 - UNUSED — DC2

128 - UNUSED — DD2

136 + FRAME 1 READ CALL WR CALL — DE2
↳DS001

118 + SAR BIT X.P — CV001-DG2

104 + RESET MEMORY DIAG REG — DB101-GM6

203 - ALLOW SET MEMORY DIAG REG — SA2
↳DB101

THIS PAGE IS FOR 3705-II ONLY.

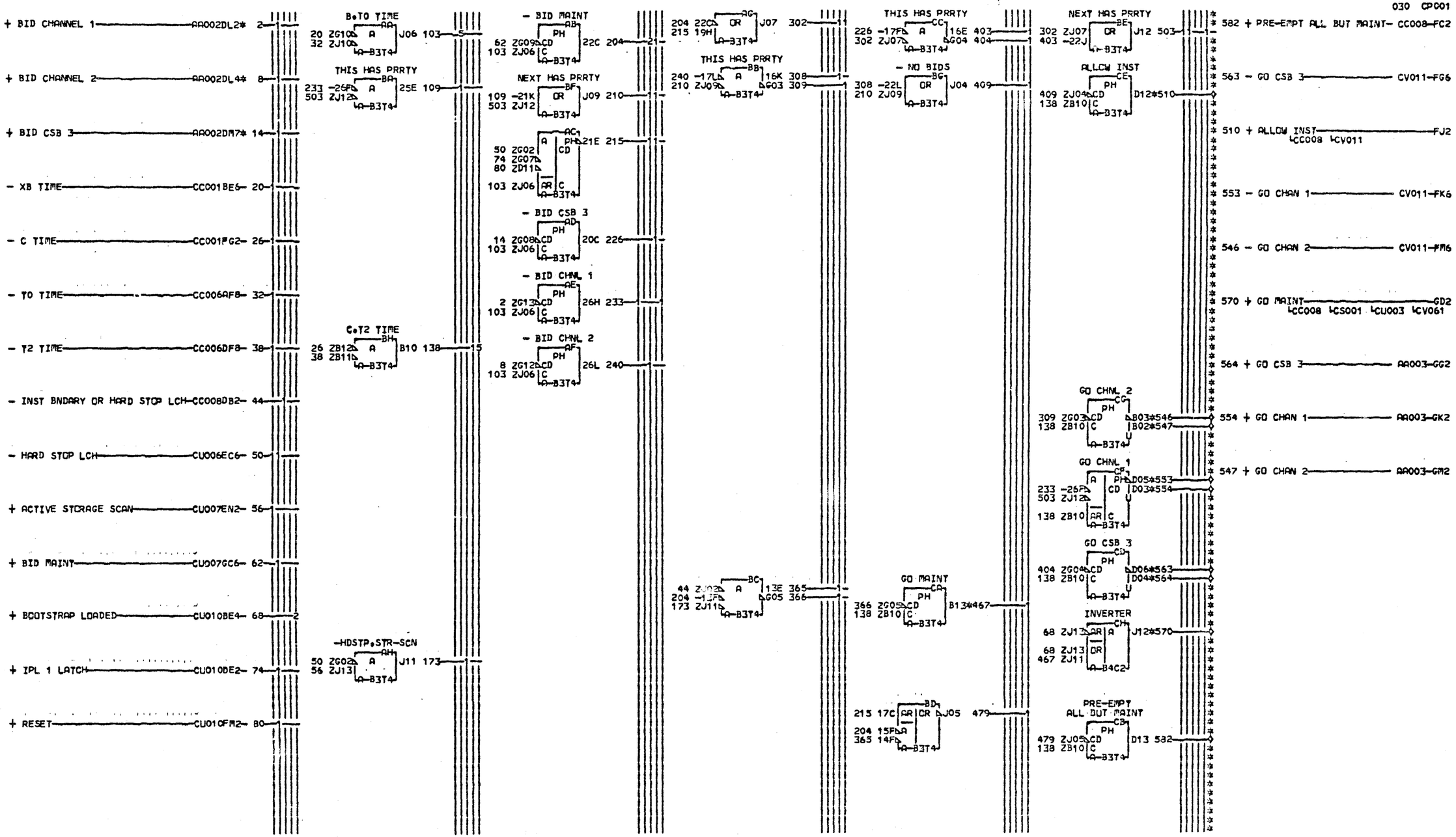
NOTE.
OUTPUT-79-BIT-0.3 SETS LTCH.
RESET OR NOT-TEST-MODE RESETS
LTCH. MEM-RESET RESETS LTCH
IF IT FOLLOWS A MEMORY-STORE-
CM003 NEW.

030 SIM TO PN 5997616 EC 310258

LOC. TYPE
A-B4E2 AB89

READ CALL WRITE CALL AND
SAR BYTE X AND 1 PARITY
E.C.-HISTORY — D-MACH.27RNB

312922		
314419	FRAME 01	
	IBM CORP.SDD	CM003
DATE LAST EC	P.N. 1750172	030
10-06-76 315053		



THIS PAGE IS FOR 3705-II ONLY.

CP001
030 SIM TO PN 5997617 EC 314418

EDGE CONN. 01A-B4U6D02 570 A-B3U1C11
2 RESISTOR 546 A-B3U1E11 01A-B4U6C02
A-B3T4G13 01A-B4U6E02
8 RESISTOR 547 A-B3A5B12
A-B3T4G12 553 A-B3U1D13
14 RESISTOR 01A-B4U6D04
A-B3T4G08 554 A-B3A3B10
467 A-B3N1E13 563 A-B3U1C13
01A-B4N6E04 01A-B4U6C04
510 A-B3U1D11 564 A-B3A5D09

LOC. TYPE
A-B3T4 6824
A-B4C2 AB93

CCL PRIORITY CONTROL
I-O PRIORITY SELECT
E.C.-HISTORY-D MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD CP001
DATE LAST EC
10-06-76 315053
P.N. 1750173 030

- CD TIME — CC001EJ2 — 2
- I1 XA TIME — CC003BL6 — 5
- T0 TIME — CC006AF8 — 8
+ NO I TIME — CC008AA2 — 11
- LAST I TIME — CC008BC2 — 14
+ EXIT INST — CD001DN2 — 17
+ EXIT I1XC•T0 TIME — CP003AC2 — 20
- VIRGIN LEVEL — CP003BL6 — 23
+ PROG LEV 1 ENTERED — CP003GC2 — 26
+ PROG LEV 2 ENTERED — CP003GE2 — 29
+ PROG LEV 3 ENTERED — CP003GG2 — 32
+ PROG LEV 4 ENTERED — CP003GJ2 — 35
+ BID PROGRAM LEVEL 1 ALL — CP005BB6 — 38
+ BID PROGRAM LEVEL 2 ALL — CP005BD6 — 41
+ BID PROGRAM LEVEL 4 ALL — CP005BM6 — 44
+ PREVENT INTERRUPT — CP005CE2 — 47
+ BID PROGRAM LEVEL 3 ALL — CP005CH6 — 50
- SET OUTPUT 7E — CQ005FD6 — 53
- SET OUTPUT 7F — CQ005FE6 — 56
- TEST MODE LCH — CU005CB2 — 59
+ RESET — CU01OFM2 — 62
- Z BUS BIT 1•1 — DK974EH6 — 65
- Z BUS BIT 1•2 — DL004GB6 — 68
- Z BUS BIT 1•3 — DL004GF6 — 71
- Z BUS BIT 1•4 — DL004GK6 — 74
- Z BUS BIT 1•5 — DF004GB6 — 77

EDGE CONN•
513 A-B3V1A13
01A-B4V6A04

11 ZG02 AR CR AE 13E 102
14 ZB11 A
17 ZB09 A-B3M2

56 ZU03 A S AE FL 39F 109
68 ZP12 A
62 -U13 AR R
53 ZU02 A R
68 ZP12 A-B3M2

56 ZU03 A S AC FL 37C 130
71 ZM09 A
62 -U13 AR R
53 ZU02 A R
71 ZM09 A-B3M2

56 ZU03 A S AD FL 34F 144
74 ZM07 A
62 -U13 AR R
53 ZU02 A R
74 ZM07 A-B3M2

GATE IRPT ADDR TO INDATA
5 ZB04 A CL P05 160 120
23 ZB05 A-B3M2

EXIT INST
17 ZB09 N DA 11E 180
A-B3M2

LEV 2 BID SMFL
41 -48W AR PH 49W 203
20 -01H AR C
2 ZG04 A C
8 ZD09
47 -08E
102 -09E
A-B3M2

LEV 1 BID SMFL
38 -49V AR PH 49U 217
20 -01H AR C
2 ZG04 A C
8 ZD09
47 -08E
102 -09E
A-B3M2

LEV 3 BID SMFL
50 -48T AR PH 47T 230
20 -01H AR C
2 ZG04 A C
8 ZD09
47 -08E
102 -09E
A-B3M2

LEV 4 BID SMFL
44 -46V AR PH 46W 243
20 -01H AR C
2 ZG04 A C
8 ZD09
47 -08E
102 -09E
A-B3M2

MASK APPT BID
53 ZU02 A CR 36F 256
65 ZU04
59 -J07 A
379 -36L
A-B3M2

MASK OFF PROG LEV 5
62 -L13 AR S FL 31L 266
R
53 ZU02 A S
77 ZF06
56 ZU03 A R
77 ZF06
A-B3M2

29 31P AR CR BE 32N 302
109 36R A
203 36P A-B3M2

26 38R CR CG 04K 309
217 39R
405 03K
505 04L
A-B3M2

32 31R AR CR BF 31Q 323
130 36R A
230 37R A-B3M2

35 34N AR CR BG 34P 337
144 38R A
243 39R A-B3M2

PROG LEV 1 NEXT
26 38F CR BD 04 374
217 39F A-B3M2

56 ZU03 A CR AA 41F 379
65 ZU04 A
62 -U13 A
256 -41E A-B3M2

PROG LEV 2 NEXT
26 38R A BP 11 404
217 39R 13Q 405
302 14R A-B3M2

PROG LEV 4 NEXT
337 14W A BM 13 412
309 13W 13V 413
A-B3M2

IRPT ADDR TO INDATA BIT 1•3
374 ZD04 A DR CD M02 474
160 ZF05 U4
A-B3M2

PROG LEVEL 3 NEXT
26 38R A BJ 05 504
217 39R 33Q 505
323 34R
405 03K
A-B3M2

PROG LEV 5 NEXT
62 ZU13 A CH BD 12 513
413 -59L
309 -54K
A-B3M2

IRPT ADDR TO INDATA BIT 1•0
160 ZF05 AR A CF J12 553
405 -11T CR U4
413 -11U A-B3M2

IRPT ADDR TO INDATA BIT 0•7
160 ZF05 AR A CE J09 567
505 -12W CR U4
413 -11W A-B3M2

266 - MASK OFF PROG LEV 5 — CC008 AK2
256 + MASK ADAPTER LEV 1 BID — CP005 BB2
374 - PROG LEV 1 NEXT — EA2
CL005 CP003 CP004
404 - PROG LEV 2 NEXT — FE6
CP003 CX001
504 - PROG LEVEL 3 NEXT — FG6
CL004 CP003
412 - PROG LEV 4 NEXT — FL6
CL004 CP003
160 - GATE IRPT ADDR TO INDATA — FN6
CS004
474 + IRPT ADDR TO INDATA BIT 1•3 — GA2
CU012
567 + IRPT ADDR TO INDATA BIT 0•7 — GB2
CU011
553 + IRPT ADDR TO INDATA BIT 1•0 — GF2
CU012
513 - PROG LEV 5 NEXT — GK6
CC008 CL004 CV011
180 - EXIT INST — CP003 HN2

LOC• TYPE
A-B3M2 6818

PRIORITY CONTROL
PROG LEVEL SELECT
E•C•-HISTORY — B-MACH•27RNB
309521C
FRAME 01
IBM CORP•SDD — CP002
DATE LAST EC
04-19-72 309545
P•N• 5997618 — 000

CP002
000

- CD TIME — CC001EJ2 — 2-1
- I1 B TIME — CC002BC6 — 8-4
- I1 D TIME — CC002BE6 — 14-4
- I1 XC TIME — CC003BH6 — 20-11-3
- TO TIME — CC006AF8 — 26-11-3
- LAST I TIME — CC008BC2 — 32-1
+ EXIT INST — CD001DN2 — 38-1
- PRUG LEV 1 NEXT — CP002EA2 — 44-11
- PROG LEV 2 NEXT — CP002FE6 — 50-11
- PRUG LEVEL 3 NEXT — CP002FG6 — 56-11
- PROG LEV 4 NEXT — CP002FL6 — 62-11
- EXIT INST — CP002HN2 — 68-11-43
- VIRGIN LEVEL CD — CU003GL6 — 74-1
+ RESET — CU010FM2 — 80-1-3

EDGE CONN.
416 A-B3D1E13
01A-B4D6E04

AA
2 ZG04Δ A ΔO6E 102-1-3
32 ZB11Δ
38 ZB09Δ
74 ZD07Δ
└A-B3M2

EXIT I1XC.TO TIME
AB
20 ZD06Δ A |04P 139
26 ZD09Δ ΔO3R 140
68 -04QΔ
└A-B3M2

LEV 1 ENTERED LATCH
BA
A | FL 07H 204-1
102 -09KΔ | S S
416 ZS03Δ | ΔB10 206
80 -U13Δ|AR|R
20 ZD06Δ|A |R
26 ZD09Δ
68 -04QΔ
416 ZS03Δ
└A-B3M2

VIRGIN LEVEL
AC
44 ZD04ΔA |OR ΔM11 303-4
204 -39TΔ
50 -P11ΔA
519 -37WΔ
56 -J05ΔA
534 -37TΔ
62 -M13ΔA
504 -39VΔ
└A-B3M2

LEV 4 CURRENT
AQ
AR | PH 16W 403-1
62 -M13Δ | CD ΔP07 404-2
14 ZD10ΔA |C
68 -07LΔ
8 -B07ΔA |C
303 -B05Δ
└A-B3M2

LEV 1 CURRENT
AD
AR | PH 19T 415-1
44 -D04Δ | CD ΔS03 416-2
14 ZD10ΔA |C
68 -07LΔ
8 -B07ΔA |C
303 -B05Δ
└A-B3M2

LEV 2 CURRENT
AE
AR | PH 19W 427-1
50 -P11Δ | CD ΔM05 428-2
14 ZD10ΔA |C
68 -07LΔ
8 -B07ΔA |C
303 -B05Δ
└A-B3M2

LEV 3 CURRENT
AF
AR | PH 17T 439-1
56 -J05Δ | CD ΔU06 440-2
14 ZD10ΔA |C
68 -07LΔ
8 -B07ΔA |C
303 -B05Δ
└A-B3M2

LEV 4 ENTERED LATCH
BD
A | FL 17N 504-1
102 -19QΔ | S S
404 ZP07Δ | Δ17H 506-2
80 -U13|AR|R
20 ZD06ΔA |R
26 ZD09Δ
68 -04QΔ
404 ZP07Δ
└A-B3M2

LEV 2 ENTERED LATCH
BB
A | FL 09L 519-1
102 -09KΔ | S S
428 ZM05Δ | Δ11 521-2
80 -U13|AR|R
20 ZD06ΔA |R
26 ZD09Δ
68 -04QΔ
428 ZM05Δ
└A-B3M2

LEV 3 ENTERED LATCH
BC
A | FL 19R 534-1
102 -19QΔ | S S
440 ZU06Δ | Δ19L 536-2
80 -U13|AR|R
20 ZD06ΔA |R
26 ZD09Δ
68 -04QΔ
440 ZU06Δ
└A-B3M2

LEV 5 CURRENT
AH
415 19EΔ A ΔS12 548-1
427 19FΔ
439 18FΔ
403 18EΔ
└A-B3M2

139 + EXIT I1XC.TO TIME — CP002-AC2

140 - EXIT I1XC.TO TIME — CU015-AC6

303 - VIRGIN LEVEL — BL6
◇CL005 └CP002 └CP004 └CU003 └CX001

415 + PROG LEV 1 CURRENT — CU014-DD2

416 - PROG LEV 1 CURRENT — DD6
◇CF002 └CP004 └CS004 └CX001 └CZ004 └CZ005

428 - PROG LEV 2 CURRENT — DF6
└CP004 └CX001 └CZ004 └CZ005

439 + PROG LEV 3 CURRENT — CP005-DH2

440 - PROG LEV 3 CURRENT — DH6
◇CP004 └CP006 └CP007 └CZ004 └CZ005

403 + PROG LEV 4 CURRENT — CP005-DK2

404 - PROG LEV 4 CURRENT — DK6
└CP004 └CP005 └CZ004 └CZ005

548 - PROG LEV 5 CURRENT — EM6
◇CD003 └CP004 └CU015 └CZ004 └CZ005

206 - PROG LEV 1 ENTERED — FC2
└CK007 └CP004

521 - PROG LEV 2 ENTERED — FE2
└CP004 └CX001

536 - PROG LEV 3 ENTERED — FG2
└CP004 └CP007

506 - PROG LEV 4 ENTERED — CP004-FJ2

204 + PROG LEV 1 ENTERED — GC2
└CP002 └CS001

519 + PROG LEV 2 ENTERED — CP002-GE2

534 + PROG LEV 3 ENTERED — CP002-GG2

504 + PROG LEV 4 ENTERED — CP002-GJ2

LOC. TYPE
A-B3M2 6818

PRIORITY CONTROL PROG LEVEL SELECT		
E.C.-HISTORY	B	MACH.3705IGAR
309521C		FRAME 01
309538		
309545		IBM CORP.SDD CP003
DATE LAST EC		P.N. 5997619 000
02-05-73 310268		

CP003
000

- I1 XA TIME ——— CC003BL6 — 2
- T0+T1 TIME ——— CC007HK1 — 8
- PROG LEV 1 NEXT ——— CP002EA2 — 14
- VIRGIN LEVEL ——— CP003BL6 — 20
- PROG LEV 1 CURRENT ——— CP003DD6 — 26
- PROG LEV 2 CURRENT ——— CP003DF6 — 32
- PROG LEV 3 CURRENT ——— CP003DH6 — 38
- PROG LEV 4 CURRENT ——— CP003DK6 — 44
- PROG LEV 5 CURRENT ——— CP003EM6 — 50
- PROG LEV 1 ENTERED ——— CP003FC2 — 56
- PROG LEV 2 ENTERED ——— CP003FE2 — 62
- PROG LEV 3 ENTERED ——— CP003FG2 — 68
- PROG LEV 4 ENTERED ——— CP003FJ2 — 74
- GATE INPUT 79 ——— CQ005DK6 — 80
- GATE STATUS TO DISPLAY B ——— CU001EJ6 — 86

THIS PAGE IS FOR 3705-II ONLY.

LEV 5 LAST
50 ZS12 AR BH PH 32W 103
2 ZB04 A C
8 ZD05
14 ZD04
20 ZB05
A-B3M2

GATE INPUT 79 PROG L1 CURRNT
26 ZS02 A CA G13 115
80 ZS02
A-B3M2

LEV 4 LAST
44 ZP07 AR BF PH 31T 121
2 ZB04 A C
8 ZD05
14 ZD04
20 ZB05
A-B3M2

LEV 3 LAST
38 ZU06 AR BD PH 33V 132
2 ZB04 A C
8 ZD05
14 ZD04
20 ZB05
A-B3M2

LEV 2 LAST
32 ZM05 AR BB PH 34U 143
2 ZB04 A C
8 ZD05
14 ZD04
20 ZB05
A-B3M2

GATE DISPLAY
56 ZB10 A OR EA P04 154
86 ZM03 U+
A-B3M2

PROG LEV 4 EN TRD TO DSPLY B
74 -27C A OR EG M08 161
86 ZM03 U+
A-B3M2

PROG LEV 3 EN TRD TO DSPLY B
68 -28P A OR EE J04 168
86 ZM03 U+
A-B3M2

PROG LEV 2 EN TRD TO DSPLY B
62 ZD11 A OR EC G11 175
86 ZM03 U+
A-B3M2

LOC. TYPE
A-B3M2 6818

PRG STAT TO INDATA BIT 1.3
103 -27T A OR DH M02 204
115 ZG13 U+
A-B3M2

PRG STAT TO INDATA BIT 1.2
121 -28U A OR DF J13 218
115 ZG13 U+
A-B3M2

PRG STAT TO INDATA BIT 1.1
132 -26V A OR DD G12 225
115 ZG13 U+
A-B3M2

GATE INPUT
143 -29U A OR DB J12 231
115 ZG13 U+
A-B3M2

115 — GATE INPUT 79 PROG L1 CURRNT — CA6
CR004

231 + PRG STAT TO INDATA BIT 1.0 — DB2
CU012

225 + PRG STAT TO INDATA BIT 1.1 — DD2
CU012

218 + PRG STAT TO INDATA BIT 1.2 — DF2
CU012

204 + PRG STAT TO INDATA BIT 1.3 — DH2
CU012

154 + PROG LEV 1 ENTRD TO DSPLY B — EA2
AP015

175 + PROG LEV 2 ENTRD TO DSPLY B — EC2
AP015

168 + PROG LEV 3 ENTRD TO DSPLY B — EE2
AP015

161 + PROG LEV 4 ENTRD TO DSPLY B — EG2
AP015

PRIORITY CONTROL
PROG LEV LAST DETERMINATION
—E.C.—HISTORY—D. MACH.27RNB
312922
314419

FRAME 01

IBM CORP. SDD | CP004

DATE LAST EC | P.N. 1750174 | 030
10-06-76 315053

CP004
030 SIM TO PN 5997620 EC 310268

+ BID PROGRAM LEV 1 —— AA002DF1* 2
+ BID PROGRAM LEV 2 —— AA002DF3 5
+ BID PROGRAM LEV 3 —— AA002DF5* 8
+ MASK ADAPTER LEV 1 BID —— CP002BB2 11
+ PROG LEV 3 CURRENT —— CP003DH2 14
+ PROG LEV 4 CURRENT —— CP003DK2 17
- PROG LEV 4 CURRENT —— CP003DK6 20
+ BID PROGRAM LEV 4 —— CP005001* 23
- GATE STATUS TO DISPLAY B —— CU001EJ6 26
- ACTIV INSN STEP OR CLK STEP —— CU006CK2 29
+ IPL BID LEV 1 —— CU010FF2 32
- DIAGNOSTIC BID PROG LEV 2 —— CU014BL6 35
+ I-O CHECK BID PROG LEV 1 —— CU014CB2 38
+ ADDR COMPARE BID PROG LEV 1 —— CU014CE2 41
+ ADDR EXCEPTION BID PROG LEV1 —— CU014CH2 44
+ DIAG PCI BID PROG LEV 2 —— CU014CL2 47
+ INVALID OP BID PROG LEV 1 —— CU014GB2 50
+ INTERVAL TIMER BID PROG LEV3 —— CU014GH2 53
+ INTERRUPT KEY BID PROG LEV 3 —— CU014GL2 56
- PCI 3 —— CU015BB6 59
+ PCI BID PROG LEV 3 —— CU015CB2 62
+ PCI BID PROG LEV 4 —— CU015CE2 65
+ SVC BID PROG LEV 4 —— CU015CH2 68
+ PROT CHECK BID PROG LEV 1 —— CV061GE2 71
+ CSB SUPPORT BID PROG LEV 2 —— CX003EL6 74

62, 56 — OR (AD) DOT — 102

41, 44, 32, 38, 71, 50 — OR (AA) DOT — 109

14 13T, 17 14T — OR (AC) Δ14U — 119
A-B3M2

ADAPTER CHECK TO IND
2 ZD05, 26 ZU07 — A OR (BD) U11 — 126
A-B3N2

5, 74 — OR (AB) DOT — 131

65, 68 — OR (AE) DOT — 137

BID LEVEL 3 NOT INT TIMER
8 ZU09, 102 -54R — OR (AH) B03 — 204
A-B3M2

BID PROGRAM LEVEL 1 ALL
109 ZD03, 2 ZJ02, 11 -14N — AR OR (AF) 04N — 211
A-B3M2

BID PROGRAM LEVEL 2 ALL
47 -43Q, 131 ZS04 — OR (AG) 41R — 232
A-B3M2

BID PROGRAM LEVEL 4 ALL
23 ZU05, 137 -44R — OR (BA) 44Q — 239
A-B3M2

BID PROGRAM LEVEL 3 ALL
53 -02R, 204 ZB03 — OR (BC) 01P — 304
A-B3M2

PREVENT INTERRUPT
29 ZJ03, 211 -17G — A (BB) 16F — 311
20 ZP07, 59 -11R — OR
35 -09W, 119 -09V — OR
A-B3M2

211 + BID PROGRAM LEVEL 1 ALL —— BB6
CP002 CS001

232 + BID PROGRAM LEVEL 2 ALL CP002-BD6

234 + BID LEVEL 3 NOT INT TIMER —— BJ6
CP007

239 + BID PROGRAM LEVEL 4 ALL CP002-BM6

311 + PREVENT INTERRUPT —— CP002-CE2

304 + BID PROGRAM LEVEL 3 ALL CP002-CH6

126 + ADAPTER CHECK TO IND —— AP013-HE2

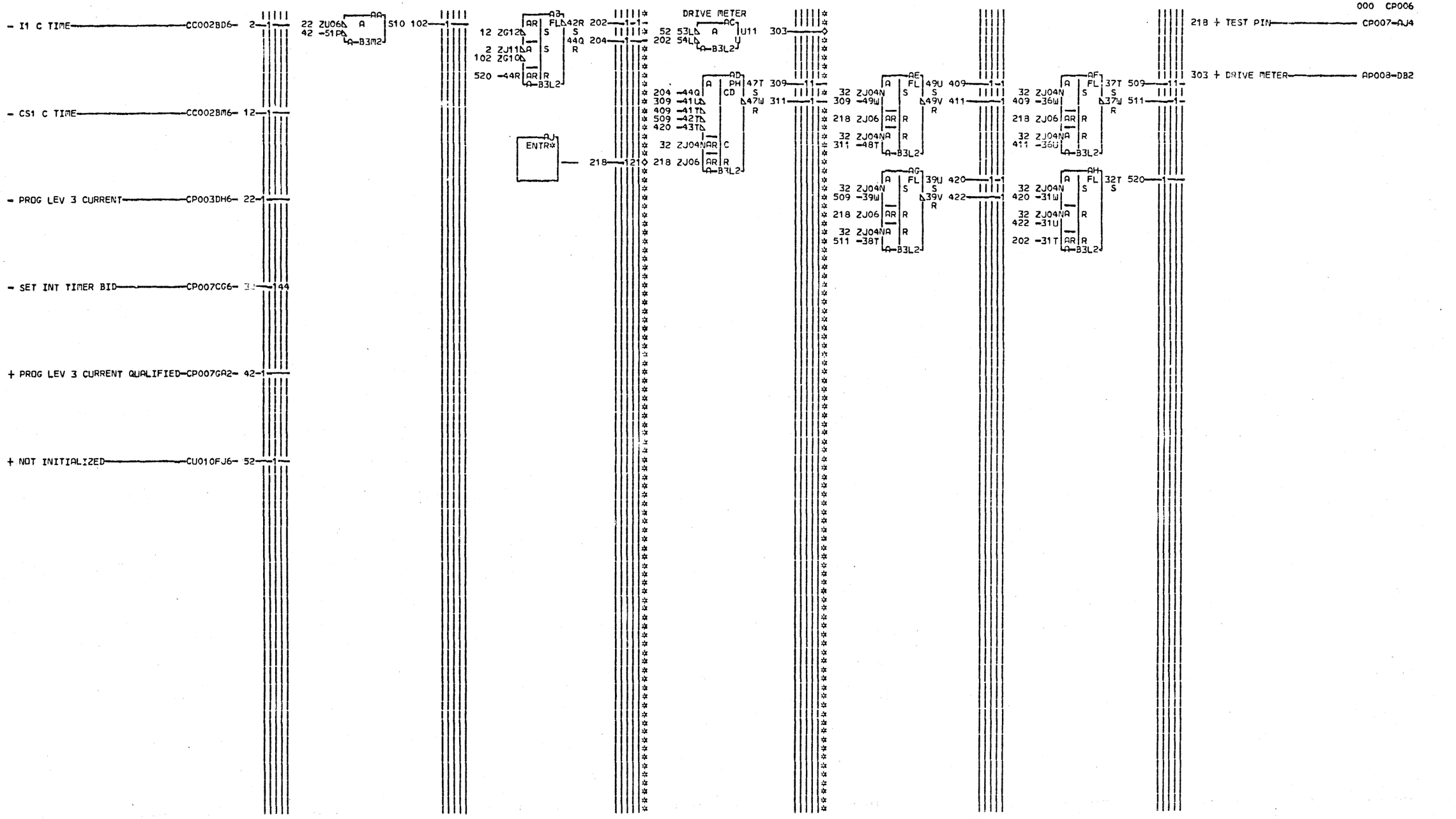
THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN. 01A-B3C1A11
2 RESISTOR 131 RESISTOR
A-B3M2J02 A-B3M2S04
8 RESISTOR 01A-B4V5D11
A-B3M2U09 01A-B4B5C02
23 RESISTOR 01A-B3B1C11
A-B3M2U05
109 RESISTOR
A-B3M2D03
01A-B4C6A02

LOC. TYPE
A-B3M2 6813
A-B3N2 6813

PRIORITY CONTROL
E.C. HISTORY — MACH.27RNB
312922
314419
FRAME 01
DATE LAST EC
10-06-76 313053
IBM CORP.SDD CP005
P.N. 1750175 030

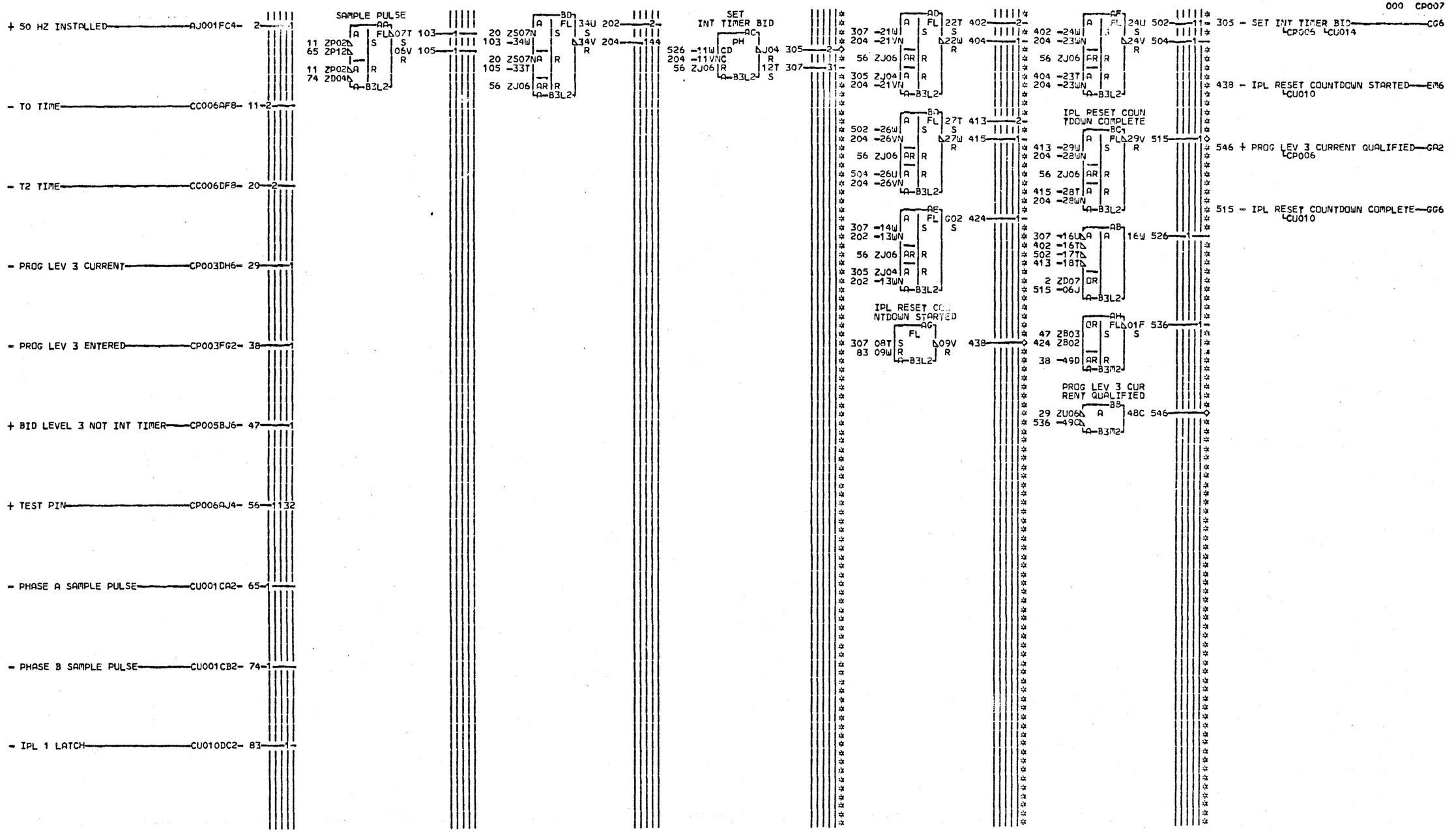
CP005
030 SIM TO PN 5997621 EC 314416



LOC. TYPE
A-B3L2 6823
A-B3M2 6818

METER AND INTERVAL TIMER		
E.C. HISTORY	MACH 27RNB	
309521C		
309538	FRAME 01	
309545		
DATE LAST EC	IBM CORP. SDD	CP006
06-28-72 309533	P.N. 5997622	000

CP006
000

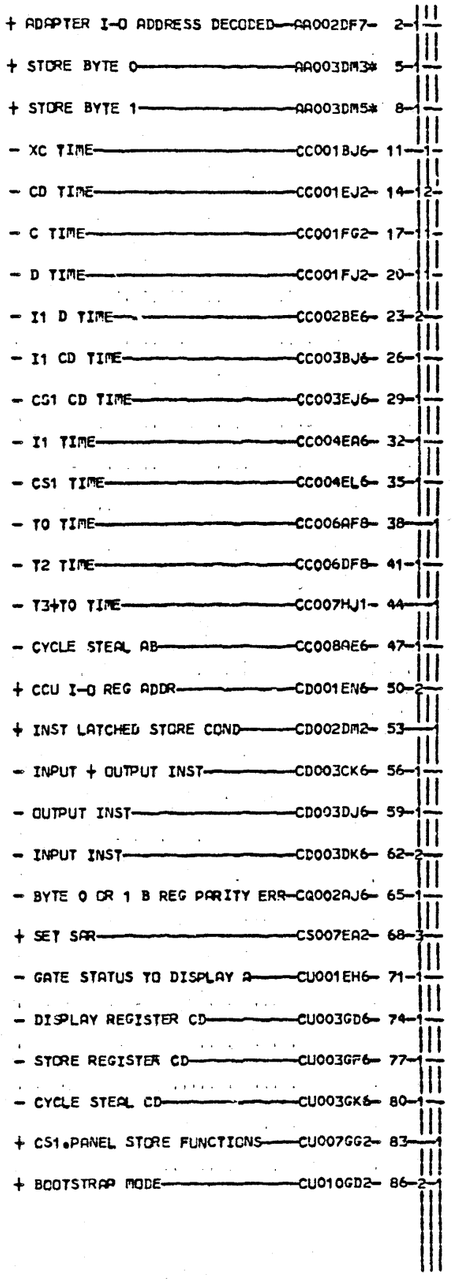


LOC. TYPE
A-B3L2 6823
A-B3M2 6818

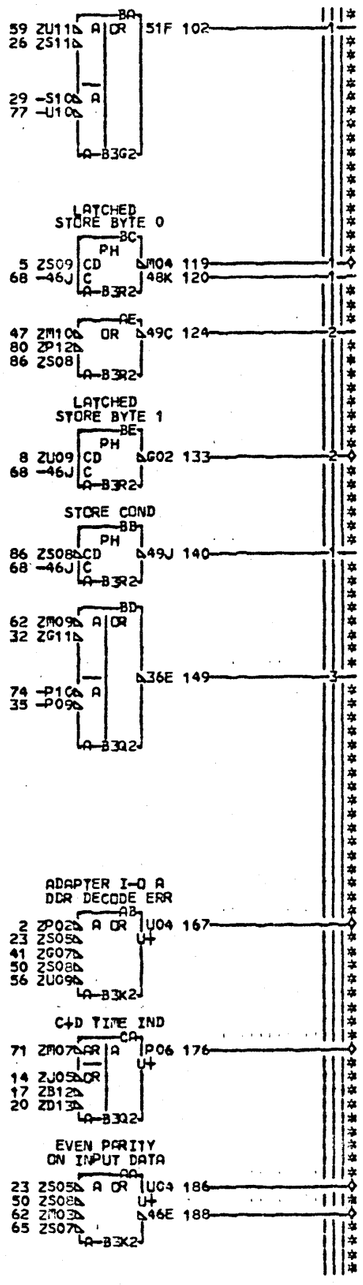
CP007
000

METER AND INTERVAL TIMER

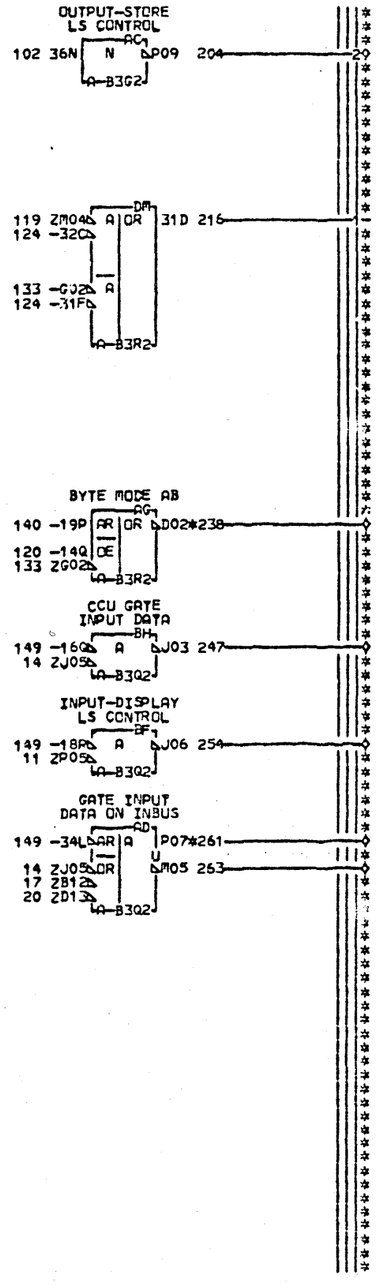
E.C. HISTORY	MACH.27RNB
309521C	
309538	FRAME 01
309545	
DATE LAST EC	IBM CORP.SDD CP007
06-28-72 309533	P.N. 5997623 000



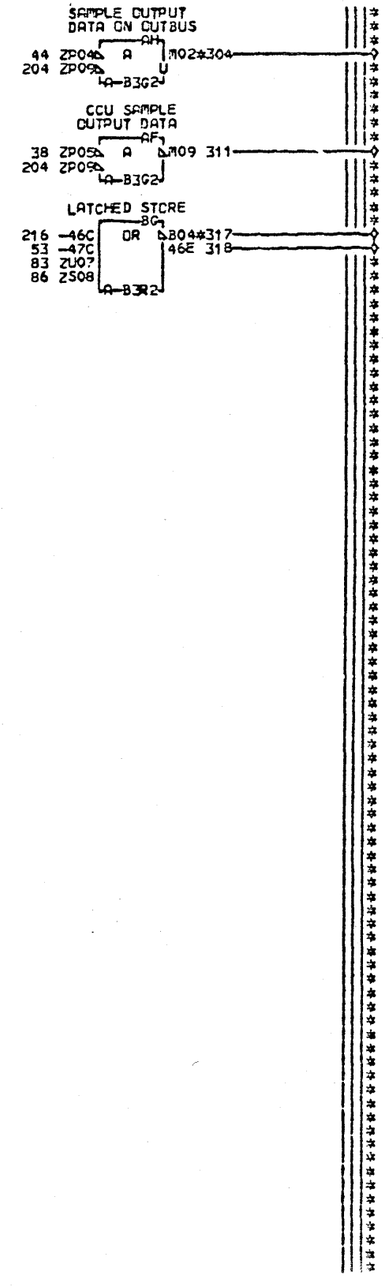
THIS PAGE IS FOR 3705-II ONLY.



EDGE CONN. 304 A-B3Q6C02
5 RESISTOR 01A-B4Q1C11
A-B3R2S09 01A-B4V5D02
8 RESISTOR 317 A-B3G1A13
A-B3R2U09 01A-B4G6A04
238 A-B3V3D07
01A-B4V3D07
261 A-B3A1D13
01A-B4A6D04
01A-B4V5D03



LOC. TYPE
A-B3G2 Y702
A-B3X2 6816
A-B3Q2 6821
A-B3R2 AB92



030 CQ001

186 + EVEN PARITY ON INPUT DATA — BF2
└CU014

188 - INT SCOPING POINT UNLOADED — BF6
└AU001

167 + ADAPTER I-O ADDR DECODE ERR — BG2
└CU014

204 - OUTPUT-STORE LS CONTROL — CA2
└CL004 └CQ005

261 + GATE INPUT DATA ON INBUS — CD2
└AA002

263 - GATE INPUT DATA ON INBUS — CD6
└CQ005

311 - CCU SAMPLE OUTPUT DATA — DA6
└CQ004 └CR001 └CX001

238 - BYTE MODE AB — CF002-DK2

304 + SAMPLE OUTPUT DATA ON OUTBUS — EA2
└AA002

119 - LATCHED STORE BYTE 0 — CS005-FL2

133 - LATCHED STORE BYTE 1 — CS005-FM2

254 - INPUT-DISPLAY LS CONTROL — GE6
└CL004

317 - LATCHED STORE — GL2
└CC007 └CM001 └CQ002 └CV061

318 + LATCHED STORE — CQ002-GL6

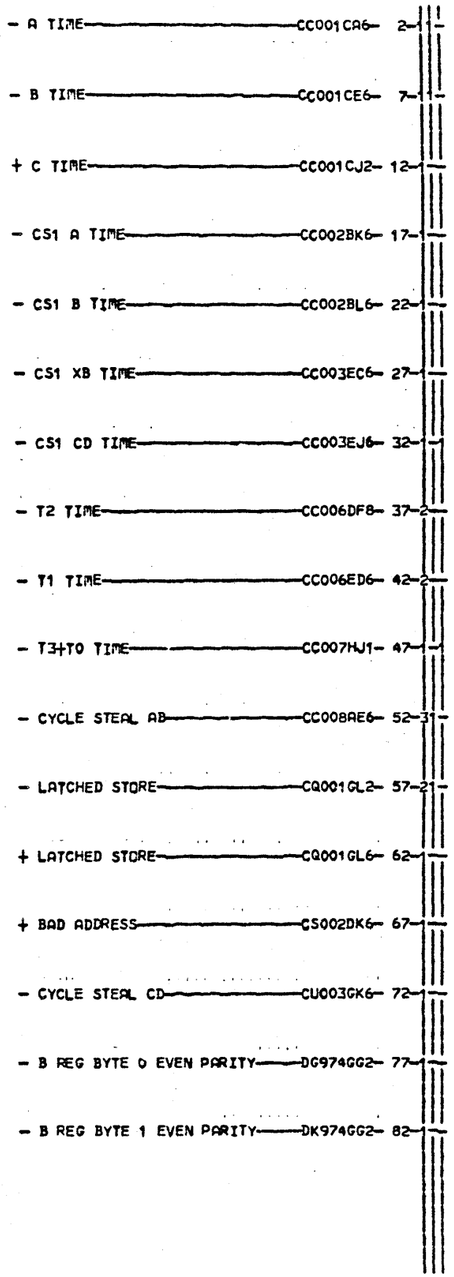
247 - CCU GATE INPUT DATA — HB6
└CQ004 └CQ005 └CS004 └CX001

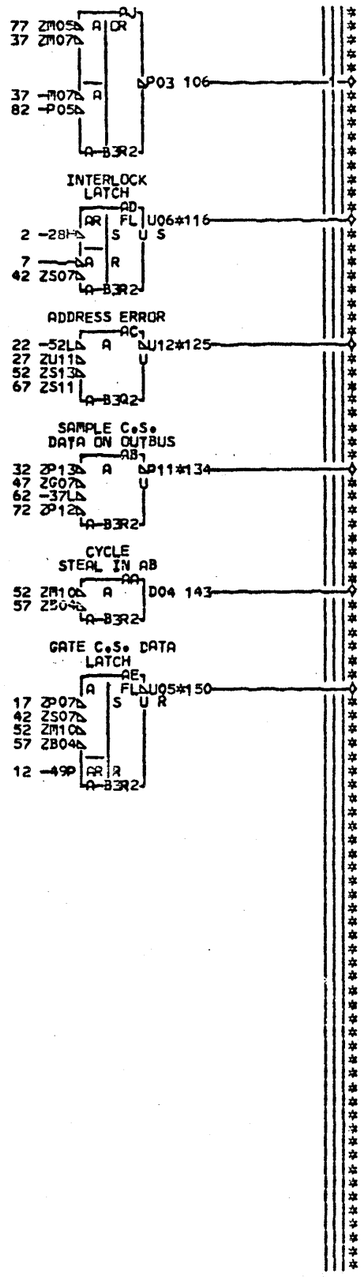
176 + C+D TIME IND — AP015-HQ2

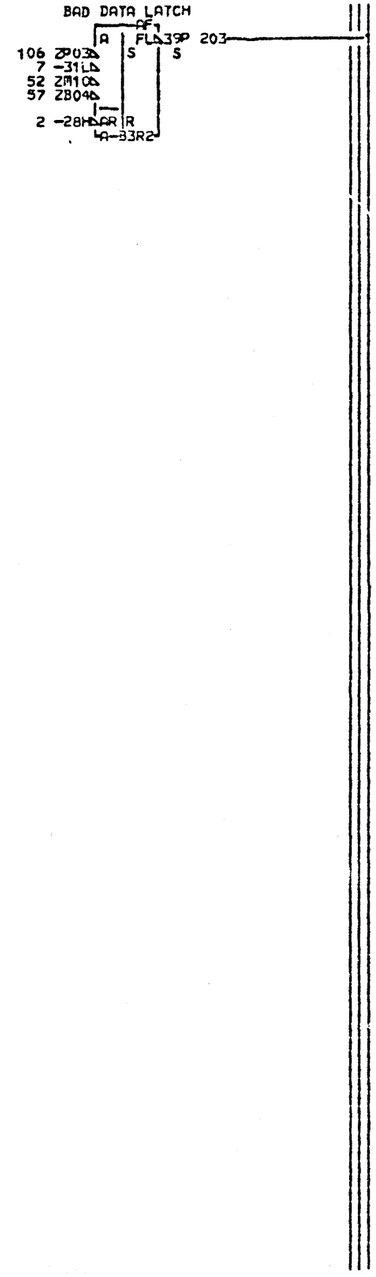
ADAPTER INTERFACE CONTROLS

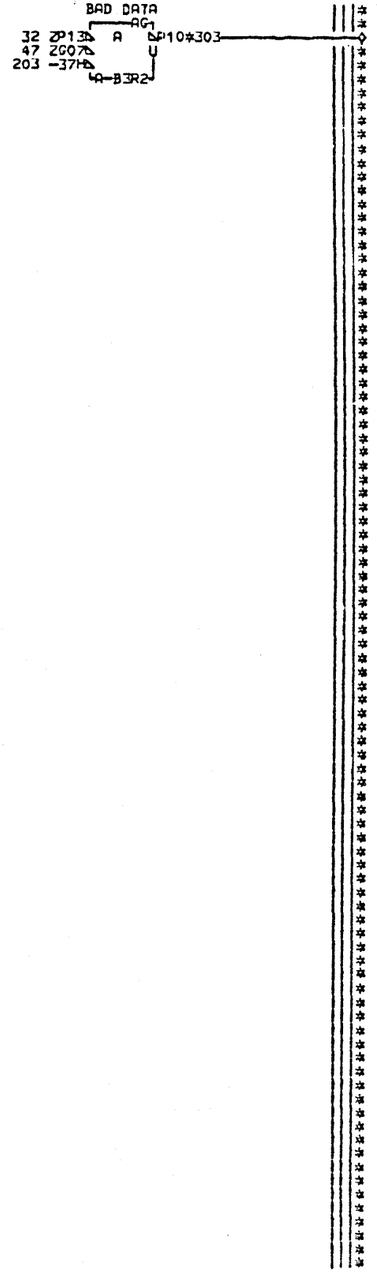
E.C.-HISTORY — MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD — CQ001
DATE LAST EC
10-06-76 315053
P.N. 1750176 — 030

CQ001
030 SIM TO PN 5997624 EC 310268









030 CQ002

143 + CYCLE STEAL IN AB — CS004-AF2

106 - BYTE 0 OR 1 B REG PARITY ERR — AJ6
└CQ001

134 - SAMPLE C.S. DATA ON OUTBUS — BC6
└AA003

125 - ADDRESS ERROR — AA003-BK6

116 + INTERLOCK — AA003-CA6

150 - GATE C.S. DATA ON INBUS AA003-DD6

303 - BAD DATA — AA003-DF6

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
116 A-B3A5B06
125 A-B3A5D02
134 A-B3A5D06
150 A-B3A5D05
303 A-B3A5B13

LOC. TYPE
A-B3Q2 6821
A-B3R2 AB92

ADAPTER INTERFACE CONTROLS

E.C.-HISTORY	MACH.27RNB
312922	
314419	FRAME 01
	IBM CORP.SDD
DATE LAST EC	P.N. 1750177
10-06-75 315053	030

CQ002

CQ002
030 SIM TO PN 5997625 EC 310268

- OP XXXX XXXX X111 XXXX——CD001B30— 2—1
- OP XXXX XXXX X000 XXXX——CD001BB3— 11—1
- OP XXXX XXXX X001 XXXX——CD001BB4— 20—1
- OP XXXX XXXX X010 XXXX——CD001BB5— 29—1
- OP XXXX XXXX X011 XXXX——CD001BB6— 38—1
- OP XXXX XXXX X100 XXXX——CD001BB7— 47—1
- OP X111 XXXX XXXX XXXX——CD001BH0— 56—6
- CCU SAMPLE OUTPUT DATA——CQ001DA6— 65—1
- CCU GATE INPUT DATA——CQ001HB6— 74—5
+ OP REG BIT 1.0——CQ005HG2— 83—6

THIS PAGE IS FOR 3705-II ONLY.

CQ004
030 SIM TO PN 5997627 EC 310268

SET OUTPUTS 70 TO 77 (AA)
56 ZM10 A G02 104
65 ZM09
83 -37 A-B3G2

GATE INPUT 71 (AC)
20 -33 A M05 117
56 ZM10
74 ZM11
83 -37 A-B3G2

GATE INPUT 70 (AG)
11 -52 A S12*125
56 ZM10
74 ZM11
83 -37 A-B3G2

GATE INPUT 74 (AH)
47 -34 A P06*133
56 ZM10 U
74 ZM11
83 -37 A-B3G2

GATE INPUT 73 (AE)
38 ZP10 A S13*141
56 ZM10
74 ZM11
83 -37 A-B3G2

GATE INPUT 72 (AD)
29 -31 A P07 149
56 ZM10
74 ZM11
83 -37 A-B3G2

EDGE CONN.
125 P-B3C1E11
01P-B4C5E02
133 P-B3H1A13
01P-B4H6A04
141 P-B3C1D13
01P-B4C6D04
203 P-B3N6A02
01P-B4N1A11
210 P-B3M6E04

01A-B4M1E13

SET OUTPUT 77 (AF)
2 -49 A S09*203
104 ZG02 A-B3G2

SET OUTPUT 73 (AB)
38 ZP10 A B04*210
104 ZG02 A-B3G2

LOC. TYPE
A-B3G2 Y702

104 - SET OUTPUTS 70 TO 77——BA6
└CS07 └CU006

210 + SET OUTPUT 73——DE2
└CV001 └CV061

117 - GATE INPUT 71——CS004—DK6

149 - GATE INPUT 72——DL6
└AP005 └CK003 └CU003

141 + GATE INPUT 73——CV051—DM2

203 - SET OUTPUT 77——FE6
◇CU006 └CU010 └CU014 └CU015
└CV061

125 + GATE INPUT 70——CM002—FH2

133 - GATE INPUT 74——FJ6
◇CS001 └DFC02 └DG002 └DH002
◇DJC02 └DK002 └DL002 └DM002
└DW001

PULSED INPUTS AND OUTPUTS
HEX 70-77
E.C.—HISTORY—D
312922
314419

MACH.27RNB
FRAME 01
IBM CORP.SDD
CQ004

DATE LAST EC
10-06-76 315053
P.N. 1750178
030

- TO TIME — CC006AA — 2—5

- OP XXXX XXXX X111 XXXX — CD001BB0 — 8—21

- OP XXXX XXXX X000 XXXX — CD001BB3 — 14—1

- OP XXXX XXXX X001 XXXX — CD001BB4 — 20—1

- OP XXXX XXXX X010 XXXX — CD001BB5 — 26—1

- OP XXXX XXXX X011 XXXX — CD001BB6 — 32—2

- OP XXXX XXXX X100 XXXX — CD001BB7 — 38—1

- OP XXXX XXXX X101 XXXX — CD001BB8 — 44—1

- OP XXXX XXXX X110 XXXX — CD001BB9 — 50—21

- OP X111 XXXX XXXX XXXX — CD001BH0 — 56—73

- OUTPUT-STORE LS CONTROL — CQ001CA2 — 62—1

- GATE INPUT DATA ON INBUS — CQ001CD6 — 68—2

- CCU GATE INPUT DATA — CQ001HB6 — 74—43

- OP REG BIT 1.0 — DQ004GB2 — 80—83

SET OUTPUTS 7B TO 7F (AA)
56 ZM10 A — M04 104
62 ZP09
80 ZP11
A—B3G2

OP REG BIT 1.0 (BH)
80 ZP11 N — 38H 138
A—B3G2

GATE INPUT 79 (AH)
20 -26P6 A — S05*145
56 ZM10
74 ZM11
80 ZP11
A—B3G2

GATE INPUT 7F (BG)
8 -07P A — G07 153
56 ZM10
74 ZM11
80 ZP11
A—B3G2

GATE INPUT 77 (AC)
8 -09P6 A — D09*161
56 ZM10 U
68 ZB10
80 ZP11
A—B3G2

GATE INPUT 7E (BF)
50 -06L6 A — J11*169
56 ZM10
74 ZM11
80 ZP11
A—B3G2

GATE INPUT 76 (AE)
50 -29P6 A — S04*177
56 ZM10 U
68 ZB10
80 ZP11
A—B3G2

GATE INPUT 7D (BE)
44 -07P6 A — J07 185
56 ZM10
74 ZM11
80 ZP11
A—B3G2

SET OUTPUT 7F (BC)
2 ZP05 A — D06 203
8 -03R6
104 ZM04
A—B3G2

SET OUTPUT 7E (BB)
2 ZP05 A — D02 210
50 -01Q6
104 ZM04
A—B3G2

(AF)
2 ZP05 A — 01J 216
32 ZP10
104 ZM04
A—B3G2

SET OUTPUT 79 (AE)
2 ZP05 A — U04*224
20 -03M6
104 ZM04
A—B3G2

SET OUTPUT 78 (AD)
2 ZP05 A — D13 231
14 -01M6
104 ZM04
A—B3G2

GATE INPUT 7C (BD)
38 -32T6 A — M03 238
56 ZM10
74 ZM11
80 ZP11
A—B3G2

GATE INPUT 7B (BA)
32 ZP10 A — G12 246
56 ZM10
74 ZM11
80 ZP11
A—B3G2

GATE INPUT 7A (AG)
26 -54L6 A — B03 254
56 ZM10
74 ZM11
80 ZP11
A—B3G2

030 CQ005

104 — SET OUTPUTS 7B TO 7F — CU015—BA6

177 — GATE INPUT 76 — AA002—CJ6

161 — GATE INPUT 77 — CL6
AA002 CX003

231 — SET OUTPUT 78 — CK001—DB6

224 — SET OUTPUT 79 — DC6
CM003 CR006 CU005 CU006
CU010 CU015 CZ004 CZ005

216 + SET OUTPUT 7B — DE2

254 + GATE INPUT 7A — CR004—DJ2

145 — GATE INPUT 79 — DK6
CG001 CP004 CZ004 CZ005

246 — GATE INPUT 7B — DM6
CR006 CR007

210 — SET OUTPUT 7E — CP002—FD6

203 — SET OUTPUT 7F — CP002—FE6

238 — GATE INPUT 7C — FJ6
CR006 CR007

185 — GATE INPUT 7D — FK6
CK004 CK005

169 — GATE INPUT 7E — FL6
CU010 CU014 CV061

153 — GATE INPUT 7F — FM6
CU014 CU015

138 + OP REG BIT 1.0 — CQ004—HG2

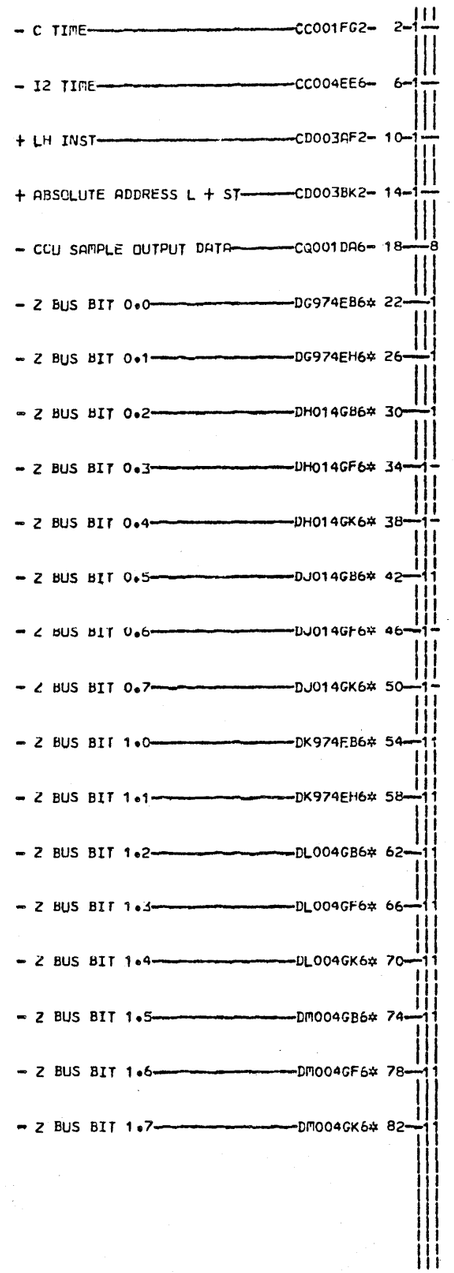
THIS PAGE IS FOR 3705-II ONLY.

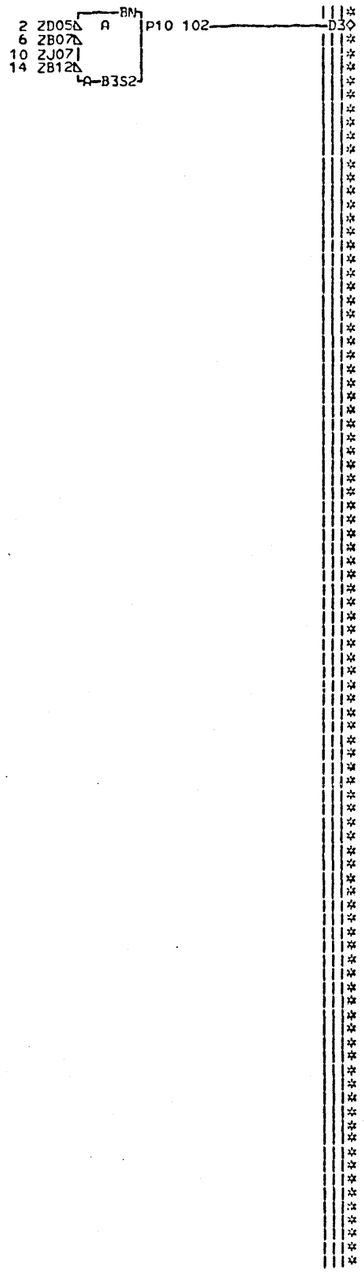
EDGE CONN.
145 A—B3E1D13
01A—B4E6D04
161 A—B3B1A13
01A—B4B6A04
01A—B4V5D06
169 A—B3C1E13
01A—B4G6E04
177 A—B3A1E11
01A—B4A6E02
224 A—B3F1A13
01A—B4F6A04

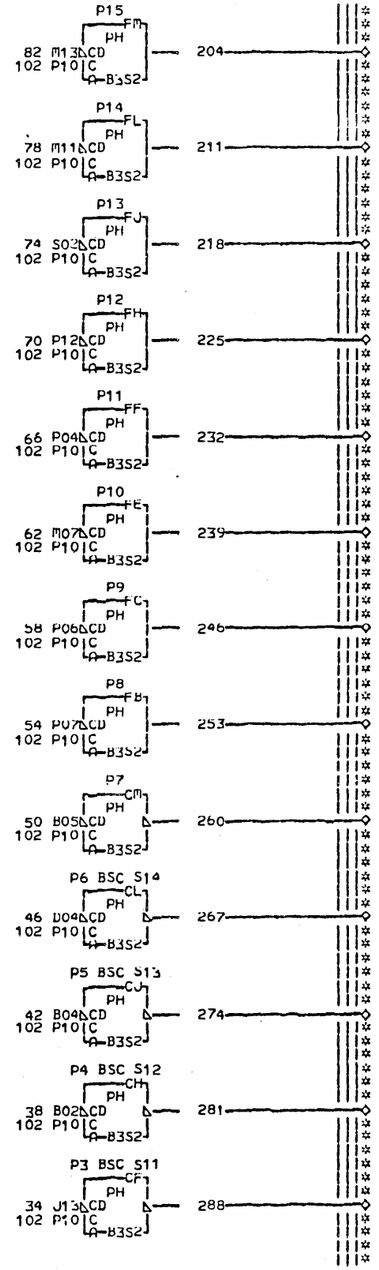
LOC. TYPE
A—B3G2 Y702

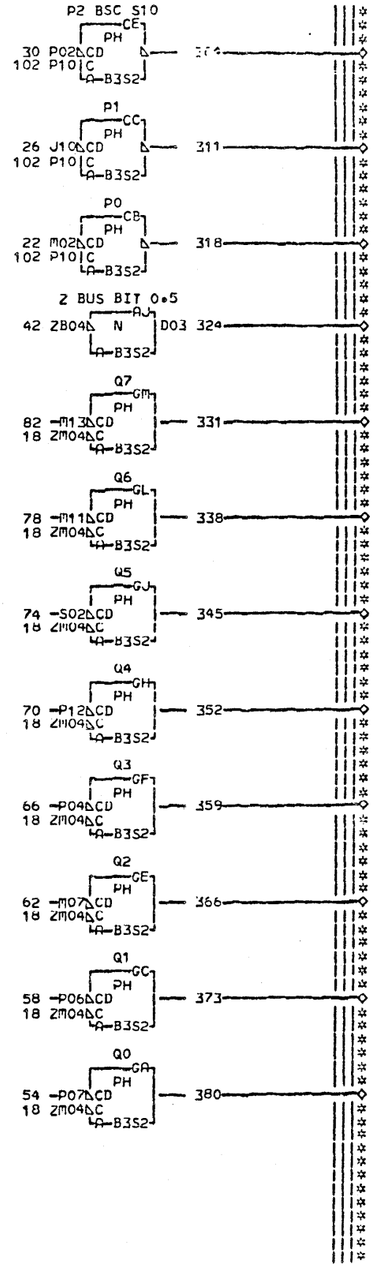
CQ005
030 SIM TO PN 5997628 EC 310268

PULSED INPUTS AND OUTPUTS
HEX 78-7F
E.C.—HISTORY—D
312922
314419
DATE LAST EC
10-06-75 315053
MACH.27RNB
FRAME 01
IBM CORP.SDD
P.N. 1750179
CQ005
030









000 CR001

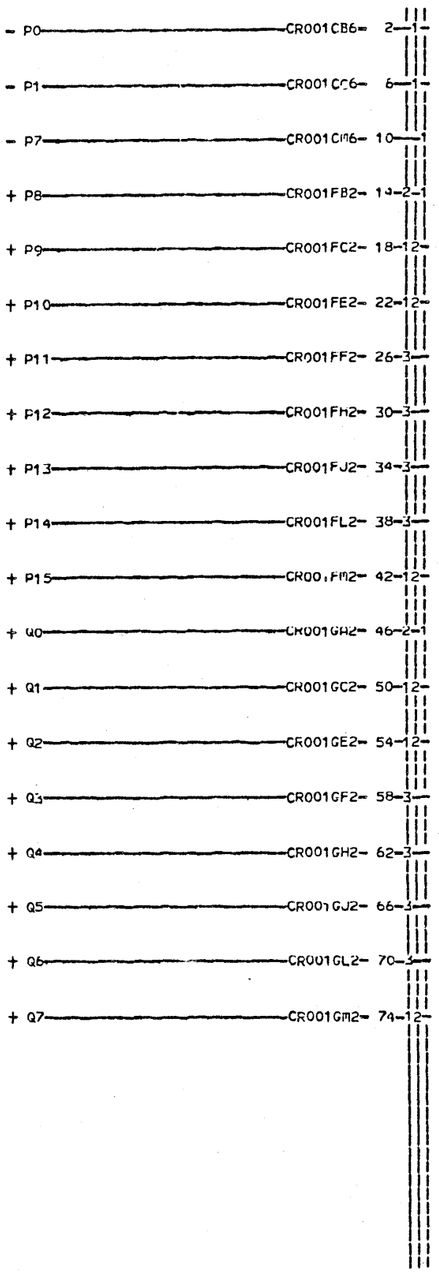
324 + Z BUS BIT 0.5 AJ2 — CZ004 CZ005
102 TEST PIN BN2
318 - P0 CB6 — CR002 CR005
311 - P1 CC6 — CR002 CR005
304 - P2 BSC S10 CE6 — CR005 CR007
288 - P3 BSC S11 CF6 — CR005 CR007
281 - P4 BSC S12 CH6 — CR005 CR007
274 - P5 BSC S13 CJ6 — CR005 CR007
267 - P6 BSC S14 CL6 — CR005 CR007
260 - P7 CM6 — CR002 CR005
253 + P8 CR002-FB2
246 + P9 CR002-FC2
239 + P10 CR002-FE2
232 + P11 CR002-FF2
225 + P12 CR002-FH2
218 + P13 CR002-FJ2
211 + P14 CR002-FL2
204 + P15 CR002-FM2
380 + Q0 CR002-GA2
373 + Q1 CR002-GC2
366 + Q2 CR002-GE2
359 + Q3 CR002-GF2
352 + Q4 CR002-GH2
345 + Q5 CR002-GJ2
338 + Q6 CR002-GL2
331 + Q7 CR002-GM2

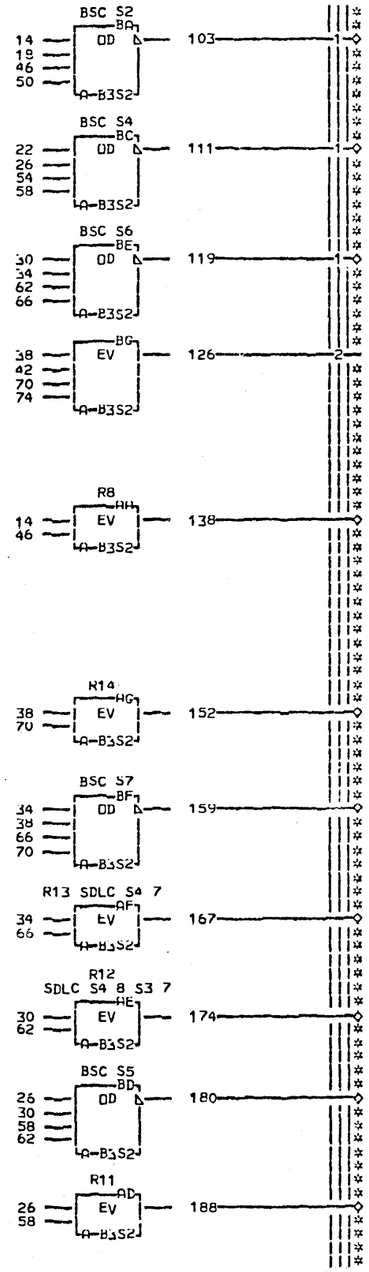
EDGE CONN.
22 RESISTOR A-B3S2M02
26 RESISTOR A-B3S2J10
30 RESISTOR A-B3S2M02
34 RESISTOR A-B3S2J13
38 RESISTOR
A-B3S2B02
42 RESISTOR A-B3S2B04
46 RESISTOR A-B3S2D04
50 RESISTOR A-B3S2B05
54 RESISTOR A-B3S2M07
58 RESISTOR
A-B3S2P06
62 RESISTOR A-B3S2M07
66 RESISTOR A-B3S2P04
70 RESISTOR A-B3S2P12
74 RESISTOR A-B3S2S02
78 RESISTOR
A-B3S2M11
82 RESISTOR A-B3S2M13

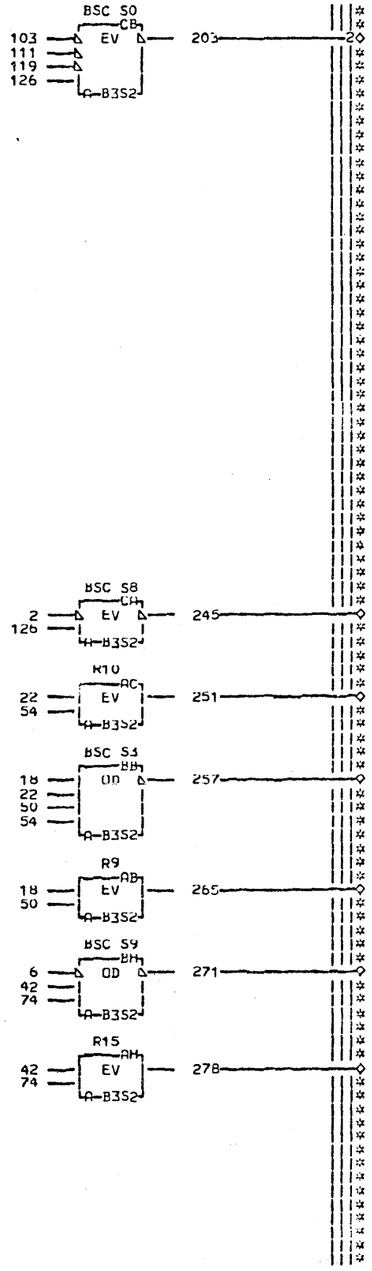
LOC. TYPE
A-B3S2 Y703

CC CONTROL
BSC AND DLC CRC
E.C. HISTORY — B MACH.3705IGAR
FRAME 01
IBM CORP. CR001
DATE LAST EC
02-19-73 310268
P.N. 1857257 000

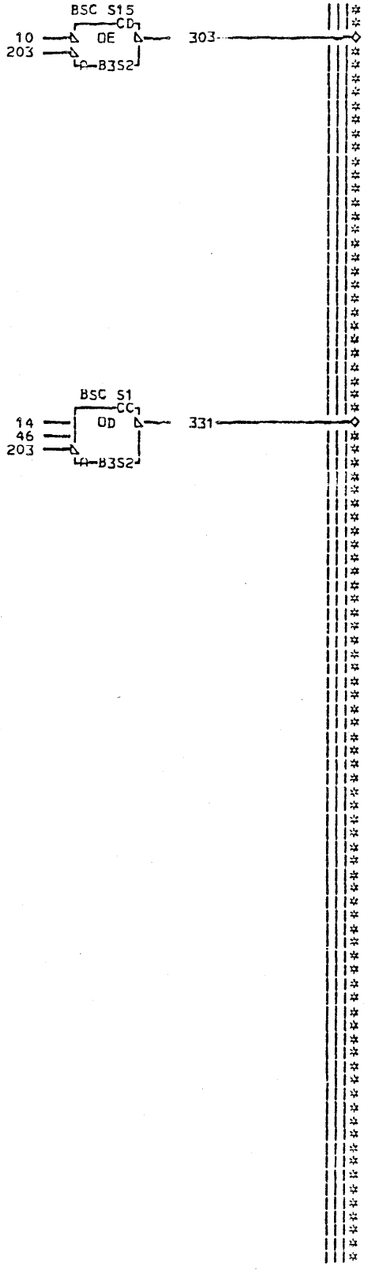
CR001
000







LOC. TYPE
A-B3S2 Y703



000 CR002

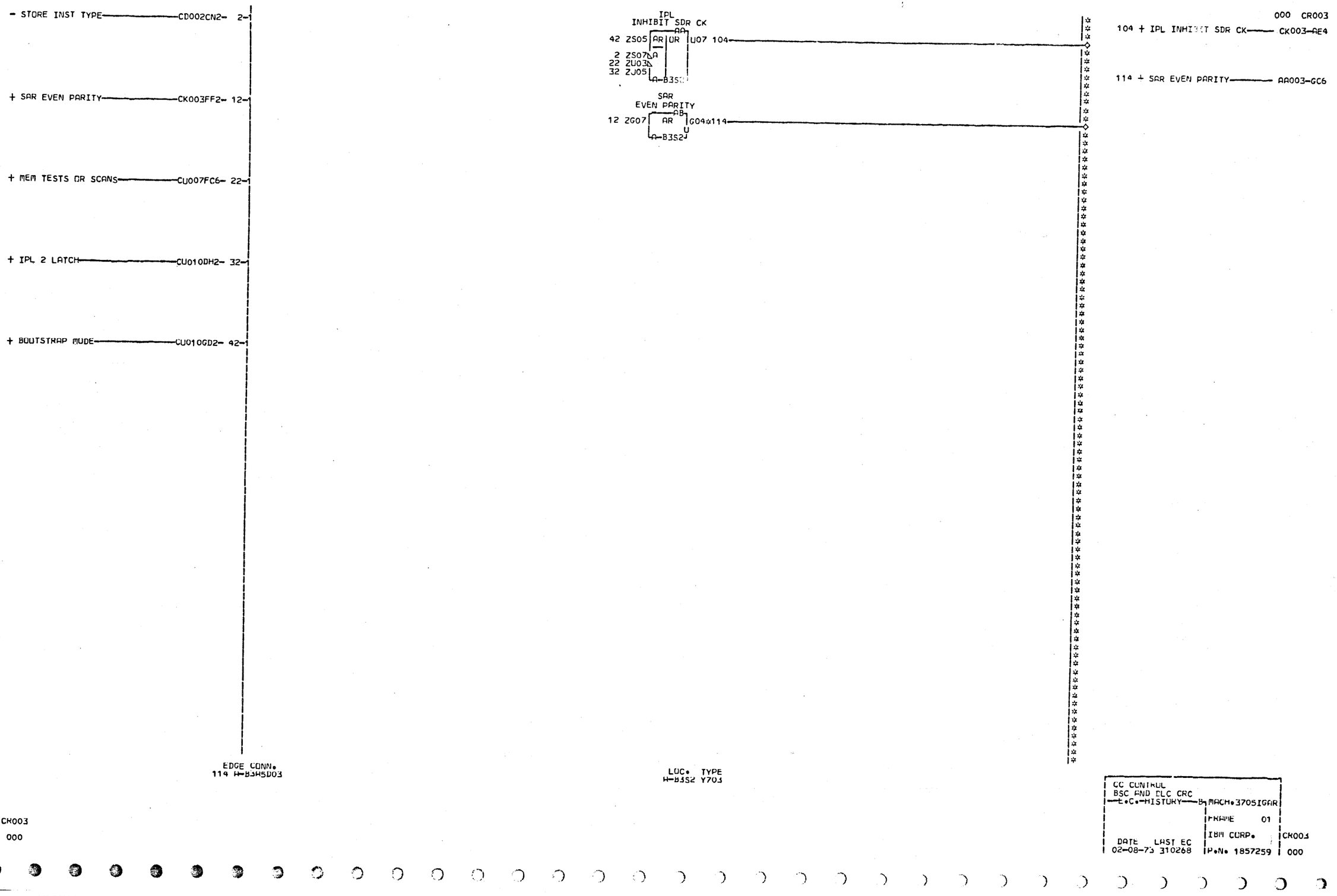
138 + R8 CR005-AB2
265 + R9 CR005-AC2
251 + R10 CR005-AE2
188 + R11 CR005-AF2
174 + R12 SDLC S4 8 S3 7 AG2
LCR005 LCR006
167 + R13 SDLC S4 7 AH2
LCR005 LCR006
152 + R14 CR005-AJ2
278 + R15 CR005-AK2
103 - BSC S2 CR006-BB2
257 - BSC S3 CR006-BD2
111 - BSC S4 CR006-BE2
180 - BSC S5 CR006-BF2
119 - BSC S6 CR006-BG2
159 - BSC S7 CR006-BH2
271 - BSC S9 CR007-BK2
245 - BSC S8 CR007-CJ2
203 - BSC S0 CR006-DB2
331 - BSC S1 CR006-EB2
303 - BSC S15 CR007-FN2

CC CONTROL
BSC AND SDLC CRC
E.C. HISTORY — by MACH.3705IGAR

DATE	LAST EC
02-05-73	J10268

FRAME 01
IBM CORP. CR002
P.N. 1857258 000

CR002
000


- STORE INST TYPE CD002CN2- 2-1
+ SAR EVEN PARITY CK003FF2- 12-1
+ MEM TESTS OR SCANS CU007FC6- 22-1
+ IPL 2 LATCH CU010DH2- 32-1
+ BOOTSTRAP MODE CU010GD2- 42-1
EDGE CONN.
114 H-B3H5D03
IPL
INHIBIT SDR CK
AA
42 ZS05 AR OR U07 104
2 ZS07 A
22 ZU03 A
32 ZJ05 A
A-B3S2
SAR
EVEN PARITY
AB
12 ZG07 AR G04 114
U
A-B3S2
LOC. TYPE
H-B3S2 Y703
000 CR003
104 + IPL INHIBIT SDR CK CK003-AE4
114 + SAR EVEN PARITY AA003-GC6
CC CONTROL
BSC AND CLC CRC
E.C. HISTORY
By MACH.3705IGAR
FRAME 01
IBM CORP.
DATE LAST EC
02-08-73 310268
P.N. 1857259
CR003
000
CR003
000

- FLOAT TYPE 1 CA INSTALLED——AJ001BH4— 2—|

+ ROS ESCAPE——————————AJ001EC4— 12—|

- GATE INPUT 79 PROG L1 CLRRNT—CP004CA6— 22—2

+ GATE INPUT 7A——————————CQ005DJ2— 32—|

GATE INPUT 7A
32 ZP13 DB N — 103
A—B3S2

TYPE 1 CA INSTALLED 3705
2 ZS11 DH A OR — 111
22 ZG05
A—B3S2

ROS ESC TO INDATA 1.7
12 ZG03 DL A OR — 118
22 ZG05
A—B3S2

000 CR004
103 - GATE INPUT 7A——————DB2
└CR006 └CR007

111 + TYPE 1 CA INSTALLED 3705——DH2
└CR007

118 + ROS ESC TO INDATA 1.7— CR007—DL2

LOC. TYPE
A—B3S2 Y705

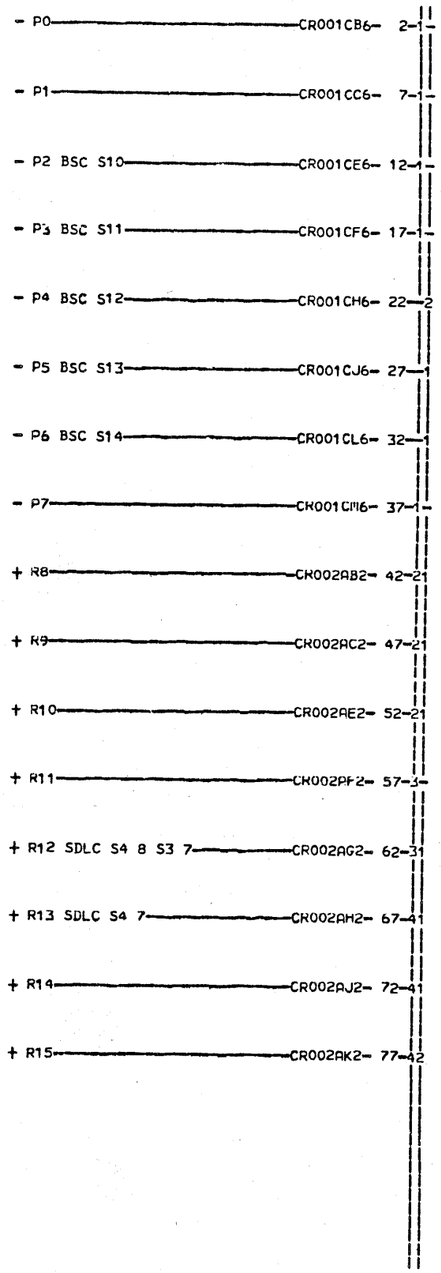
CR004
000

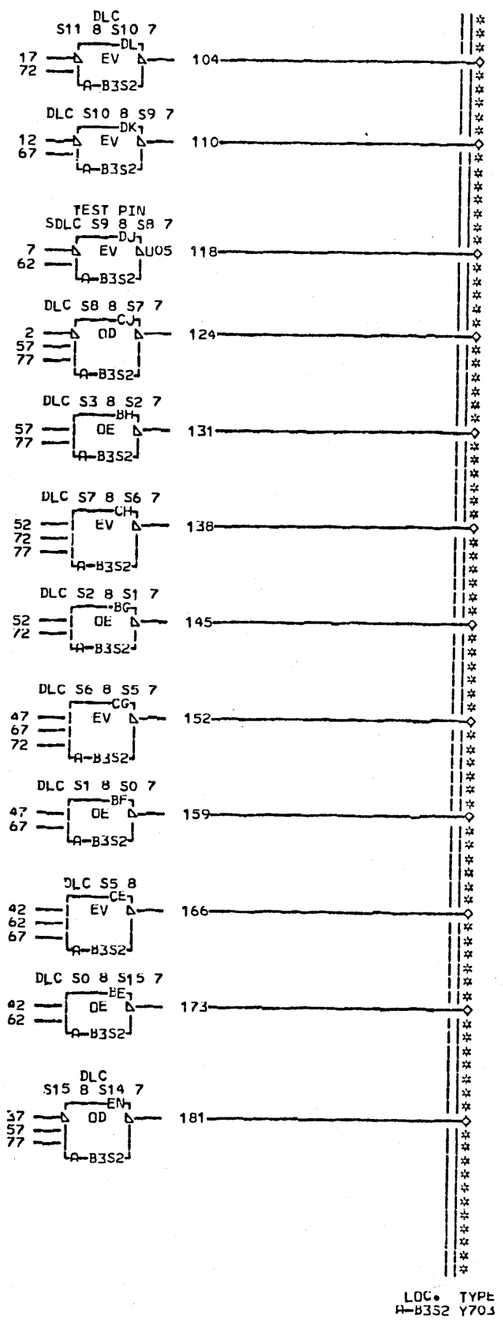
CE CONTROL
BSC AND DLC CRC
—E.C.—HISTORY——th MACH.3705IGAR

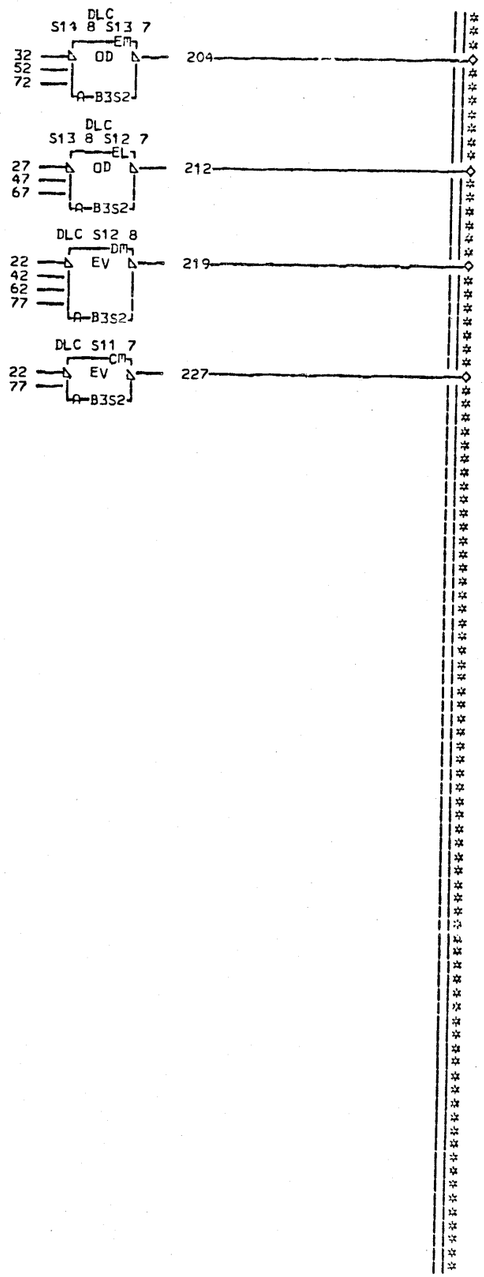
FRAME 01
IBM CURP.
P.N. 1857260

DATE LAST EC
02-19-73 310268

CR004
000







000 CR005

173 - DLC S0 8 S15 7 BE2
└CR006 └CR007

159 - DLC S1 8 S0 7 CR006-BF2

145 - DLC S2 8 S1 7 CR006-BG2

131 - DLC S3 8 S2 7 CR006-BH2

166 - DLC S5 8 CR006-CE2

152 - DLC S6 8 S5 7 CR006-CG2

138 - DLC S7 8 S6 7 CR006-CH2

124 - DLC S8 8 S7 7 CJ2
└CR006 └CR007

227 - DLC S11 7 CR007-CM2

118 - TEST PIN SDLC S9 8 S8 7 CR007-DJ2

110 - DLC S10 8 S9 7 CR007-DK2

109 - DLC S11 8 S10 7 CR007-DL2

219 - DLC S12 8 CR007-DM2

212 - DLC S13 8 S12 7 CR007-EL2

204 - DLC S14 8 S13 7 CR007-EM2

181 - DLC S15 8 S14 7 CR007-EN2

CC CONTROL
BSC AND DLC CRC
E.C. HISTORY By MACH.3705IGAR

DATE	LAST EC
02-08-73	310268

FRAME 01
IBM CORP.
P.N. 1857261
CR005
000

+ POWER ON RESET — PP008DB4 — 2-1
- SET OUTPUT 79 — CQ005DC6 — 5-1
- GATE INPUT 7B — CQ005DM6 — 8-62
- GATE INPUT 7C — CQ005FJ6 — 11-62
+ R12 SDLC S4 8 S3 7 — CR002AG2 — 14-2
+ R13 SDLC S4 7 — CR002AH2 — 17-1
- BSC S2 — CR002BB2 — 20-1
- BSC S3 — CR002BD2 — 23-1
- BSC S4 — CR002BE2 — 26-1
- BSC S5 — CR002BF2 — 29-1
- BSC S6 — CR002BG2 — 32-1
- BSC S7 — CR002BH2 — 35-1
- BSC S0 — CR002DB2 — 38-1
- BSC S1 — CR002EB2 — 41-1
- GATE INPUT 7A — CR004DB2 — 44-62
- DLC S0 8 S15 7 — CR005BF2 — 47-1
- DLC S1 8 S0 7 — CR005BF2 — 50-2
- DLC S2 8 S1 7 — CR005BG2 — 53-11
- DLC S3 8 S2 7 — CR005BH2 — 56-2
- DLC S5 8 — CR005CE2 — 59-1
- DLC S6 8 S5 7 — CR005CG2 — 62-2
- DLC S7 8 S6 7 — CR005CH2 — 65-2
- DLC S8 8 S7 7 — CR005CJ2 — 68-1
- Z BUS BIT 0.4 — DH014GK6 — 71-1

THIS PAGE IS FOR 3705-II ONLY.

SET PROG PWR OFF — AC — A FL S10 104 — S S — 5 ZU12 A, 7 ZB02 A, 2 ZU02 AR R — A-B3S2

CRC TO INDATA BIT 0.3 — AD — 8 ZS13 A OR S03 114 — 23 -49V A, 11 -U09 A, 56 -49T A, 14 -48V A, 44 -48W A — A-B3S2

CRC TO INDATA BIT 0.5 — BA — 8 ZS13 A OR S04 127 — 29 -54Q A, 11 -U09 A, 59 -54N A, 44 -53R A, 62 -53Q A — A-B3S2

CRC TO INDATA BIT 0.7 — AH — 8 ZS13 A OR U06 140 — 35 -54V A, 11 -U09 A, 65 -54T A, 44 -53W A, 68 -53V A — A-B3S2

CRC TO INDATA BIT 0.2 — AA — 8 ZS13 A OR P09 153 — 20 -46V A, 11 -U09 A, 53 -46T A, 44 -48W A, 56 -46W A — A-B3S2

CRC TO INDATA BIT 0.6 — AF — 8 ZS13 A OR U13 166 — 32 -51V A, 11 -U09 A, 62 -51T A, 44 -53W A, 65 -51W A — A-B3S2

CRC TO INDATA BIT 0.4 — AG — 8 ZS13 A OR S09 179 — 26 -51Q A, 11 -U09 A, 14 -51N A, 17 -51R A, 44 -53R A — A-B3S2

LOC. TYPE
A-B3S2 Y703

CRC TO INDATA BIT 0.1 — AE — 8 ZS13 A OR P11 204 — 41 -49Q A, 11 -U09 A, 50 -49N A, 44 -48R A, 53 -48Q A — A-B3S2

CRC TO INDATA BIT 0.0 — AB — 8 ZS13 A OR M12 217 — 38 -46Q A, 11 -U09 A, 47 -46N A, 44 -48R A, 50 -46R A — A-B3S2

104 + PROGRAM POWER OFF — AE6

217 + CRC TO INDATA BIT 0.0 — CU011-AF6

204 + CRC TO INDATA BIT 0.1 — CU011-CF6

179 + CRC TO INDATA BIT 0.4 — CU011-DF6

166 + CRC TO INDATA BIT 0.6 — CU011-DG6

153 + CRC TO INDATA BIT 0.2 — CU011-EF6

140 + CRC TO INDATA BIT 0.7 — CU011-EG6

127 + CRC TO INDATA BIT 0.5 — CU011-EH6

114 + CRC TO INDATA BIT 0.3 — CU011-GF6

CC CONTROL
BSC AND DLC CRC
—E.C.—HISTORY— D
312922
314419

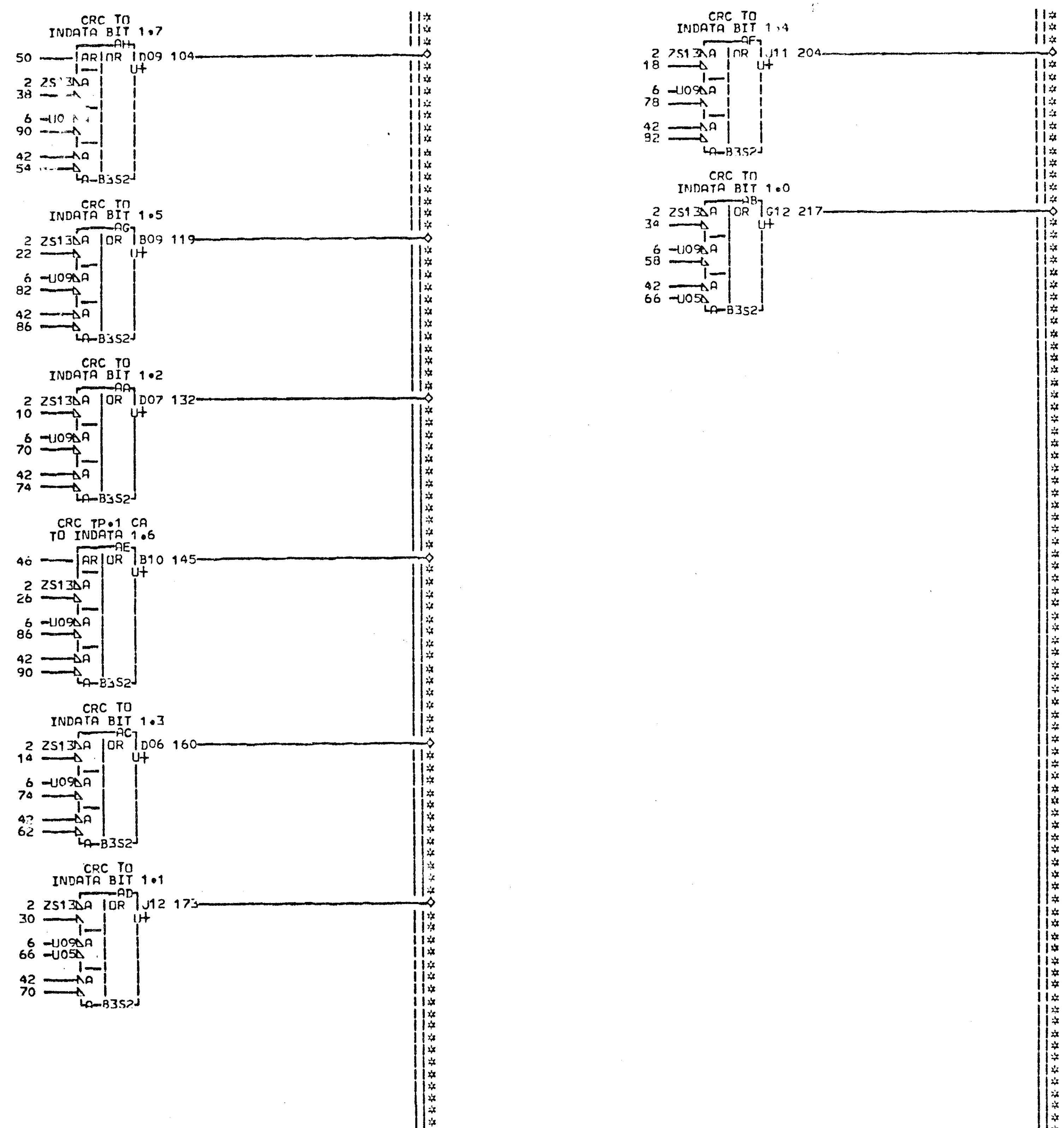
DATE LAST EC
10-06-76 315053

AACH.27RNB
FRAME 01
IBM CORP.
P.N. 1750180

CR006
030

CR006
030 SIM TO PN 1857262 EC 310262

- GATE INPUT 7B——CQ005DM6— 2-62
- GATE INPUT 7C——CQ005FJ6— 6-62
- P2 BSC S10——CR001CE6— 10-1
- P3 BSC S11——CR001CF6— 14-1
- P4 BSC S12——CR001CH6— 18-1
- P5 BSC S13——CR001CJ6— 22-1
- P6 BSC S14——CR001CL6— 26-1
- BSC S9——CR002BK2— 30-1
- BSC S8——CR002CJ2— 34-1
- BSC S15——CR002FN2— 38-1
- GATE INPUT 7A——CR004DB2— 42-62
+ TYPE 1 CA INSTALLED 3705——CR004DH2— 46-1
+ ROS ESC TO INDATA 1.7——CR004DL2— 50-1
- DLC S0 8 S15 7——CR005BE2— 54-1
- DLC S8 8 S7 7——CR005CJ2— 58-1
- DLC S11 7——CR005CM2— 62-1
- TEST PIN SDLC S9 8 S8 7——CR005DJ2— 66-1
- DLC S10 8 S9 7——CR005DK2— 70-2
- DLC S11 8 S10 7——CR005DL2— 74-2
- DLC S12 8——CR005DM2— 78-1
- DLC S13 8 S12 7——CR005EL2— 82-1
- DLC S14 8 S13 7——CR005EM2— 86-2
- DLC S15 8 S14 7——CR005EN2— 90-2



LOC. TYPE
A-B3S2 Y703

217 + CRC TO INDATA BIT 1.0— CU012-AF6
204 + CRC TO INDATA BIT 1.4— CU012-BF6
173 + CRC TO INDATA BIT 1.1— CU012-CF6
160 + CRC TO INDATA BIT 1.3— CU012-DF6
145 + CRC TP.1 CA TO INDATA 1.6——DG6 CU012
132 + CRC TO INDATA BIT 1.2— CU012-EF6
119 + CRC TO INDATA BIT 1.5— CU012-FF6
104 + CRC TO INDATA BIT 1.7— CU012-GF6

CC CONTROL BSC AND DLC CRC	MACH.27RNB
E.C. HISTORY 310268	FRAME 01
	IBM CORP.
DATE 03-26-73 LAST EC 310262	P.N. 1857263

030 CR008

- HEXSW BYTE X BIT 6 FROM CV——AP001EC4— 2
- HEXSW BYTE X BIT 7 FROM CV——AP001ED4— 9
- FORCE INDATA PARITY ERROR——CK001GA6— 16
- GATE HEXSW A AND C AND E——CS004GH2— 23
+ CCU INDATA BIT 1.0——CU012DC4— 30
+ CCU INDATA BIT 1.1——CU012DD4— 37
+ CCU INDATA BIT 1.2——CU012DE4— 44
+ CCU INDATA BIT 1.3——CU012DG4— 51
+ CCU INDATA BIT 1.4——CU012DJ4— 58
+ CCU INDATA BIT 1.5——CU012DK4— 65
+ CCU INDATA BIT 1.6——CU012DL4— 72
+ CCU INDATA BIT 1.7——CU012DN4— 79

CCU INDATA BIT 1.P

16 ZD1C
30 ZD12
37 ZJ12
44 ZD07
51 ZD06
58 ZJ11
65 ZB09
72 ZB10
79 ZD09
EV
B11 104
A-B3S2

HEWSX BYTE X BIT 7

9 ZJ04
23 ZJ06
A OR
U04 117
A-B3S2

HEWSX BYTE X BIT 6

2 ZJ03
23 ZJ06
A OR
S08 124
A-B3S2

104 + CCU INDATA BIT 1.P—— CU013-CG2
124 + HEWSX BYTE X BIT 6—— CU011-DD2
117 + HEWSX BYTE X BIT 7—— CU011-DE2

THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE
A-B3S2 Y703

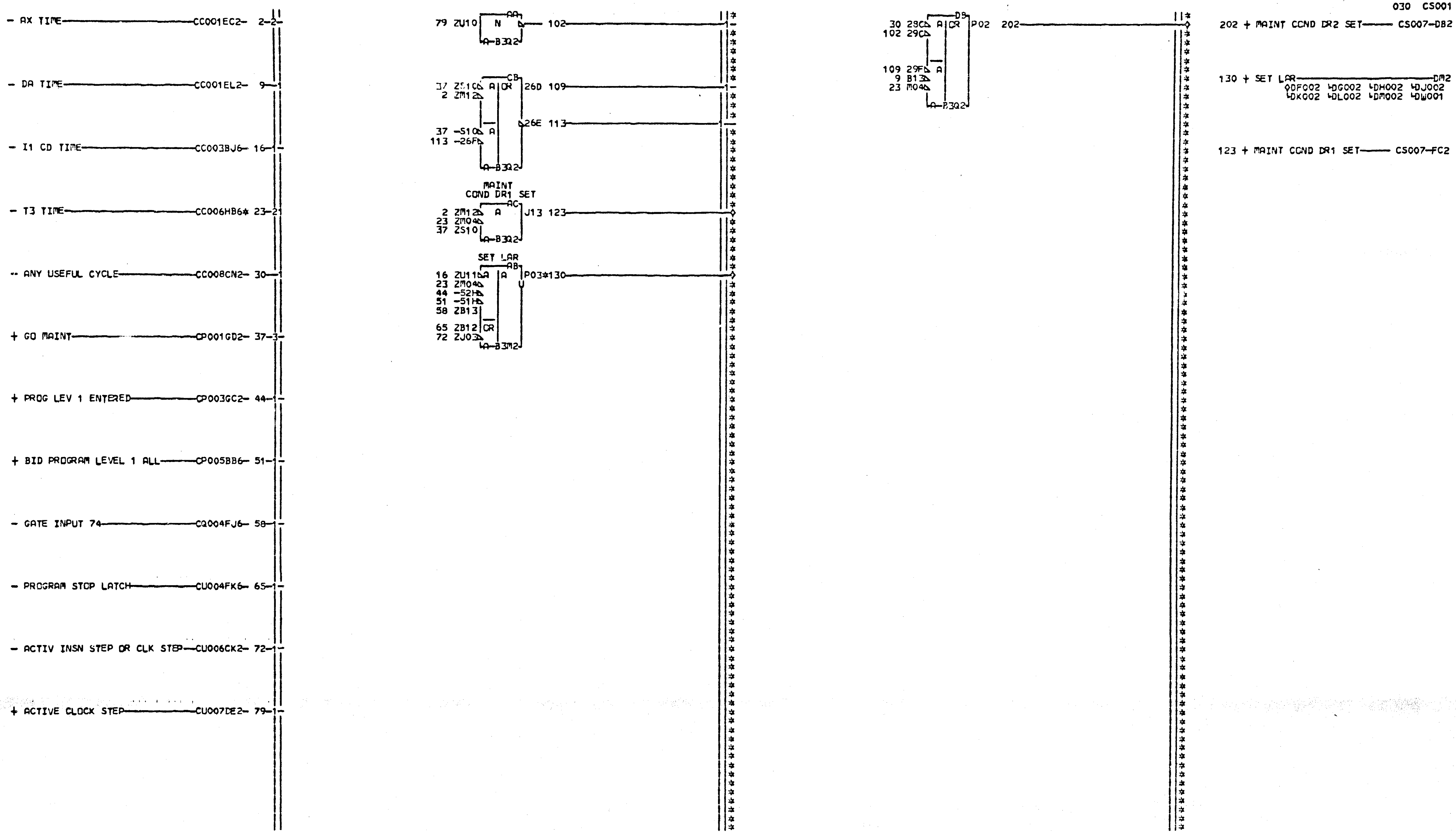
CC CONTROL
BSC AND TLC CRC
E.C. HISTORY
312922
314419

DATE LAST EC
10-06-76 315033

MACH.27RNB
FRAME 01
IBM CORP.
P.N. 1750181

CR008
030

CR008
030 SIM TO PN 1857264 EC 310269



- AX TIME——CC001EC2- 2-2
- DA TIME——CC001EL2- 9-1
- I1 CD TIME——CC003BJ6- 16-1
- T3 TIME——CC006HB6* 23-2
-- ANY USEFUL CYCLE——CC008CN2- 30-1
+ GO MAINT——CP001GD2- 37-3
+ PROG LEV 1 ENTERED——CP003GC2- 44-1
+ BID PROGRAM LEVEL 1 ALL——CP005BB6- 51-1
- GATE INPUT 74——CQ004FJ6- 58-1
- PROGRAM STOP LATCH——CU004FK6- 65-1
- ACTIV INSN STEP OR CLK STEP—CU006CK2- 72-1
+ ACTIVE CLOCK STEP——CU007DE2- 79-1
79 ZU10 N AA 102
A-B3Q2
37 ZS10 2 ZM12 A OR CB 26D 109
37 -S10 113 -26F A 26E 113
A-B3Q2
MAINT COND DR1 SET
2 ZM12 23 ZM04 37 ZS10 A AC J13 123
A-B3Q2
SET LAR
16 ZU11 23 ZM04 44 -52H 51 -51H 58 ZB13 A AB P03*130
65 ZB12 72 ZJ03 OR
A-B3M2
30 29C 102 29C A OR DB P02 202
109 29F 9 B13 23 M04 A
A-B3Q2
202 + MAINT COND DR2 SET—— CS007-DB2
130 + SET LAR——DM2
DF002 DG002 DH002 DJ002
DK002 DL002 DM002 DW001
123 + MAINT COND DR1 SET—— CS007-FC2

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
23 RESISTOR
A-B3M2M04
130 A-B3L6D02
01A-B4L1D11

LOC. TYPE
A-B3M2 6818
A-B3Q2 6821

CS001
030 SIM TO PN 5997632 EC 310268

DATA FLOW REGISTER CONTROL		
E.C.-HISTORY 312922 314419	MACH.27RNB FRAME 01	
DATE 10-06-76 LAST EC 315053	IBM CORP.SDD P.N. 1750182	CS001 030

030 CS002

- BX TIME — CC001EG2 — 2
- I1 TIME + 125 NS — CC004FB6 — 5
+ A0T2 TIME — CC008AM2 — 8
- ANY I TIME — CC008BM2 — 11
- RI INST TYPE — CD002CF6 — 14
- L INST — CD003AH6 — 17
- IC+ICT INST — CD003CC2 — 20
- STCT+STC INST — CD003CE2 — 23
- RR OR I-O INST TYPE — CD004BG2 — 26
- INST GROUP 6 — CD004BH2 — 29
- BAL+LA+LH INST — CD004BK2 — 32
+ SAR EVEN PARITY — CK003FF2 — 35
- ADDRESS EXCEPTION CONDITION — CM002EM6 — 38
+ INHIBIT ADDR EXCEPT — CM002GA6 — 41
- L+ST INST — CS004HM2 — 44
+ ANY USEFUL TIME + 125 NS — CS006BD2 — 47
+ CS2 BX TIME — CS006BF2 — 50
- I2 BX TIME — CS006BH6 — 53
- I3 BX TIME — CS006BK6 — 56
- CS1 BX TIME — CS006BM6 — 59
- I1 CD TIME — CS006DF6 — 62
- CS1 CD TIME — CS006DM6 — 65
- I2 AX TIME — CS006EH6 — 68
- DISPLAY REGISTER CD — CU003GD6 — 71
+ BLOCK ADDR EXCEPTION — CU003GE6 — 74
- STORE REGISTER CD — CU003GF6 — 77
+ RESET MACHINE CHECK — CU006CB6 — 80
+ RESET — CU010FM2 — 83
+ PROTECTION ERROR — CV061FB2 — 86
+ SAR BIT 1.7 — DM001EK2 — 89

THIS PAGE IS FOR 3705-II ONLY.

SAMPLE AT A0T2 (AG) 8 ZB07 N 06D → 110

+ RST PH OUT (AH) 2 ZG09 N 38N → 117

NOT DSP+ST REG (BD) 71 ZG08 A OR 11U → 124; 77 -G10 A

L+ST BAL+LA+LH (CJ) 44 01C A OR 01D → 137; 56 02C; 53 02F W; 32 D03 A

DCD (AE) 89 ZS08 1 → 46F → 150; 53 -48E 2 → 49E → 152

- ADDR EXCPTN (AC) 41, 38, 74 → DOT; OR * → 157

STORE REGISTER CD (AB) 77 ZG10 N 33C → 166

L INST (AA) 17 ZL11 N 06E → 172

RESET M0C0 PH (DM) 102 13T A OR ↑+V → 203; 110 14T; 14U → 207; 8 B07 A; 207 13V; 117 13V

IC STC+STCT (DD) 152 -03C A OR 04E → 238; 20 ZP12; 23 -B05 A; 150 -03F

+ BAD ADDR (AF) 35 ZB11, 83 ZG07, 86 ZB12, 157 ZB13 OR → 11K → 252

BAD ADDR PH (DK) 252 01N A OR 01P → 352; 110 02N; 8 B07 A; 356 D02 → D02* → 356

INST-FTCH CS GRP-6 L RR+IO ALL SDR BYTES (BB) 203 -38F OR OR 39C → 305; 83 ZG07; 29 ZB02 A; 68 -02H; 17 -U11 A; 53 -01K; 26 -D04 A; 62 -04H; 2 ZG09 A; 5 ZU07 A; 65 -21F A; 71 -G08; 65 -24C A; 77 -G10 A

CS1 BX NOT REG SDR BYTES 0+1 (BA) 137 13L OR OR S13 → 404; 50 14K; 305 39C; 124 13R A; 59 14R A

BAD INST ADDRESS (BF) 352 -17H A OR M10 → 453; 11 ZG04 A

CONDITION SET SDR BYTE 1 (BE) 238 -54D OR 54E → 504; 404 ZS13

RI IC+ICT STC (BC) 404 ZS13 RR OR 53C → 545; 14 ZD05 A; 62 -04H; 20 -P12 A; 150 -03L A; 23 -B05 A; 152 -03K A

172 + L INST — CS004-AD2

166 + STORE REGISTER CD — CS004-AF2

157 - ADDRESS EXCEPTION — BK4
CK007 CU014

305 - CONDITION SET SDR BYTE X — DF2
CS007

356 + BAD ADDRESS — DK6
CK007 CM001 CQ002 CS005 DB101 DP992 DR992

203 + RESET MACHINE CK SDR OP REG — DM2
CS007

545 - CONDITION SET SDR BYTE 0 — EB2
CS007

504 - CONDITION SET SDR BYTE 1 — ED2
CS007

453 + BAD INST ADDRESS — CS007-EJ2

EDGE CONN.
157 A-B4D6E02
01A-B3D1E11
356 A-B331E13
01A-B486E04

LOC. TYPE
A-B3F2 6810

DATA FLOW REGISTER CONTROL

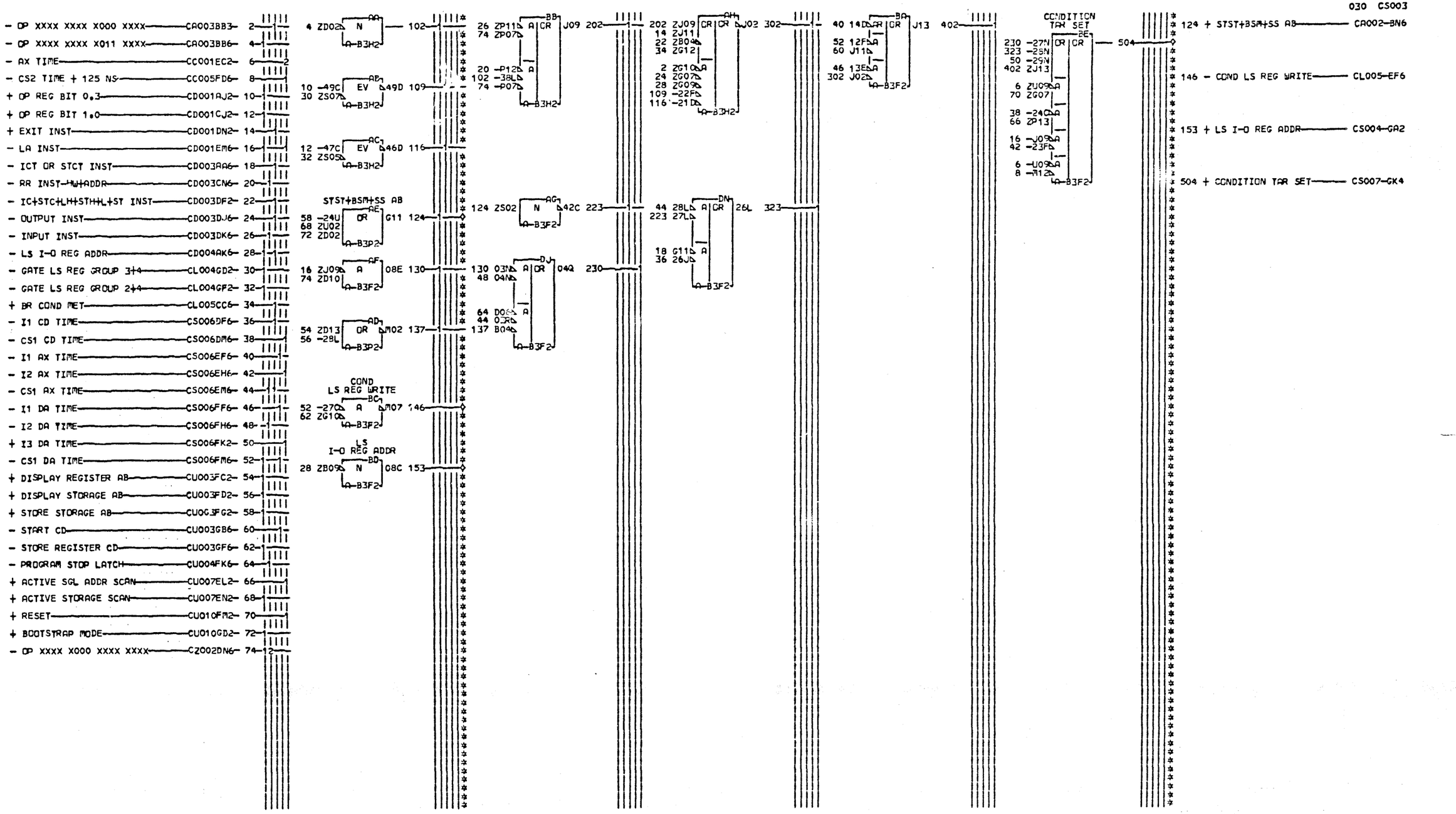
E.C. HISTORY: 312922 314419

DATE 10-06-76 LAST EC 315053

MACH.27RNB
FRAME 01
IBM CORP.SDD
P.N. 1750183

CS002
030

CS002
030 SIM TO PN 5997633 EC 310262



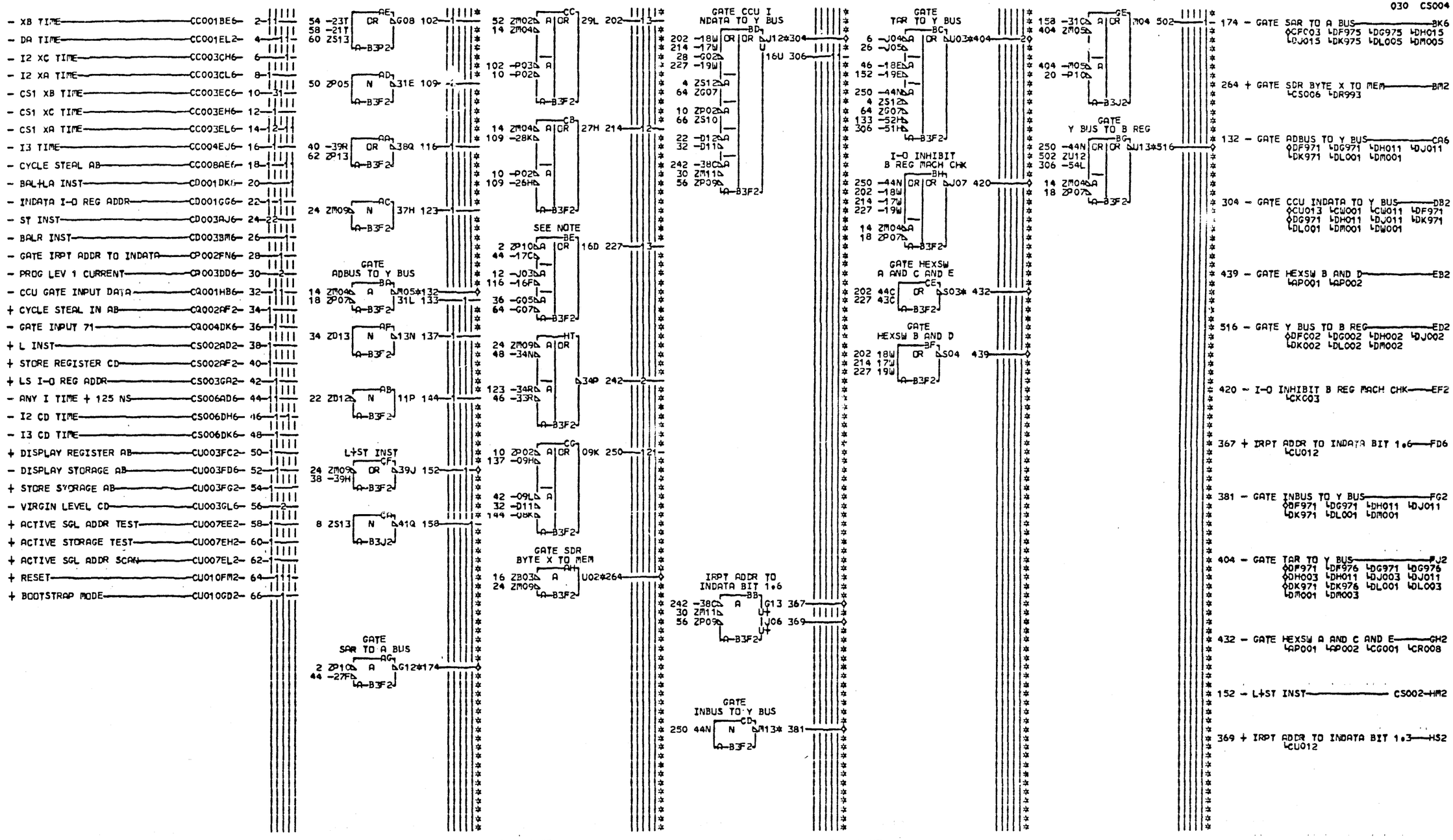
THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE
A-B3F2 6810
A-B3H2 AB91
A-B3P2 6820

DATA FLOW REGISTER CONTROL

--E.C.--HISTORY--D	MACH.27RNB	
312922	FRAME 01	
314419	IBM CORP.SDD	CS003
DATE LAST EC	P.No. 1750184	030
10-06-76 315053		

CS003
030 SIM TO PN 5997634 EC 310268



THIS PAGE IS FOR 3705-II ONLY.

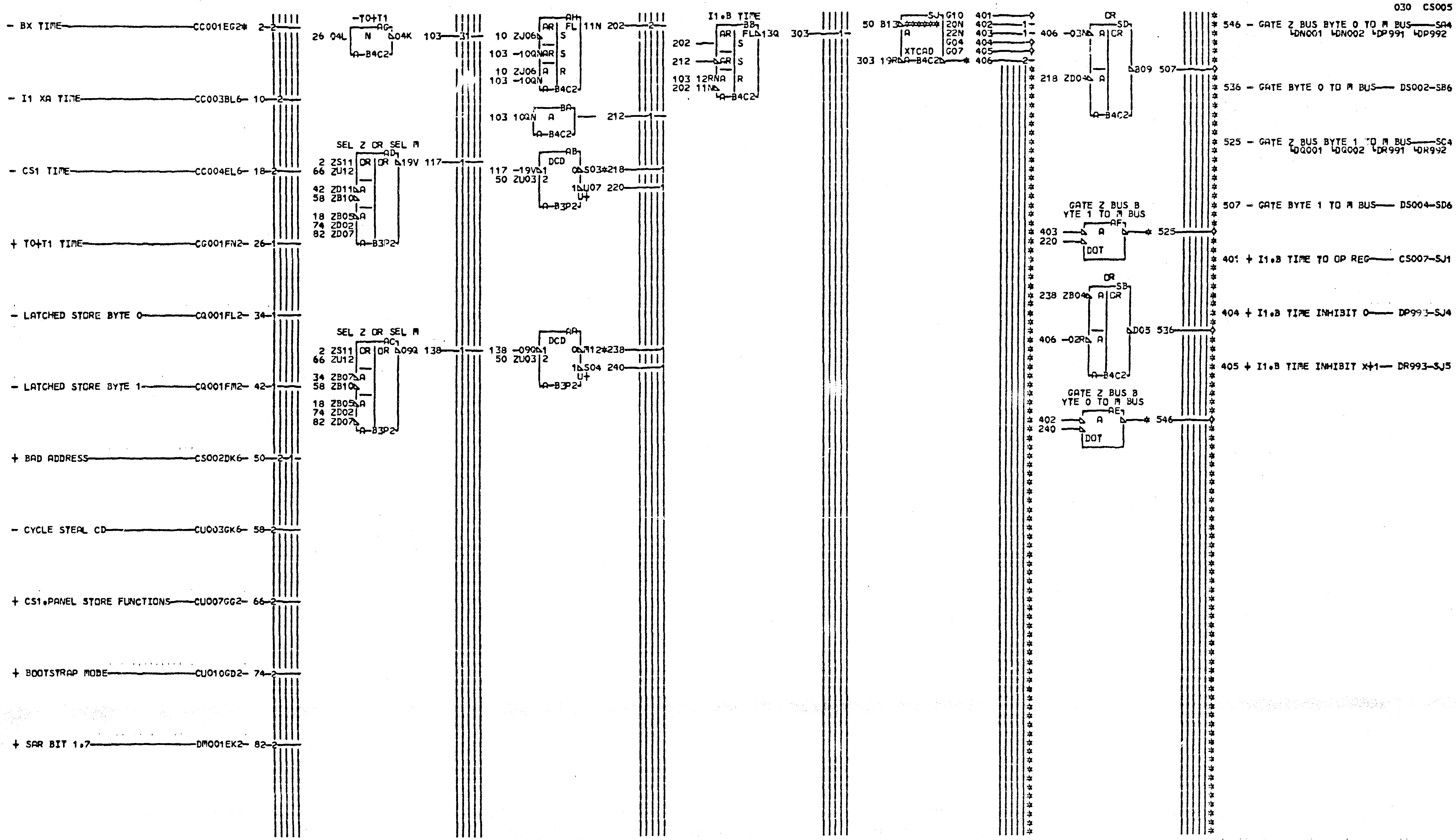
NOTE + RESET AT INPUT M SERVES NO FUNCTIONAL PURPOSE

CS004
030 SIM TO PN 5997635 EC 310262

EDGE CONN.
132 A-B3H1D11 01A-B4H6D02
174 A-B3H1B11 01A-B4H6B02
264 A-B3H1C13 01A-B4H6C04
304 A-B3H1D13 01A-B4H6D04
381 A-B3H1E13
404 A-B3J1A11 01A-B4J6A02
432 A-B3E1E11 01A-B4E6E02
516 A-B3H1E11 01A-B4H6E02

LOC. TYPE
A-B3F2 6810
A-B3J2 6815
A-B3P2 6820

DATA FLOW REGISTER CONTROL
E.C.-HISTORY- MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD CS004
DATE LAST EC
10-06-75 315053
P.N. 1750185 030



THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
2 RESISTOR
A-B3F2S11
218 A-B3J1D11
01A-B4J6D02
238 A-B3J1C13
01A-B4J6C04
406 RESISTOR
A-B4C2
525 RESISTOR

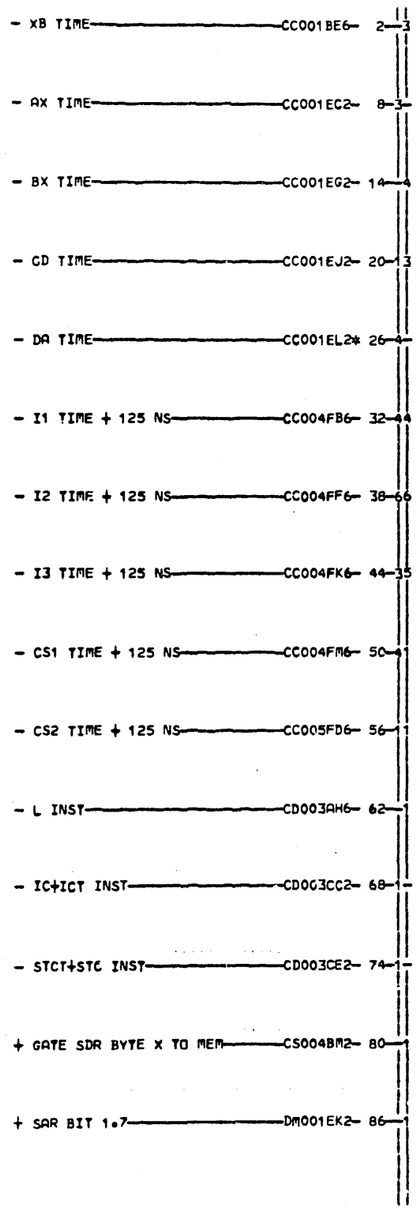
A-B3P2U07
01A-B3J1C11
01A-B4J6C02
546 RESISTOR
A-B3P2S04
01A-B3J1B13
01A-B4J6B04

LCC. TYPE
A-B3P2 6820
A-B4C2 AB93

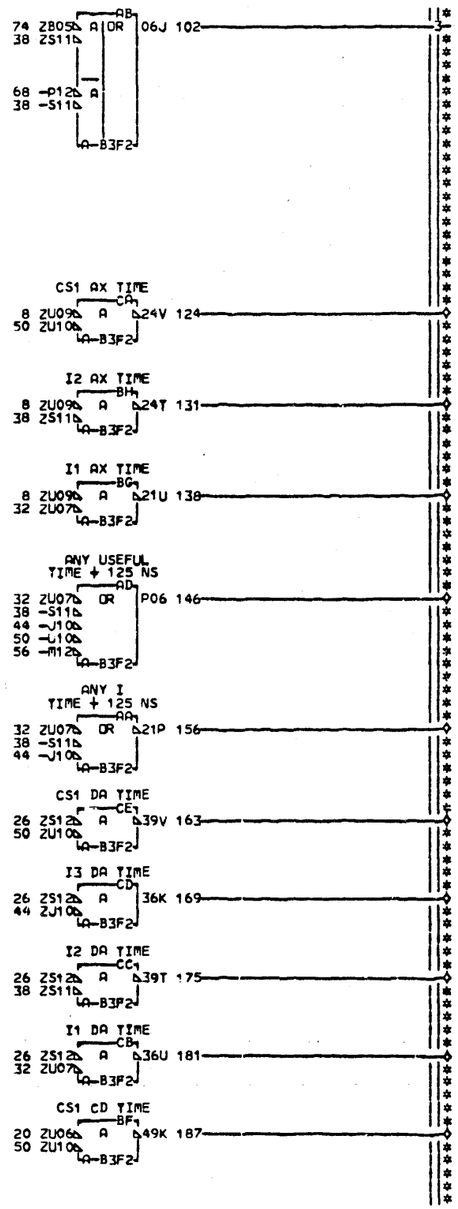
M BUS ASSEMBLER CONTROL

E.C.-HISTORY 312922 214419	D MACH.27RNB
	FRAME 01
	IBM CORP.SDD
DATE 08-31-76 LAST EC 315053	P.N. 1750186

CS005
030 SIM TO PN 5997636 EC 309533

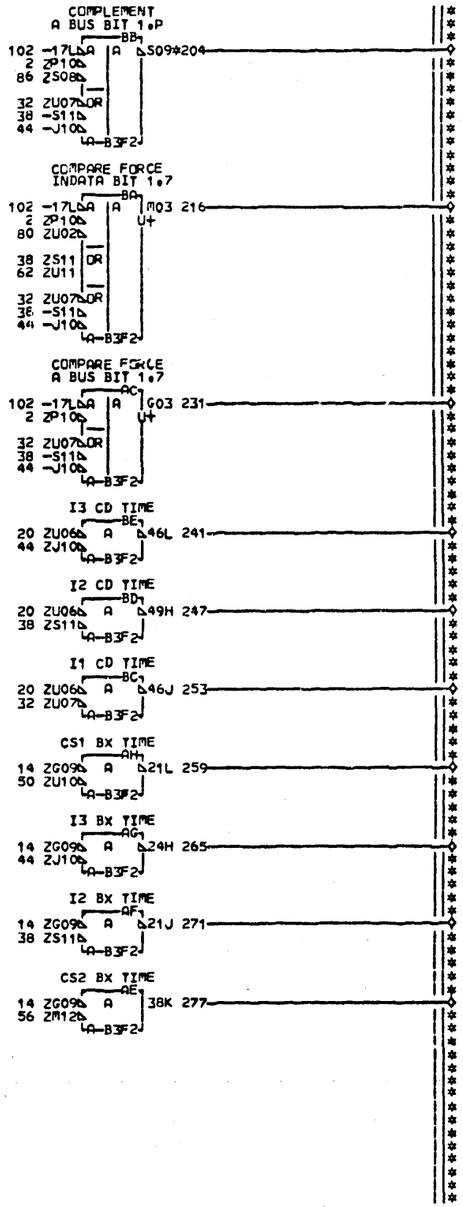


THIS PAGE IS FOR 3705-II ONLY.



EDGE CONN.
26 RESISTOR
A-B3F2S12
204 A-B3V3D11
01A-B4V3D11

LOC. TYPE
A-B3F2 6810



030 CS006

156 - ANY I TIME + 125 NS — AD6
└CS004 └CU014

231 + COMPARE FORCE A BUS BIT 1.7 — BA2
└DM005

146 + ANY USEFUL TIME + 125 NS — BD2
└CS002

277 + CS2 BX TIME — CS002-BF2

271 - I2 BX TIME — CS002-BH6

265 - I3 BX TIME — CS002-BK6

259 - CS1 BX TIME — CS002-BM6

216 + COMPARE FORCE INDATA BIT 1.7 — CC2
└CU012

204 - COMPLEMENT A BUS BIT 1.P — CD6
└DK975

253 - I1 CD TIME — DF6
└CS002 └CS003

247 - I2 CD TIME — CS004-DH6

241 - I3 CD TIME — CS004-DK6

187 - CS1 CD TIME — DM6
└CS002 └CS003

138 - I1 AX TIME — CS003-EF6

131 - I2 AX TIME — EH6
└CS002 └CS003

124 - CS1 AX TIME — EM6
└CS003 └CS007

181 - I1 DA TIME — CS003-FF6

175 - I2 DA TIME — CS003-FH6

169 + I3 DA TIME — CS003-FK2

163 - CS1 DA TIME — CS003-FM6

DATA FLOW REGISTER CONTROL
TIMING GENERATION
E.C. HISTORY — D MACH.27RNB
312922
314419
FRAME 01
IBM CORP. SDD CS006
DATE LAST EC
10-06-76 315053
P.N. 1750187 030

CS006
030 SIM TO PN 5997637 EC 310262

030 CS007

- OP XXXX XXXX X001 XXXX — CA003BB4 — 2-2
- OP XXXX XXXX X010 XXXX — CA003BB5 — 5-2
- DA TIME — CC001DM6 — 8-1
- AX TIME — CC001EC2 — 11-2
- BX TIME — CC001EG2 — 14-1
- I1 B TIME — CC002BC6 — 17-1
- T3 TIME — CC006HB6 — 20-1
- T3+T0 TIME — CC007HJ1* — 23-1
- COND LS WRITE — CL005FH2 — 26-1
- SET OUTPUTS 70 TO 77 — CQ004BA6 — 29-1
+ MAINT COND DR2 SET — CS001D32 — 32-2
+ MAINT COND DR1 SET — CS001FC2 — 35-2
- CONDITION SET SDR BYTE X — CS002DF2 — 38-1
+ RESET MACHINE CK SDR OP REG — CS002DM2 — 41-1
- CONDITION SET SDR BYTE 0 — CS002EB2 — 44-1
- CONDITION SET SDR BYTE 1 — CS002ED2 — 47-1
+ BAD INST ADDRESS — CS002EJ2 — 50-1
+ CONDITION TAR SET — CS003GK4 — 53-1
+ I1.B TIME TO OP REG — CS005SJ1 — 56-1
- CS1 AX TIME — CS006EM6 — 59-1
- PHASE B SAMPLE PULSE — CU001CB2 — 62-1
+ DISPLAY REGISTER AB — CU003FC2 — 65-1
- START CD — CU003GB6 — 68-1
+ RESET — CU010FM2 — 71-1

START•CS1B (BF)
14 ZG09 A OR D10 103
68 ZD05 U+
A-B3U5

41 -34F AB
71 ZG07 OR D33E 109
A-B3F2

INHIBIT SDR (BG)
50 CR
103 DOT
* 203

SET OP REG (AD)
14 ZG09 A OR U05 303
17 ZS07 +
11 -U09 A
23 -S05
203 ZM10

11 -U09 A
23 -S05
109 -49Q

23 -S05 A
59 -48R
65 ZP05
A-B3F2

-T3+T0 (AA)
23 ZS05 A — 321
203 ZM10
A-B3F2

GATE LS WRITE (BA)
26 -23F A J10 338
203 ZJ12
A-B3K2

SET TAR (AC)
20 ZD07 A U04*345
53 -14P
203 ZM10
A-B3F2

SET OP REG (BH)
56 CR * 403
303 DOT

SET SDR BYTE X (AH)
38 07R A P11* 417
321
A-B3F2

SET SDR BYTE 1 (AG)
47 09Q A B10* 424
321
A-B3F2

SET SDR BYTE 0 (AF)
44 07N A D09* 431
321
A-B3F2

345 + SET TAR — CH2
◊DF971 ŁDG971 ŁDH011 ŁDJ011 ŁDK971 ŁDL001 ŁDM001

181 + SET SAR — CQG01-EA2

182 + SET SAR — EB2
◊CU007 ŁDF971 ŁDG971 ŁDH011 ŁDJ011 ŁDK971 ŁDL001 ŁDM001

431 + SET SDR BYTE 0 — EE2
ŁDN001 ŁDN002 ŁDP991 ŁDP992

424 + SET SDR BYTE 1 — EF2
ŁDQ001 ŁDQ002 ŁDR991 ŁDR992

417 + SET SDR BYTE X — EG2
◊DN002 ŁDP991 ŁDP992 ŁDQ002 ŁDR991 ŁDR992

338 + GATE LS WRITE — CC006-EM2

PGM DISPLAY (BE)
2 ZG09 A FLA B12 152
29 ZG11 S S
5 ZJ11 A S
29 ZG11
32 ZD12 OR R
35 ZB11
A-B3R2

PGM DISPLAY TO IND (BD)
62 ZD04 A OR M13 253
152 ZS11 U+
A-B3L2

SET DR1 (BB)
35 ZB11 AR OR J02*165
2 ZG09 A
29 ZG11
A-B3R2

SET DR2 (BC)
32 ZD12 AR OR D13*173
5 ZJ11 A
29 ZG11
A-B3R2

SET SAR (AE)
8 -21P A 21E 181
23 ZG07 G08*182
A-B3R2

165 + SET DR1 — FC6
◊DF003 ŁDG003 ŁDH003 ŁDJ003 ŁDK003 ŁDL003 ŁDM003

173 + SET DR2 — FD6
◊DF003 ŁDG003 ŁDH003 ŁDJ003 ŁDK003 ŁDL003 ŁDM003

253 + PGM DISPLAY TO IND — AP009-FK2

403 + SET OP REG — SD4
◊DN001 ŁDN002 ŁDP991 ŁDP992 ŁDQ001 ŁDQ002 ŁDR991 ŁDR992

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
23 RESISTOR 345 A-B3L6D04
A-B3F2S05 01A-B4L1D13
165 A-B3M6C04 403 A-B3L6E02
01A-B4M1C13 01A-B4L1E11
173 A-B3M6D02 417 A-B3M6C02
01A-B4M1D11 01A-B4M1C11
182 A-B3M6A04 424 A-B3M6B04
01A-B4M1A13 01A-B4M1B13
203 RESISTOR 431 A-B3M6B02

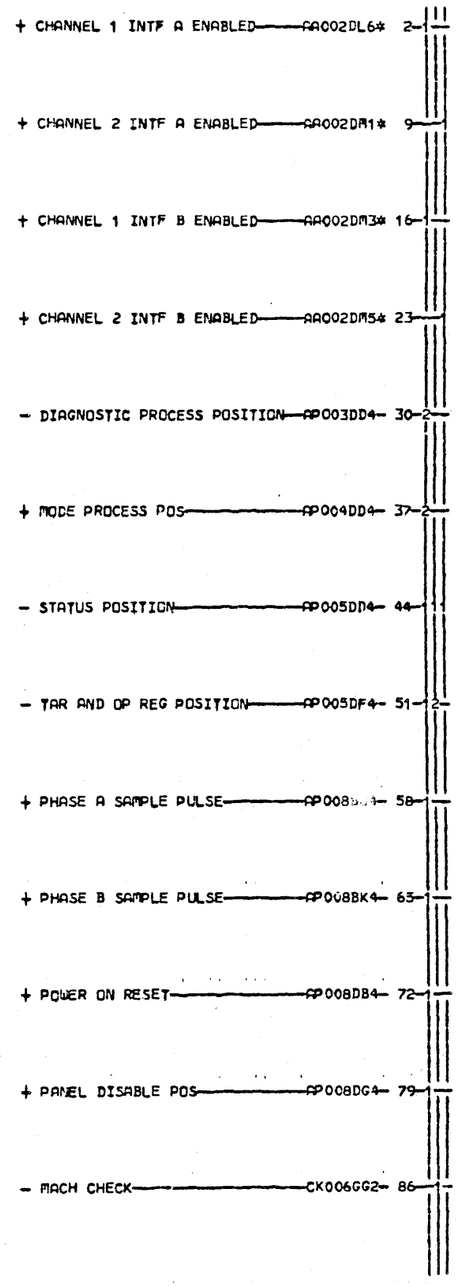
A-B3F2M10 01A-B4M1B11

LOC. TYPE
A-B3F2 6810
A-B3K2 6813
A-B3L2 6823
A-B3R2 A892
A-B3U5 A885

SET REGISTER TIMING

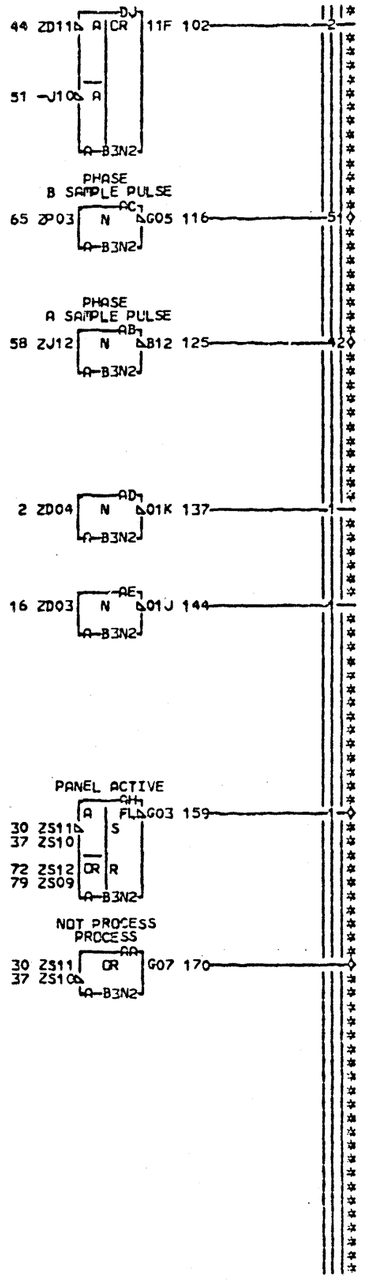
E.C.-HISTORY-	D. MACH.27RNB	
312922		
314419	FRAME 01	
	IBM CORP.SDD	CS007
DATE LAST EC	P.N. 1750188	030
08-JI-76 315053		

CS007
030 SIM TO PN 5997638 EC 310268

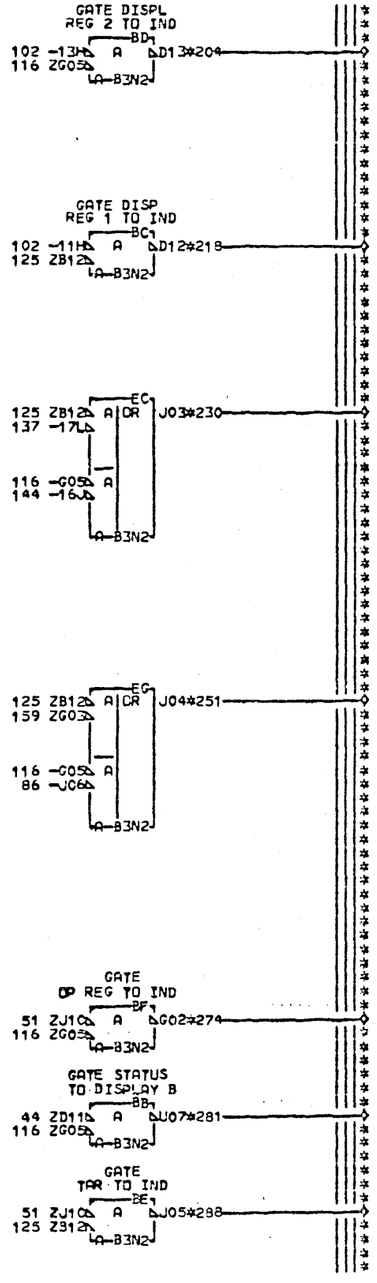


THIS PAGE IS FOR 3705-II ONLY.

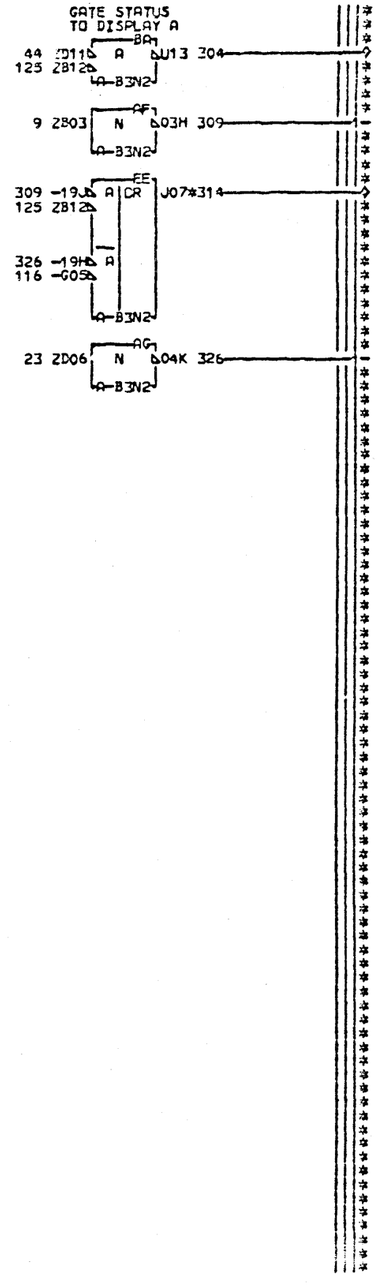
CU001
030 SIM TO PN 5997639 EC 310268



EDGE CONN. 01A-B4U6B04 01A-B4T6A02
2 RESISTOR 218 A-B3U1A11 298 A-B3T1A13
A-B3N2D04 01A-B4U6A02 01A-B4T6A04
9 RESISTOR 230 A-B3T1C13 314 A-B3T1D11
A-B3N2B03 01A-B4T6C04 01A-B4T6D02
16 RESISTOR 251 A-B3T1D13
A-B3N2D03 01A-B4T6D04
23 RESISTOR 274 A-B3T1311
A-B3N2D06 01A-B4T6B02
204 A-B3U1b13 281 A-B3T1A11



LOC. TYPE
A-B3N2 6819



030 CU001

170 + NOT PROCESS PROCESS — CU005-AL6

159 - PANEL ACTIVE — BM6
◇CU003 ↳CU004 ↳CU006 ↳CU007
↳CU009 ↳CU010 ↳CU014

125 - PHASE A SAMPLE PULSE — CA2
↳CC008 ↳CP007 ↳CU006

116 - PHASE B SAMPLE PULSE — CB2
↳CP007 ↳CS007 ↳CU004 ↳CU005

230 + INDICATOR CHAN 1 — AP009-EC2

314 + INDICATOR CHAN 2 — AP009-EE2

251 + PANEL ACTIVE.CCU CHECK IND — EG2
↳AP009

304 - GATE STATUS TO DISPLAY A — EM6
◇CA001 ↳CC001 ↳CC002 ↳CK004
↳CK005 ↳CQ001

281 - GATE STATUS TO DISPLAY B — EJ6
◇CL005 ↳CP004 ↳CP005 ↳CU004
↳CU010 ↳CU014 ↳CV061

218 - GATE DISP REG 1 TO IND — EK6
◇DF003 ↳DG003 ↳DH003 ↳DJ003
↳DK003 ↳DL003 ↳DM003

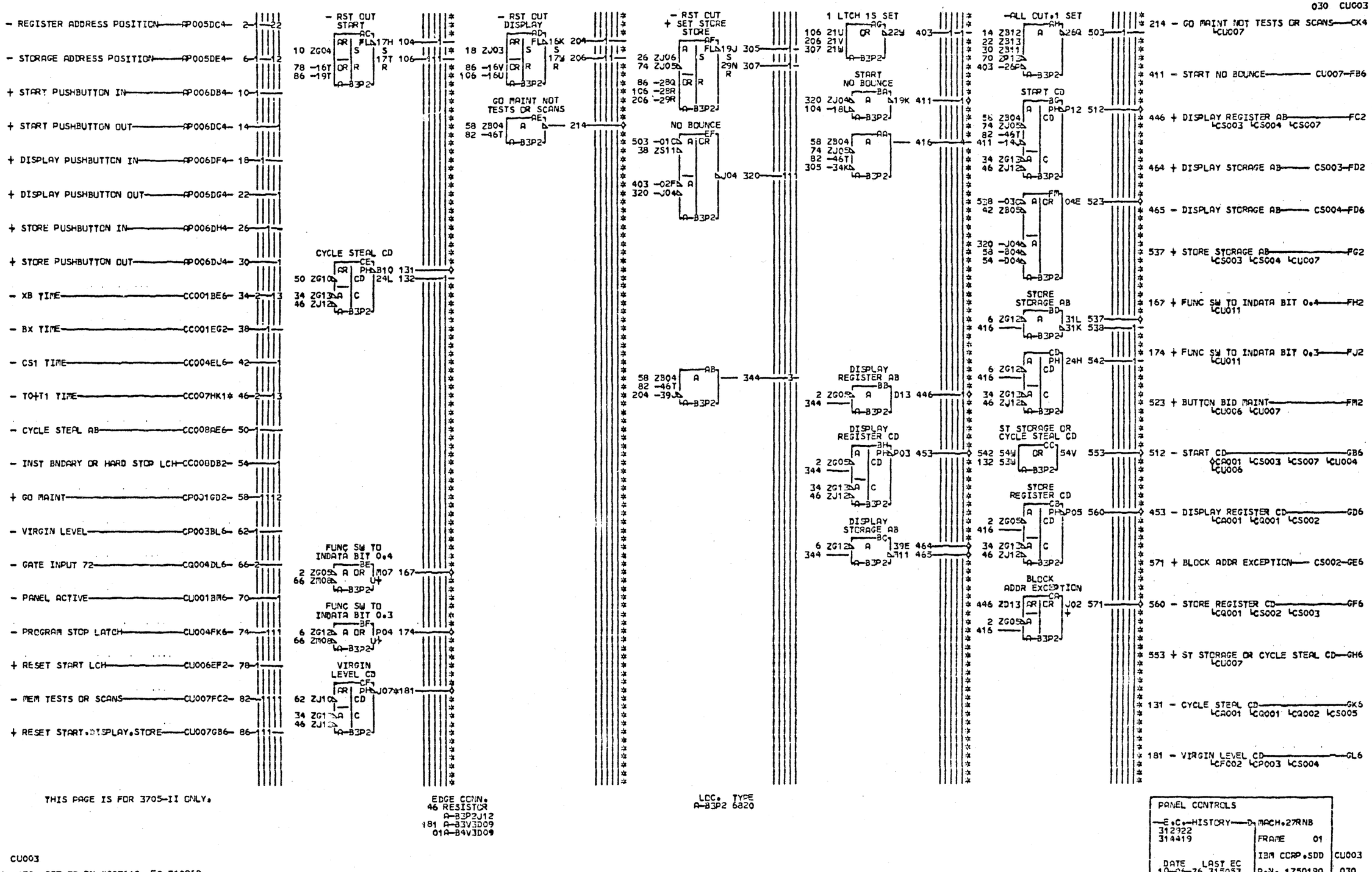
204 - GATE DISPL REG 2 TO IND — EL6
◇DF003 ↳DG003 ↳DH003 ↳DJ003
↳DK003 ↳DL003 ↳DM003

288 - GATE TAR TO IND — EM6
◇DF003 ↳DG003 ↳DH003 ↳DJ003
↳DK003 ↳DL003 ↳DM003

274 - GATE OP REG TO IND — DN005-FM6

PANEL CONTROLS

EC. HISTORY	MACH.27RNB
312922	FRAME 01
314419	IBM CCRP.SDD CU001
DATE LAST EC	P.N. 1750189 030
10-06-75 315033	



THIS PAGE IS FOR 3705-II ONLY.

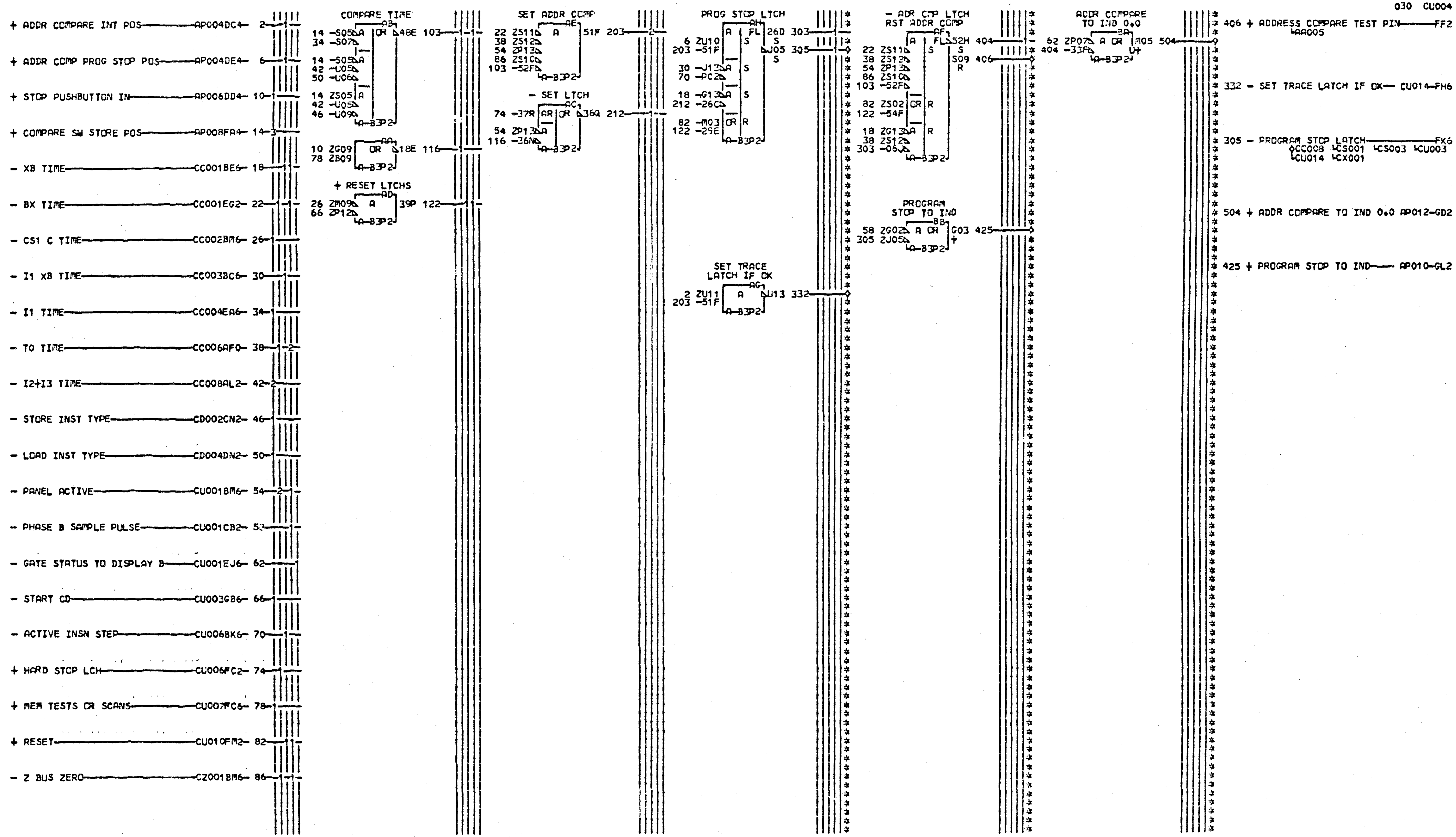
EDGE CONN.
46 RESISTOR
A-B3P2J12
181 A-B3V3D09
01A-B4V3D09

LOC. TYPE
A-B3P2 6820

PANEL CONTROLS

E.C.—HISTORY	MACH.27RNB
312922	
314419	FRAME 01
	IBM CORP.SDD — CU003
DATE LAST EC	P.N. 1750190 — 030
10-06-76 315053	

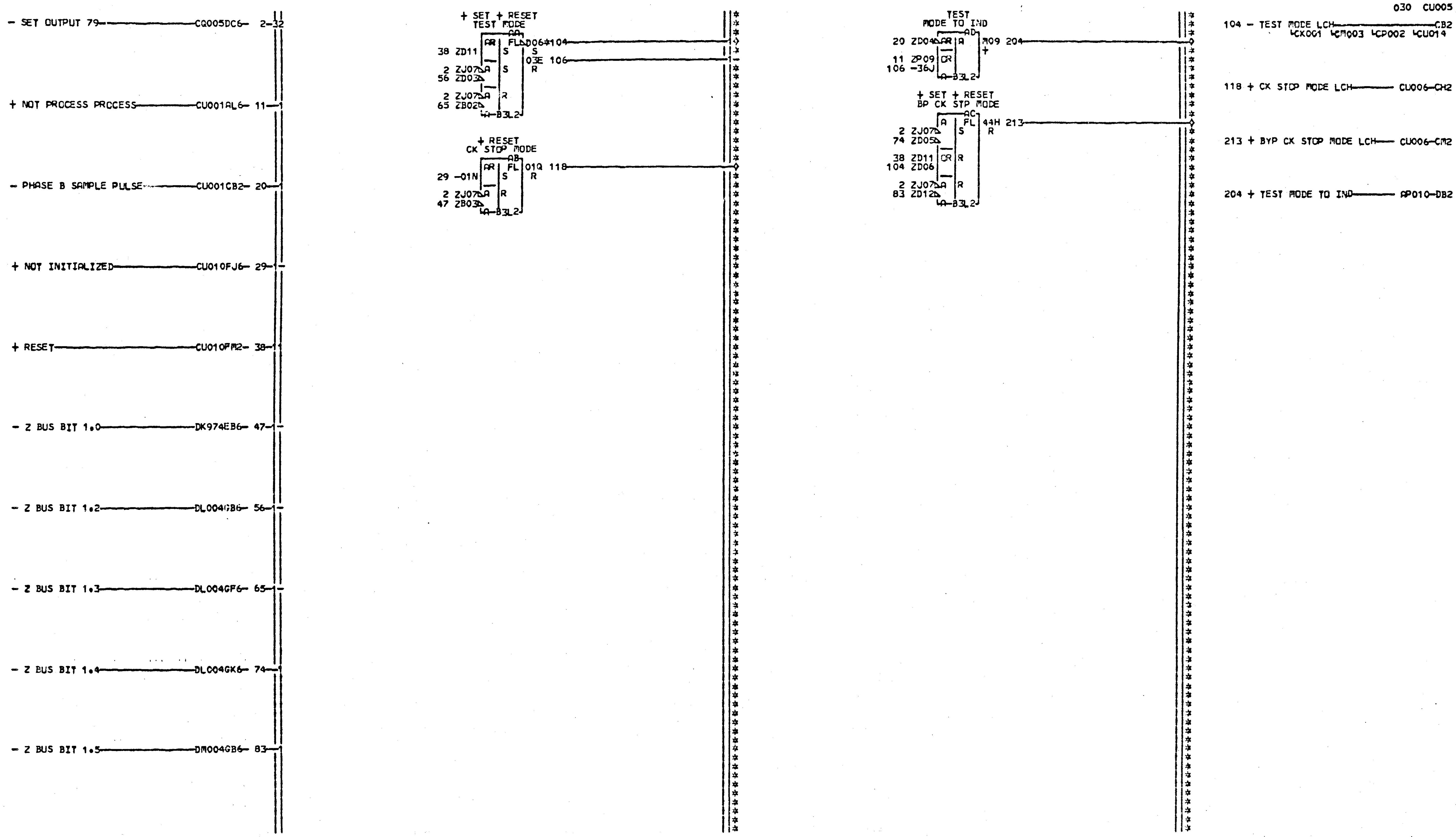
CU003
030 SIM TO PN 5997640 EC 310258


+ ADDR COMPARE INT POS AP004DC4 2
+ ADDR COMP PROG STOP POS AP004DE4 6
+ STOP PUSHBUTTON IN AP006DD4 10
+ COMPARE SW STORE POS AP008FA4 14
- XB TIME CC001BE6 18
- BX TIME CC001EG2 22
- CS1 C TIME CC002BM6 26
- I1 XB TIME CC003BC6 30
- I1 TIME CC004EA6 34
- TO TIME CC006AF0 38
- I2+I3 TIME CC008AL2 42
- STORE INST TYPE CD002CN2 46
- LOAD INST TYPE CD004DN2 50
- PANEL ACTIVE CU001BM6 54
- PHASE B SAMPLE PULSE CU001CB2 58
- GATE STATUS TO DISPLAY B CU001EJ6 62
- START CD CU003GB6 66
- ACTIVE INSN STEP CU006BK6 70
+ HARD STOP LCH CU006FC2 74
+ MEM TESTS OR SCANS CU007FC6 78
+ RESET CU010FM2 82
- Z BUS ZERO CZ001BM6 86
COMPARE TIME
SET ADDR COMP
- SET LTCH
+ RESET LTCHS
PROG STOP LTCH
SET TRACE LATCH IF OK
- ADR CMP LTCH RST ADDR COMP
PROGRAM STOP TO IND
ADDR COMPARE TO IND 0,0
406 + ADDRESS COMPARE TEST PIN FF2 AAC05
332 - SET TRACE LATCH IF OK CU014-FH6
305 - PROGRAM STOP LATCH FK6 CC008 CS001 CS003 CU003 CU014 CX001
504 + ADDR COMPARE TO IND 0,0 AP012-GD2
425 + PROGRAM STOP TO IND AP010-GL2

THIS PAGE IS FOR 3705-II ONLY.

LOC. TYPE
A-B3P2 6820

PANEL CONTROLS		
E.C. HISTORY 312922 314419	MACH.27RNB	
	FRAME 01	
	IBM CORP.SDD	CU004
DATE 10-06-76 LAST EC 315053	P.N. 1750191	030



- SET OUTPUT 79 CQ005DC6 2
+ NOT PROCESS PROCESS CU001AL6 11
- PHASE B SAMPLE PULSE CU001CB2 20
+ NOT INITIALIZED CU010FJ6 29
+ RESET CU010FM2 38
- Z BUS BIT 1.0 DK974EB6 47
- Z BUS BIT 1.2 DL004GB6 56
- Z BUS BIT 1.3 DL004GF6 65
- Z BUS BIT 1.4 DL004GK6 74
- Z BUS BIT 1.5 DM004GB6 83
+ SET + RESET TEST MODE
+ RESET CK STOP MODE
TEST MODE TO IND
+ SET + RESET BP CK STP MODE
104 - TEST MODE LCH CB2 CK001 CM003 CP002 CU014
118 + CK STOP MODE LCH CU006-CH2
213 + BYP CK STOP MODE LCH CU006-CM2
204 + TEST MODE TO IND AP010-DB2

THIS PAGE IS FOR 3705-II ONLY.

NOTE. + RESET CU010FM2 SETS TEST MODE LATCH. EITHER TEST MODE LATCH OR NOT PROCESS PROCESS TURNS ON TEST MODE LITE.

EDGE CONN.
104 A-B3F1311
01A-B4F6B02

LOC. TYPE
A-B3L2 6823

CU005
030 SIM TO PN 5997642 EC 310268

TEST MODE AND CK STOP MODE	
E.C. HISTORY D 312922 314419	MACH.27RNB
	FRAME 01
DATE LAST EC 10-06-76 315053	IBM CORP.SDD CU005
	P.N. 1750192 030

- BYPASS CHECK STOP POSITION — AP003DB4 — 2
- CC CHECK HARD STOP POSITION — AP003DC4 — 5
+ INSTRUCTION STEP POS — AP004DF4 — 8
+ CHECK RESET PUSHBUTTON IN — AP006DM4 — 11
- OP XXXX XXXX X000 XXXX — CA003BB3 — 14
- BX TIME — CC001EG2 — 17
- TO TIME — CC006AF0* — 20
- MACH CHECK — CK006GG2 — 23
- SET MACH CHECK — CK007DK2 — 26
- SET OUTPUTS 70 TO 77 — CQ004BA6 — 29
- SET OUTPUT 77 — CQ004FE6 — 32
- SET OUTPUT 79 — CQ005DC6 — 35
- PANEL ACTIVE — CU001BM6 — 38
- PHASE A SAMPLE PULSE — CU001CA2 — 41
+ BUTTON BID MAINT — CU003FM2 — 44
- START CD — CU003GB6 — 47
+ CK STOP MODE LCH — CU005CH2 — 50
+ BYP CK STOP MODE LCH — CU005CM2 — 53
+ SET STEP OR MEM TESTS — CU007CD2 — 56
+ ACTIVE CLOCK STEP — CU007DE2 — 59
- MEM TESTS OR SCANS — CU007FC2 — 62
+ MEM TESTS OR SCANS — CU007FC6 — 65
- ACTIVE STORAGE SCAN OR TEST — CU007FD2 — 68
+ ACTIVE RESET PUSHBUTTON — CU010AK2 — 71
- POR OR RESET SW — CU010BM2 — 74
+ IPL 2 LATCH — CU010DH2 — 77
+ POWER ON OR IPL RESET — CU010FL6 — 80
- Z BUS BIT 0.1 — DG974EH6 — 83
- Z BUS BIT 1.1 — DK974EH6 — 86

50 -22L, 77 ZD09 → AA CR → 21K 102

53 -18F, 65 ZJ05, 2 ZS04, 38 ZS03, 5 ZM12, 38 ZS03 → AD OR → 19D 109

56 -48W, 62 -46W, 17 ZS11, 47 ZP12 → BA A OR → P11 120

LOAD LATCH
149 13H, 80 14H → FM A OR → 14K 145; 14J 149

80 14L, 35 J07, 86 B13 → A

RESET MACHINE CHECK
74 -18H → AB AR OR → P11 159

11 ZB12, 38 ZS03, 32 -J12, 83 -J03 → A

RESET START LCH
17 ZS11, 20 ZS12, 44 -13H, 47 ZP12 → BB A → 12H 171

ACTIV INSN STEP OR CLK STEP
59 ZG05, 8 ZB07, 38 ZS03 → AE AR OR → B10 180

ACTIVE INSN STEP
8 ZB07, 38 ZS03 → AC A → M02 189

HARD STOP
71 -09N, 14 -B11, 29 -B09, 23 -G04, 68 -J10, 5 ZM12, 23 -G04, 38 ZS03, 26 ZG03, 59 ZG05, 102 -16D, 109 -18C → CH AR FL S S S S S S S S R → M03 203

MACH CK SET IPL
23 ZG04, 50 -44W, 109 -43W → AG A → 44T 239

LOAD TO IND
145 -38N, 41 ZP12 → BF A OR → M13 246

ALLOW CHANNELS ON LINE
65 ZB09, 203 ZM04 → BE A U → U04*304

HARD STOP TO IND
41 ZD10, 203 ZM04 → BD A OR U → P06 325

HARD STOP LCH
203 ZM04 → BC N → 28H 331

TIE UP
PWR -.75V → CF → 36D 480

189 - ACTIVE INSN STEP — CU004-BK6

159 + RESET MACHINE CHECK — CB6
LCK006 LCS002

180 - ACTIV INSN STEP OR CLK STEP — CK2
LCP005 LCS001 LCU014

480 + TIE UP — CU010-DC4

239 + MACH CK SET IPL — CU010-DJ2

203 - HARD STOP LCH — EC6
LCC008 LCP001 LCU007

171 + RESET START LCH — CU003-EF2

331 + HARD STOP LCH — CU004-FC2

325 + HARD STOP TO IND — AP010-GB2

304 - ALLOW CHANNELS ON LINE — AA003-GC6

246 + LOAD TO IND — AP009-GM2

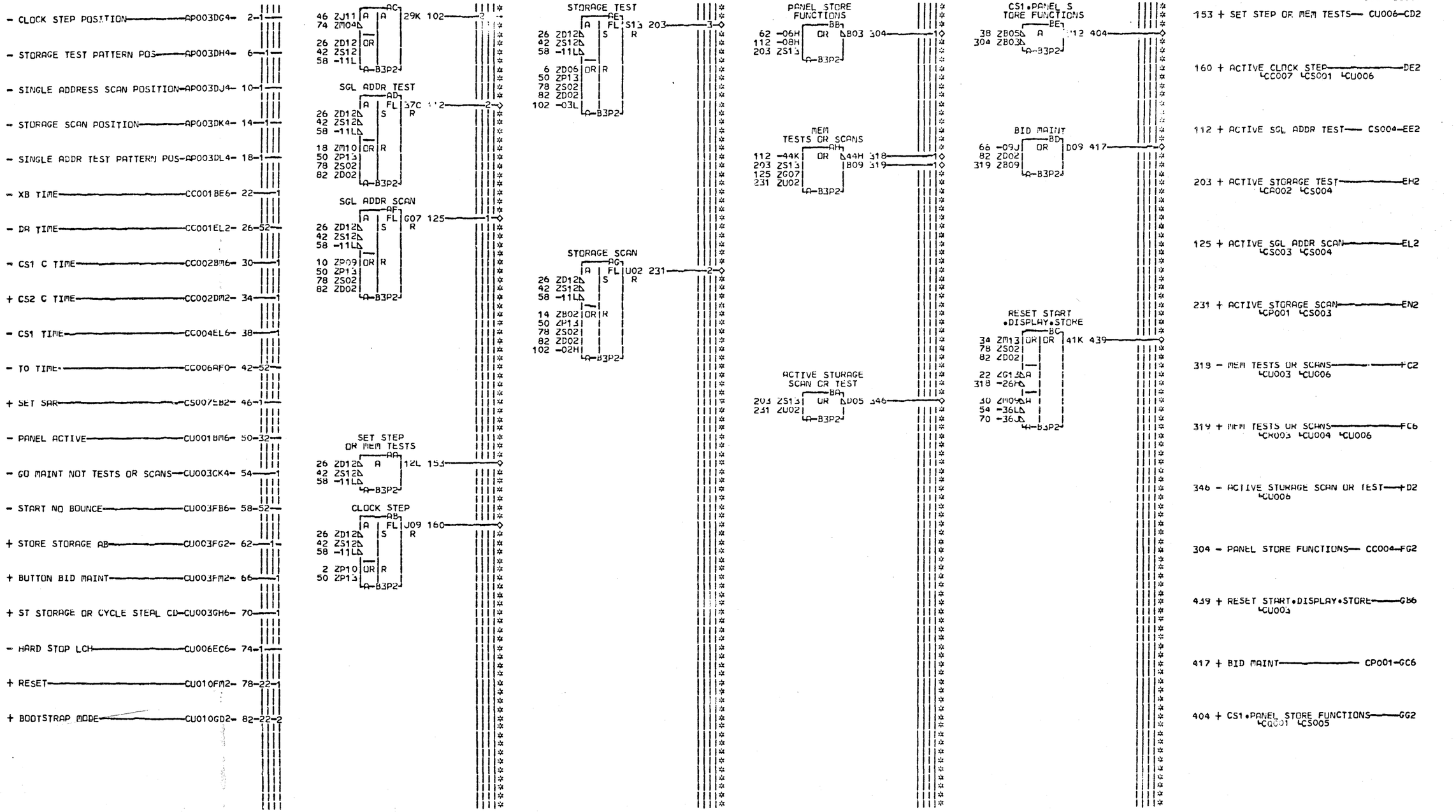
THIS PAGE IS FOR 3705-II ONLY.

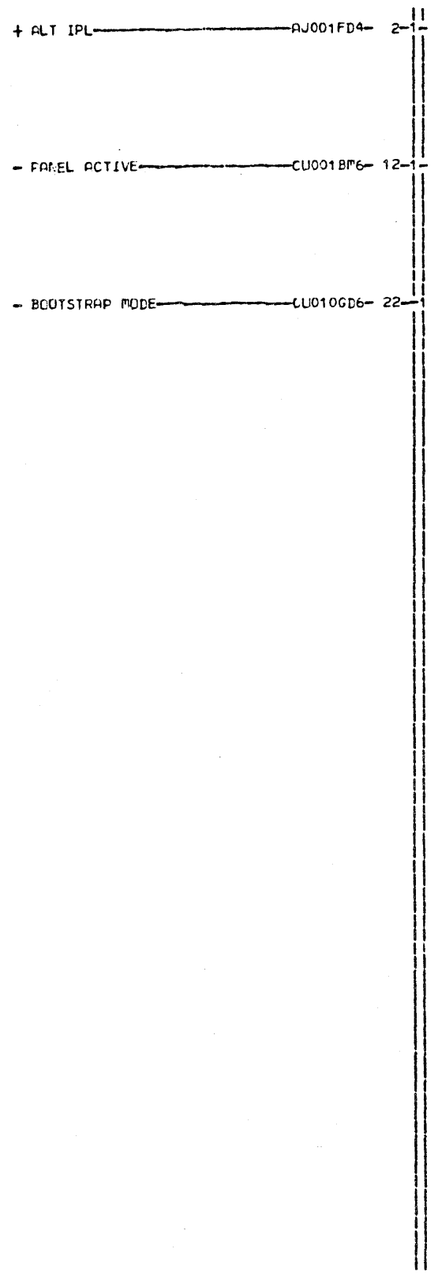
EDGE CONN.
20 RESISTOR
A-B3P2512
304 A-B3A5R02

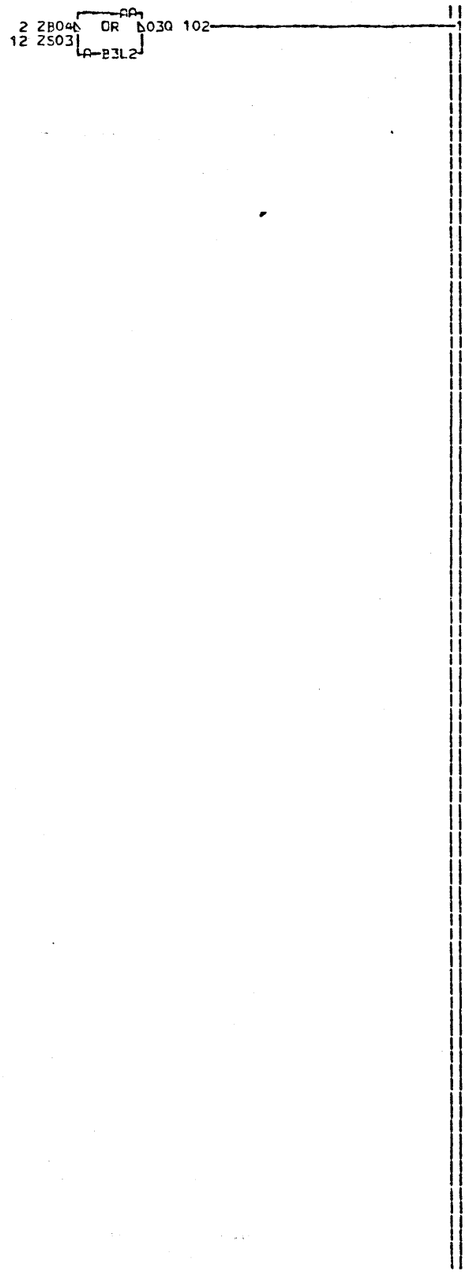
LOC. TYPE
A-B3L2 6823
A-B3P2 6820

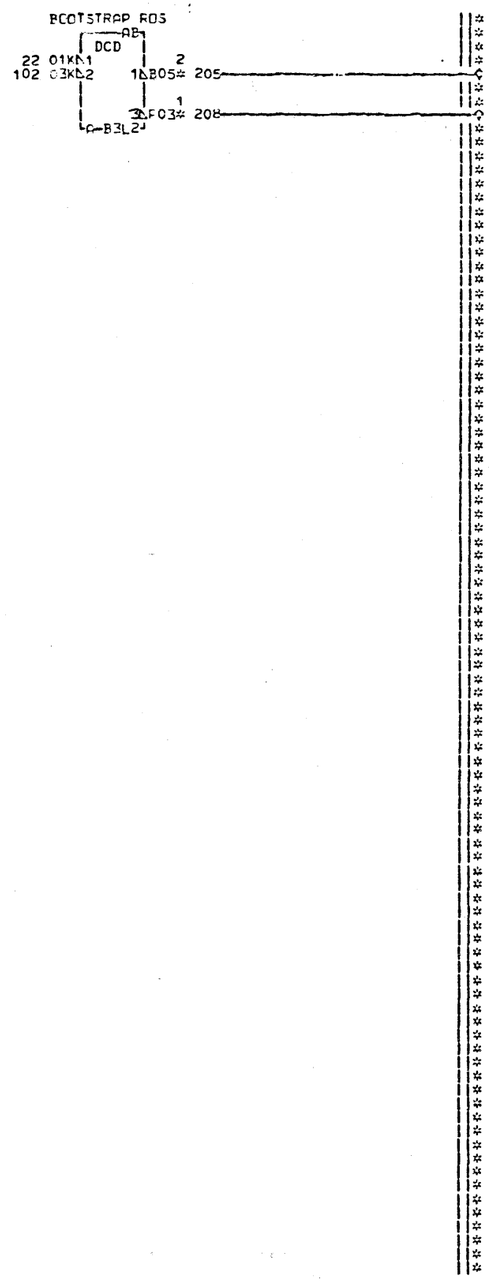
PANEL CONTROLS
-E.C.-HISTORY- -D- MACH.27RNB
312922
314419
FRAME 01
IBM CORP.SDD CU006
DATE LAST EC
10-06-76 315053
P.N. 1750193 030

CU006
030 SIM TO PN 5997643 EC 310268


- CLOCK STEP POSITION—AP003DG4- 2-1
- STORAGE TEST PATTERN POS—AP003DH4- 6-1
- SINGLE ADDRESS SCAN POSITION—AP003DJ4- 10-1
- STORAGE SCAN POSITION—AP003DK4- 14-1
- SINGLE ADDR TEST PATTERN POS—AP003DL4- 18-1
- XB TIME—CC001BE6- 22-1
- DA TIME—CC001EL2- 26-52
- CS1 C TIME—CC002BM6- 30-1
+ CS2 C TIME—CC002DM2- 34-1
- CS1 TIME—CC004EL6- 38-1
- TO TIME—CC006AF0- 42-52
+ SET SAR—CS007EB2- 46-1
- PANEL ACTIVE—CU001BM6- 50-32
- GO MAINT NOT TESTS OR SCANS—CU003CK4- 54-1
- START NO BOUNCE—CU003FB6- 58-52
+ STORE STORAGE AB—CU003FG2- 62-1
+ BUTTON BID MAINT—CU003FM2- 66-1
+ ST STORAGE OR CYCLE STEAL CD—CU003GH6- 70-1
- HARD STOP LCH—CU006EC6- 74-1
+ RESET—CU010FM2- 78-22-1
+ BOOTSTRAP MODE—CU010GD2- 82-22-2
SGL ADDR TEST
SGL ADDR SCAN
SET STEP OR MEM TESTS
CLOCK STEP
STORAGE TEST
STORAGE SCAN
PANEL STORE FUNCTIONS
MEM TESTS OR SCANS
ACTIVE STORAGE SCAN OR TEST
RESET START •DISPLAY•STORE
CS1•PANEL STORE FUNCTIONS
BID MAINT
153 + SET STEP OR MEM TESTS— CU006—CD2
160 + ACTIVE CLOCK STEP—DE2
112 + ACTIVE SGL ADDR TEST— CS004—EE2
203 + ACTIVE STORAGE TEST—EH2
125 + ACTIVE SGL ADDR SCAN—EL2
231 + ACTIVE STORAGE SCAN—EN2
318 - MEM TESTS OR SCANS—FC2
319 + MEM TESTS OR SCANS—FC6
346 - ACTIVE STORAGE SCAN OR TEST—D2
304 - PANEL STORE FUNCTIONS— CC004—FG2
439 + RESET START•DISPLAY•STORE—GB6
417 + BID MAINT— CP001—GC6
404 + CS1•PANEL STORE FUNCTIONS—GG2
LOC• TYPE
A-B3P2 6820
PANEL CONTROLS
E•C• HISTORY B
309521C
309545
309548
309553
DATE LAST EC
02-05-73 310268
MACH•3705IGAR
FRAME 01
IBM CORP•SDD
P•N• 5997644
CU007
000







000 CU009

208 - BOOTSTRAP ROS 1 ——— CU011-ED6

205 - BOOTSTRAP ROS 2 ——— CU001-ED6

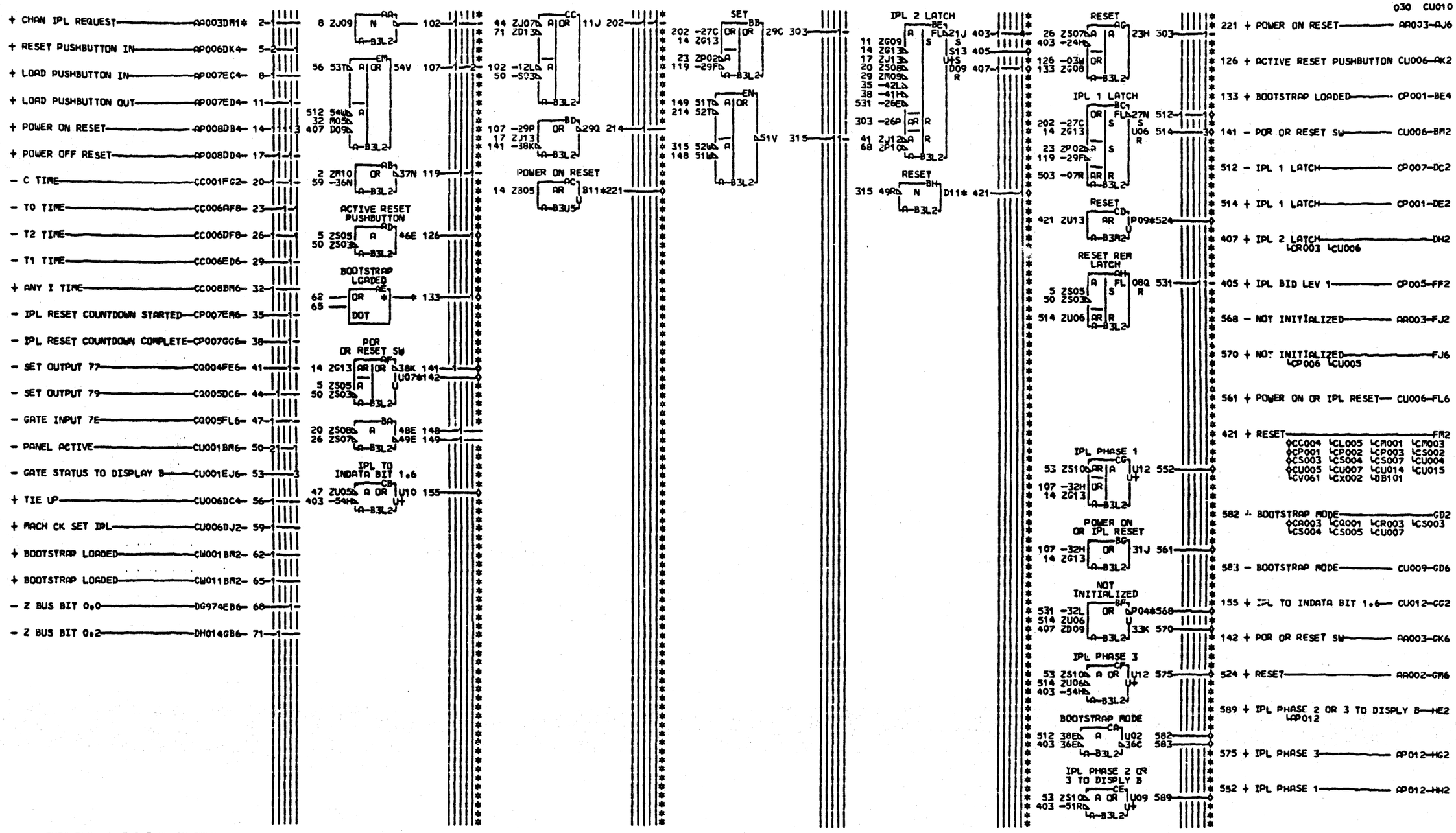
EDGE CONN.
205 A-E3W3D13
01A-B4V3D13
208 A-B3T1E11
01A-B4T6E02

LOC. TYPE
A-B3L2 6823

PANEL CONTROLS

—E.C.—HISTORY——	B MACH.27RNB
309521C	FRAME 01
	IBM CORP.SDD CU009
DATE LAST EC	P.N. 5997645 000
04-19-72 309545	

CU009
000



THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN. 01A-B4V6B02
2 RESISTOR 524 A-B3B1B11
A-B3L2M10 01A-B4B6B02
133 RESISTOR 01A-B4V5D07
A-B3L2G08 568 A-B3A5B04
01A-B3T1E13
01A-B4T6E04
142 A-B3A5B05
221 A-B3A5D07
421 A-B3V1B11

LOC. TYPE
A-B3L2 6823
A-B3M2 6818
A-B3U5 AB85

PANEL CONTROLS
E.C.-HISTORY
312922
314419
DATE LAST EC
10-06-76 315053
MACH.27RNB
FRAME 01
IBM CORP.SDD
P.N. 1750194
CU010
030

CU010
030 SIM TO PN 5997646 EC 310268

+ FORCE INDATA X.6 — AJ001FK4 — 2
+ FORCE INDATA X.7 — AJ001FM4 — 3

+ HEXSW BYTE 0 BIT 0 — AP001DJ2 — 5
+ HEXSW BYTE 0 BIT 1 — AP001DK2 — 6

+ HEXSW BYTE 0 BIT 2 — AP001DL2 — 8
+ HEXSW BYTE 0 BIT 3 — AP001DM2 — 9

+ HEXSW BYTE 0 BIT 4 — AP001GE2 — 11
+ HEXSW BYTE 0 BIT 5 — AP001GF2 — 12

+ HEXSW BYTE 0 BIT 6 — AP001GG2 — 14
+ HEXSW BYTE 0 BIT 7 — AP001GH2 — 15

UNUSED — CG001CB2 — 17
UNUSED — CG001CC2 — 18

+ MACH CK TO INDATA BIT 0.0 — CK004EB2 — 20
+ MACH CK TO INDATA BIT 0.1 — CK004EC2 — 21

+ MACH CK TO INDATA BIT 0.2 — CK004ED2 — 23
+ MACH CK TO INDATA BIT 0.4 — CK004EF2 — 24

+ MACH CK TO INDATA BIT 0.5 — CK004EG2 — 26
+ MACH CK TO INDATA BIT 0.6 — CK004EH2 — 27

+ MACH CK TO INDATA BIT 0.7 — CK004EJ2 — 29
+ L1 PROG CHECK TO INDATA 0.3 — CK005EG2 — 30

- UNUSED — CM002FC2 — 32
- UNUSED — CM002FD2 — 33

+ MEM SIZE TO INDATA 0.4 — CM002FE2 — 35
+ MEM SIZE TO INDATA 0.5 — CM002FF2 — 36

+ MEM SIZE TO INDATA 0.6 — CM002FG2 — 38
ALWAYS MINUS — CM002FH2 — 39

- UNUSED — CM002GC6 — 41
+ IRPT ADDR TO INDATA BIT 0.7 — CP002GB2 — 42

+ CRC TO INDATA BIT 0.0 — CR006AF6 — 44
+ CRC TO INDATA BIT 0.1 — CR006CF6 — 45

+ CRC TO INDATA BIT 0.4 — CR006DF6 — 47
+ CRC TO INDATA BIT 0.6 — CR006DG6 — 48

+ CRC TO INDATA BIT 0.2 — CR006EF6 — 50
+ CRC TO INDATA BIT 0.7 — CR006EG6 — 51

+ CRC TO INDATA BIT 0.5 — CR006EH6 — 53
+ CRC TO INDATA BIT 0.3 — CR006GF6 — 54

+ HEWSX BYTE X BIT 6 — CR008BD2 — 56
+ HEWSX BYTE X BIT 7 — CR008DE2 — 57

+ FUNC SW TO INDATA BIT 0.4 — CU003FH2 — 59
+ FUNC SW TO INDATA BIT 0.3 — CU003FJ2 — 60

+ PCI L2 TO INDATA 0.0 — CU014CM2 — 62
+ INTERRUPT KEY TO INDATA 0.6 — CU014GM2 — 63

+ 2ND T.P. TO INDATA BIT 0.7 — CU015CF2 — 65
+ BAR TO INDATA BIT 0.4 — CX009GA2 — 66

+ BAR TO INDATA BIT 0.6 — CX009GB2 — 68
+ BAR TO INDATA BIT 0.7 — CX009GC2 — 69

+ LEV 5 Z COND TO INDATA 0.7 — CZ004EK2 — 71
+ LEV 5 C COND TO INDATA 0.6 — CZ005EK2 — 72

+ LAR TO INDATA BIT X.6 — DF002EB2 — 74
+ LAR TO INDATA BIT X.7 — DF002ED2 — 75

+ LAR TO INDATA BIT 0.0 — DG002EB2 — 77
+ LAR TO INDATA BIT 0.1 — DG002ED2 — 78

+ LAR TO INDATA BIT 0.2 — DH002EB2 — 80
+ LAR TO INDATA BIT 0.3 — DH002ED2 — 81

+ LAR TO INDATA BIT 0.4 — DH002EF2 — 83
+ LAR TO INDATA BIT 0.5 — DJ002EB2 — 84

+ LAR TO INDATA BIT 0.6 — DJ002ED2 — 86
+ LAR TO INDATA BIT 0.7 — DJ002EF2 — 87

THIS PAGE IS FOR 3705-II ONLY.

CU011
030 SIM TO PN 5997647 EC 312926

CCU INDATA BIT 0.7 (DA) — OR — DOT — * 104
CCU INDATA BIT 0.6 (DN) — OR — DOT — * 119
CCU INDATA BIT 0.5 (DL) — OR — DOT — * 131
CCU INDATA BIT 0.4 (DK) — OR — DOT — * 142
CCU INDATA BIT 0.3 (DJ) — OR — DOT — * 154
CCU INDATA BIT 0.2 (DG) — OR — DOT — * 166
CCU INDATA BIT 0.1 (DE) — OR — DOT — * 177
FLOAT UNUSED (DR) — OR — DOT — 187

CCU INDATA BIT 0.0 (DD) — OR — DOT — * 204
CCU INDATA BIT X.7 (DC) — OR — DOT — * 215
CCU INDATA BIT X.6 (DA) — OR — DOT — * 224
FLOAT UNUSED (DQ) — OR — DOT — 232

224 + CCU INDATA BIT X.6 — DA4 (CG001 DF971)
215 + CCU INDATA BIT X.7 — DC4 (CU013 DF971)
204 + CCU INDATA BIT 0.0 — DD4 (CU013 DG971)
177 + CCU INDATA BIT 0.1 — DE4 (CU013 DG971)
166 + CCU INDATA BIT 0.2 — DG4 (CU013 DH011)
154 + CCU INDATA BIT 0.3 — DJ4 (CU013 DH011)
142 + CCU INDATA BIT 0.4 — DK4 (CU013 DH011)
131 + CCU INDATA BIT 0.5 — DL4 (CU013 DJ011)
104 + CCU INDATA BIT 0.7 — DM4 (CU013 DJ011)
118 + CCU INDATA BIT 0.6 — DN4 (CU013 DJ011)
232 + FLOAT UNUSED — CG001-DQ4
187 + FLOAT UNUSED — CG001-DR4

EDGE CONN.
104 A-B3T6E02 — 01A-B4T1E11
118 A-B3T5E04 — 01A-B4T1E13
131 A-B3T5F04 — 01A-B4T1D13
142 A-B3T5D02 — 01A-B4T1D11
154 A-B3T5C04 — 01A-B4T1C13
166 A-B3T5B02 — 01A-B4T1311
177 A-B3T5A04 — 01A-B4T1A13
204 A-B3T6A02 — 01A-B4T1A11
215 A-B3S5A02 — 01A-B4S1A11
224 A-B3R5E04 — 01A-B4R1E13

LOC. TYPE

CC INDATA BUS
E.C.-HISTORY — D
312922
314419
DATE LAST EC
08-31-76 315053
MACH.27RNB
FRAME 01
IBM CORP.SDD
P.N. 1750195
CU011
030

+ HEXSW BYTE 1 BIT 0 — AP002EB2 — 2-1
+ HEXSW BYTE 1 BIT 1 — AP002EC2 — 3-1
+ HEXSW BYTE 1 BIT 2 — AP002ED2 — 4-1
+ HEXSW BYTE 1 BIT 3 — AP002EE2 — 5-1
+ HEXSW BYTE 1 BIT 4 — AP002EH2 — 6-1
+ HEXSW BYTE 1 BIT 5 — AP002EJ2 — 7-1
+ HEXSW BYTE 1 BIT 6 — AP002EK2 — 8-1
+ HEXSW BYTE 1 BIT 7 — AP002EL2 — 9-1
+ FUNC SEL TO INDATA BIT 1.6 — AP005EG2 — 10-1

+ FUNC SEL TO INDATA BIT 1.5 — AP005EH2 — 12-1
+ FUNC SEL TO INDATA BIT 1.4 — AP005EJ2 — 13-1
+ FUNC SEL TO INDATA BIT 1.3 — AP005EK2 — 14-1
+ FUNC SEL TO INDATA BIT 1.2 — AP005EL2 — 15-1
+ FUNC SEL TO INDATA BIT 1.1 — AP005EM2 — 16-1
+ FET MEM INSTALL INDATA 1.4 — CG001ED2 — 17-1
+ CCU CYC CLK CK TO INDATA 1.0 — CK005EC2 — 18-1
+ CCU CHECK TO INDATA BIT 1.5 — CK005EH2 — 19-1
+ CSB CLK CK TO INDATA BIT 1.6 — CK005EK2 — 20-1

+ CCU CLK CK TO INDATA BIT 1.7 — CK005EL2 — 22-1
+ IRPT ADDR TO INDATA BIT 1.3 — CP002GA2 — 23-1
+ IRPT ADDR TO INDATA BIT 1.0 — CP002GF2 — 24-1
+ PRG STAT TO INDATA BIT 1.0 — CP004DB2 — 25-1
+ PRG STAT TO INDATA BIT 1.1 — CP004DD2 — 26-1
+ PRG STAT TO INDATA BIT 1.2 — CP004DF2 — 27-1
+ PRG STAT TO INDATA BIT 1.3 — CP004DH2 — 28-1
+ CRC TO INDATA BIT 1.0 — CR007BF6 — 29-1
+ CRC TO INDATA BIT 1.4 — CR007BF6 — 30-1

+ CRC TO INDATA BIT 1.1 — CR007CF6 — 32-1
+ CRC TO INDATA BIT 1.3 — CR007DF6 — 33-1
+ CRC TP 1 CA TO INDATA 1.6 — CR007DG6 — 34-1
+ CRC TO INDATA BIT 1.2 — CR007EF6 — 35-1
+ CRC TO INDATA BIT 1.5 — CR007FF6 — 36-1
+ CRC TO INDATA BIT 1.7 — CR007GF6 — 37-1
+ IRPT ADDR TO INDATA BIT 1.6 — CS004FD6 — 38-1
+ IRPT ADDR TO INDATA BIT 1.3 — CS004HS2 — 39-1
+ COMPARE FORCE INDATA BIT 1.7 — CS006CC2 — 40-1

+ IPL TO INDATA BIT 1.6 — CU010GG2 — 42-1
+ I-O CK TO INDATA BIT 1.2 — CU014CC2 — 43-1
+ ADDR COMPARE TO INDATA 1.0 — CU014CF2 — 44-1
+ 1ST TAP. TO INDATA BIT 1.1 — CU014CJ2 — 45-1
+ INVALID OP TO INDATA 1.4 — CU014GC2 — 46-1
+ INT TIMER TO INDATA 1.5 — CU014GJ2 — 47-1
+ 2ND TAP. TO INDATA BIT 1.6 — CU015CC2 — 48-1
+ 2ND TAP. TO INDATA BIT 1.7 — CU015CJ2 — 49-1
+ STORE KEY TO INDATA BIT 1.5 — CV051FA2 — 50-1

+ STORE KEY TO INDATA BIT 1.6 — CV051FC2 — 52-1
+ STORE KEY TO INDATA BIT 1.7 — CV051FE2 — 53-1
+ PROT KEY TO INDATA BIT 1.5 — CV051FG2 — 54-1
+ PROT KEY TO INDATA BIT 1.6 — CV051FJ2 — 55-1
+ PROT KEY TO INDATA BIT 1.7 — CV051FL2 — 56-1
+ PROTECT CHK TO INDATA 1.3 — CV061GF2 — 57-1
+ ALT ROS TO INDATA DOT 1.0 — CW001GB2 — 58-1
+ ALT ROS TO INDATA DOT 1.1 — CW001GC2 — 59-1
+ ALT ROS TO INDATA DOT 1.2 — CW001GD2 — 60-1

+ ALT ROS TO INDATA DOT 1.3 — CW001GE2 — 62-1
+ ALT ROS TO INDATA DOT 1.4 — CW001GG2 — 63-1
+ ALT ROS TO INDATA DOT 1.5 — CW001GH2 — 64-1
+ ALT ROS TO INDATA DOT 1.6 — CW001GJ2 — 65-1
+ ALT ROS TO INDATA DOT 1.7 — CW001GK2 — 66-1
+ ROS TO INDATA DOT BIT 1.0 — CW011GB6 — 67-1
+ ROS TO INDATA DOT BIT 1.1 — CW011GC6 — 68-1
+ ROS TO INDATA DOT BIT 1.2 — CW011GD6 — 69-1
+ ROS TO INDATA DOT BIT 1.3 — CW011GE6 — 70-1

+ ROS TO INDATA DOT BIT 1.4 — CW011GG6 — 72-1
+ ROS TO INDATA DOT BIT 1.5 — CW011GH6 — 73-1
+ ROS TO INDATA DOT BIT 1.6 — CW011GJ6 — 74-1
+ ROS TO INDATA DOT BIT 1.7 — CW011GK6 — 75-1
+ BAR TO INDATA BIT 1.0 — CX009GD2 — 76-1
+ BAR TO INDATA BIT 1.1 — CX009GF2 — 77-1
+ BAR TO INDATA BIT 1.2 — CX009GG2 — 78-1
+ BAR TO INDATA BIT 1.3 — CX009GH2 — 79-1
+ BAR TO INDATA BIT 1.4 — CX009GK2 — 80-1

+ BAR TO INDATA BIT 1.5 — CX009GL2 — 82-1
+ BAR TO INDATA BIT 1.6 — CX009GM2 — 83-1
+ LAR TO INDATA BIT 1.0 — DK002EB2 — 84-1
+ LAR TO INDATA BIT 1.1 — DK002ED2 — 85-1
+ LAR TO INDATA BIT 1.2 — DL002EB2 — 86-1
+ LAR TO INDATA BIT 1.3 — DL002ED2 — 87-1
+ LAR TO INDATA BIT 1.4 — DL002EF2 — 88-1
+ LAR TO INDATA BIT 1.5 — DM002EB2 — 89-1
+ LAR TO INDATA BIT 1.6 — DM002ED2 — 90-1

+ LAR TO INDATA BIT 1.7 — DM002EF2 — 92-1

THIS PAGE IS FOR 3705-II ONLY.

CU012

030 SIM TO PN 5997648 EC 310248

CCU INDATA BIT 1.1 — DD — CR — DOT
3, 16, 45, 26, 85, 77, 68, 32, 59 → 104

CCU INDATA BIT 1.0 — DC — CR — DOT
2, 84, 24, 18, 44, 25, 76, 67, 29, 58 → 118

CCU INDATA BIT 1.7 — DN — CR — DOT
9, 92, 22, 40, 53, 56, 49, 75, 37, 66 → 133

CCU INDATA BIT 1.6 — DL — CR — DOT
8, 10, 90, 42, 20, 38, 52, 55, 48, 74, 83, 34, 65 → 148

CCU INDATA BIT 1.5 — DK — CR — DOT
64, 7, 12, 99, 19, 36, 50, 54, 47, 73, 82 → 166

CCU INDATA BIT 1.4 — DJ — CR — DOT
6, 63, 13, 88, 46, 72, 30, 80, 17 → 182

EDGE CONN. 01A-B3U8C02
104 A-B3U8C02 01A-B4U1C11
118 A-B3U6804 01A-B4U1313
133 A-B3V5802 01A-B4V1311
148 A-B3V6A04 01A-B4V1A13
166 A-B3U5E02
182 A-B3U6D04 01A-B4U1D13
204 A-B3U5D02 01A-B4U1D11
220 A-B3U6C04 01A-B4U1C13

LOC. TYPE

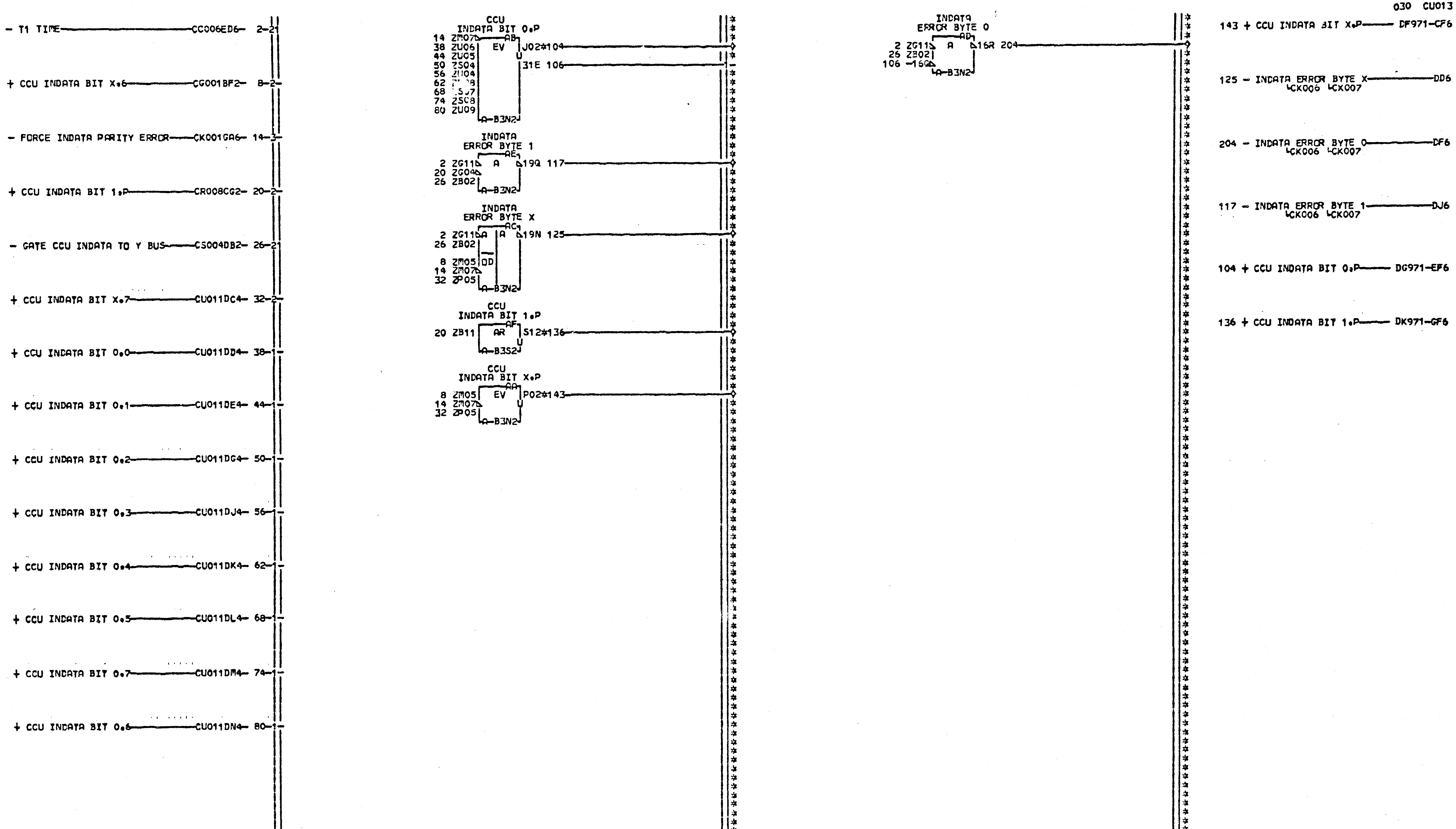
CCU INDATA BIT 1.3 — DG — CR — DOT
62, 5, 14, 87, 28, 33, 23, 57, 70, 39, 79 → 204

CCU INDATA BIT 1.2 — DE — CR — DOT
4, 15, 86, 43, 27, 78, 69, 35, 60 → 220

118 + CCU INDATA BIT 1.0 — DC4 (CR008 DK971)
104 + CCU INDATA BIT 1.1 — DD4 (CR008 DK971)
220 + CCU INDATA BIT 1.2 — DE4 (CR008 DL001)
204 + CCU INDATA BIT 1.3 — DG4 (CR008 DL001)
182 + CCU INDATA BIT 1.4 — DJ4 (CR008 DL001)
166 + CCU INDATA BIT 1.5 — DK4 (CR008 DM001)
148 + CCU INDATA BIT 1.6 — DL4 (CR008 DM001)
133 + CCU INDATA BIT 1.7 — DN4 (CR008 DM001)

CC INDATA BUS

E.C.—HISTORY—D	MACH.27RNB
312922	
314419	FRAME 01
DATE LAST EC	IBM CORP.SDD CU012
10-06-76 315053	P.N. 1750196 030



- T1 TIME CC006ED6 2-2
+ CCU INDATA BIT X.6 CG001BF2 8-2
- FORCE INDATA PARITY ERROR CK001GA6 14-3
+ CCU INDATA BIT 1.P CR008CG2 20-2
- GATE CCU INDATA TO Y BUS CS004DB2 26-2
+ CCU INDATA BIT X.7 CU011DC4 32-2
+ CCU INDATA BIT 0.0 CU011DD4 38-1
+ CCU INDATA BIT 0.1 CU011DE4 44-1
+ CCU INDATA BIT 0.2 CU011DG4 50-1
+ CCU INDATA BIT 0.3 CU011DJ4 56-1
+ CCU INDATA BIT 0.4 CU011DK4 62-1
+ CCU INDATA BIT 0.5 CU011DL4 68-1
+ CCU INDATA BIT 0.7 CU011DM4 74-1
+ CCU INDATA BIT 0.6 CU011DN4 80-1
CCU INDATA BIT 0.P
EV AB J02 104 31E 106 A-B3N2
INDATA ERROR BYTE 1
A AE 19Q 117 A-B3N2
INDATA ERROR BYTE X
A AC 19N 125 DD A-B3N2
CCU INDATA BIT 1.P
AR AF S12 136 A-B3S2
CCU INDATA BIT X.P
EV AA P02 143 A-B3N2
INDATA ERROR BYTE 0
A AD 16R 204 A-B3N2
143 + CCU INDATA BIT X.P DF971-CF6
125 - INDATA ERROR BYTE X CK006 CK007 DD6
204 - INDATA ERROR BYTE 0 CK006 CK007 DF6
117 - INDATA ERROR BYTE 1 CK006 CK007 DJ6
104 + CCU INDATA BIT 0.P DG971-EF6
136 + CCU INDATA BIT 1.P DK971-GF6

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
104 A-B3U6A02
01A-B4U1A11
136 A-B3V6B04
01A-B4V1B13
143 A-B3S6A04
01A-B4S1A13

LOC. TYPE
A-B3N2 6819
A-B3S2 Y703

CU013
030 SIM TO PN 5997649 EC 310268

CCU INDATA BUS
E.C.-HISTORY 312922 314419
MACH.27RNB
FRAME 01
IBM CORP.SDD
DATE 08-31-76 LAST EC 315053
P.N. 1750197

+ INTERRUPT PUSHBUTTON IN — AP007EA4 — 2
+ INTERRUPT PUSHBUTTON OUT — AP007EB4 — 5
- XB TIME — CC001BE6 — 8
- CD TIME — CC001EJ2 — 11
- CYCLE STEAL AB — CC008AE6 — 14
+ LEV 5 I-O INST ATTEMPT — CD003HB2 — 17
- INVALID OP DECODE — CK007BC4 — 20
+ PROG LEV 1 CURRENT — CP003DD2 — 23
- SET INT TIMER BID — CP007CG6 — 26
+ EVEN PARITY ON INPUT DATA — CQ001BF2 — 29
+ ADAPTER I-O ADDR DECODE ERR — CQ001BG2 — 32
- SET OUTPUT 77 — CQ004FE6 — 35
- GATE INPUT 7E — CQ005FL6 — 38
- GATE INPUT 7F — CQ005FM6 — 41
- ADDRESS EXCEPTION — CS002BK4 — 44
- ANY I TIME + 125 NS — CS006AD6 — 47
- PANEL ACTIVE — CU001BM6 — 50
- GATE STATUS TO DISPLAY B — CU001EJ6 — 53
- SET TRACE LATCH IF OK — CU004FH6 — 56
- PROGRAM STOP LATCH — CU004FK6 — 59
- TEST MODE LCH — CU005CB2 — 62
- ACTIV INSN STEP OR CLK STEP — CU006CK2 — 65
+ RESET — CU010FM2 — 68
- Z BUS BIT 0.2 — DH014G36 — 71
- Z BUS BIT 0.6 — DJ014GF6 — 74
- Z BUS BIT 0.7 — DJ014GK6 — 77
- Z BUS BIT 1.1 — DK974EH6 — 80
- Z BUS BIT 1.4 — DL004GK6 — 83
- Z BUS BIT 1.5 — DM004GB6 — 86

303 18R6 A CR 16R 102

26 G08 A
209 16P6

5 ZG05 A CR 16L 116

41 -G03 A

ALLOW IRPT BF FL10 129
2 ZG11 AR S R
50 ZS03 CR R
68 ZD11
35 ZJ12 A R
71 ZD13

SET I-O CHECK BB
29 OR * 141
32
17
DOT

47 -31N A CR AH
11 ZU06

8 -P10 A P04 155
14 -P07

ADDR COMPARE AB FL24D 180
23 -23P A S S
56 ZG10 D03 182
68 -U13 AR R U+S
35 ZU07 A R
83 ZM07

102 -14H OR G09 202
41 ZG03
59 Z31E
65 ZJ03

102 14H N 14K 209

INTERRUPT KEY CA FL21F 217
116 -21E A S S
129 ZJ06 22L 219
68 -U13 OR R +S
129 -J06

I-O CHECK AA FL41F 231
141 ZM11 AR S S 2
68 -D11 S13 233
35 ZJ12 A R U+S
86 ZD12

SET I-O CHECK BC
141 ZM11 N 01U 245

ADDR EXCEPTION AC FL44C 252
155 ZM07 A S S 2
44 ZP07 S13 254
68 -D11 U+S
35 ZJ12 A R
86 ZD12

INVALID OP BG FL44D 264
155 ZM07 A S S 2
20 ZS02 S13 266
68 -D11 U+S
35 ZJ12 A R
86 ZD12

ADDR COMPARE TO INDATA 1.0 AP
38 ZJ11 A CR U12 281
180 -26L U+

PCI L2 TO INDATA 0.0 BA
41 ZG03 A CR P02 288
380 -26H U+

INTERVAL TIMER BH FL24C 303
26 ZG08 A S S
202 ZG09 06P 305
68 -U13 AR R S
35 ZU07 A R
80 ZU04

INTERRUPT KEY TO INDATA 0.6 CD
41 ZG03 A CR U02 318
217 -21H U+

I-O CHK TO DISPLAY B CE
53 ZS10 A CR S09 332
231 -51F U+

I-O CK TO INDATA BIT 1.2 AE
38 ZU05 A CR U13 339
231 -54C U+

ADDR EXCEPT TO DISPLAY B CG
53 ZS10 A CR S12 353
252 -52C U+

INVALID OP TO DISPLAY B CF
53 ZS10 A CR P13 360
264 -54F U+

INVALID OP TO INDATA 1.4 CB
38 ZU05 A CR U04 367
264 -46L U+

1ST Y.P. TO INDATA BIT 1.1 AG
38 ZU05 A CR U03 374
252 -46H U+

PCI BID LEV 2 AD FL49J 380
35 ZU07 A S S
74 ZS07 06Q 382
62 ZJ07 CR R S
68 -U13
35 ZU07 A R
77 ZM10

INT TIMER TO INDATA 1.5 CC
41 ZG03 A CR G07 404
305 -06P U+

155 - SAMPLE EXCEPTION — CK007-AH6

380 - DIAGNOSTIC BID PROG LEV 2 — BL6
↳CP005

233 + I-O CHECK BID PROG LEV 1 — CB2
↳CP005

339 + I-O CK TO INDATA BIT 1.2 — CC2
↳CU012

182 + ADDR COMPARE BID PROG LEV 1 — CE2
↳CP005

281 + ADDR COMPARE TO INDATA 1.0 — CF2
↳CU012

254 + ADDR EXCEPTION BID PROG LEV1 — CH2
↳CP005

374 + 1ST Y.P. TO INDATA BIT 1.1 — CJ2
↳CU012

382 + DIAG PCI BID PROG LEV 2 CP005 — CL2

288 + PCI L2 TO INDATA 0.0 — CU011-CM2

141 + SET I-O CHECK — CC008-DA4

245 - SET I-O CHECK — CK007-EB2

266 + INVALID OP BID PROG LEV 1 — GB2
↳CP005

367 + INVALID OP TO INDATA 1.4 — GC2
↳CU012

305 + INTERVAL TIMER BID PROG LEV3 — GH2
↳CP005

404 + INT TIMER TO INDATA 1.5 CU012 — GJ2

219 + INTERRUPT KEY BID PROG LEV 3 — GL2
↳CP005

318 + INTERRUPT KEY TO INDATA 0.6 — GM2
↳CU011

332 + I-O CHK TO DISPLAY B — AP013-GQ2

360 + INVALID OP TO DISPLAY B AP014-GR2

353 + ADDR EXCEPT TO DISPLAY B — GS2
↳AP013

LOC. TYPE
A-B3F2 6810
A-B3L2 6823
A-B3M2 6818

THIS PAGE IS FOR 3705-II ONLY.

CU014
030 SIM TO PN 5997650 EC 310262

PANEL CONTROL

E.C. HISTORY		MACH.27RNB	
312922			
314419		FRAME 01	
		IBM CORP. SDD	CU014
DATE	LAST EC	P.N. 1750263	030
10-06-76	315053		

- OP XXXX XXXX X1CO XXXX——CA003BB7— 2
- OP XXXX XX X101 XXXX——CA003BB8— 9
- EXIT I1XC.TO TIME——CP003AC6— 16
- PROG LEV 5 CURRENT——CP003EM6— 23
- SET OUTPUT 77——CQ004FE6— 30-3
- SET OUTPUTS 7B TO 7F——CQ005BA6— 37-2
- SET OUTPUT 79——CQ005DC6— 44-2
- GATE INPUT 7F——CQ003FM6— 51-3
+ RESET——CU010FM2— 58-3
- Z BUS BIT 1.2——DL004GB6— 65-1
- Z BUS BIT 1.6——DM004GF6— 72-2
- Z BUS BIT 1.7——DM004GK6— 79-2

THIS PAGE IS FOR 3705-II ONLY.

SVC BID LEV 4
AC
16 -S3E
23 ZS12
58 -U1
30 ZU0
79 ZS08
A FLA54D 103
S S
OR R
R
A-B3M2
C8T 105
+S

PCI BID LEV 4
AB
9 ZU10
37 ZS09
58 -U13
30 ZU07
72 ZS05
A FLA51F 115
S S
RR R
R
A-B3M2
23N 117
+S

PCI BID LEV 3
AA
2 ZS11
37 ZS09
58 -U13
30 ZU07
65 ZP12
A FLA54C 127
S S
RR R
R
A-B3M2
21P 129
+S

DIAG SCOPE
SYNC POINT 2
AH
44 ZM12
79 ZS08
A
A-B3M2
P13 140

DIAG SCOPE
SYNC POINT 1
AG
44 ZM12
72 ZS05
A
A-B3M2
P10 147

2ND T.P. TO
INDATA BIT 1.7
AF
51 ZG03
103 -C6V
A OR
U+
A-B3M2
S13 204

2ND T.P. TO
INDATA BIT 0.7
AE
51 ZG03
115 -2AR
A OR
U+
A-B3M2
J09 211

2ND T.P. TO
INDATA BIT 1.6
AD
51 ZG03
127 -24N
A OR
U+
A-B3M2
J10 218

127 - PCI 3——CP005-BB6

129 + PCI BID PROG LEV 3——CP005-CB2

218 + 2ND T.P. TO INDATA BIT 1.6——CC2
CU012

117 + PCI BID PROG LEV 4——CP005-CE2

211 + 2ND T.P. TO INDATA BIT 0.7——CF2
CU011

105 + SVC BID PROG LEV 4——CP005-CH2

204 + 2ND T.P. TO INDATA BIT 1.7——CJ2
CU012

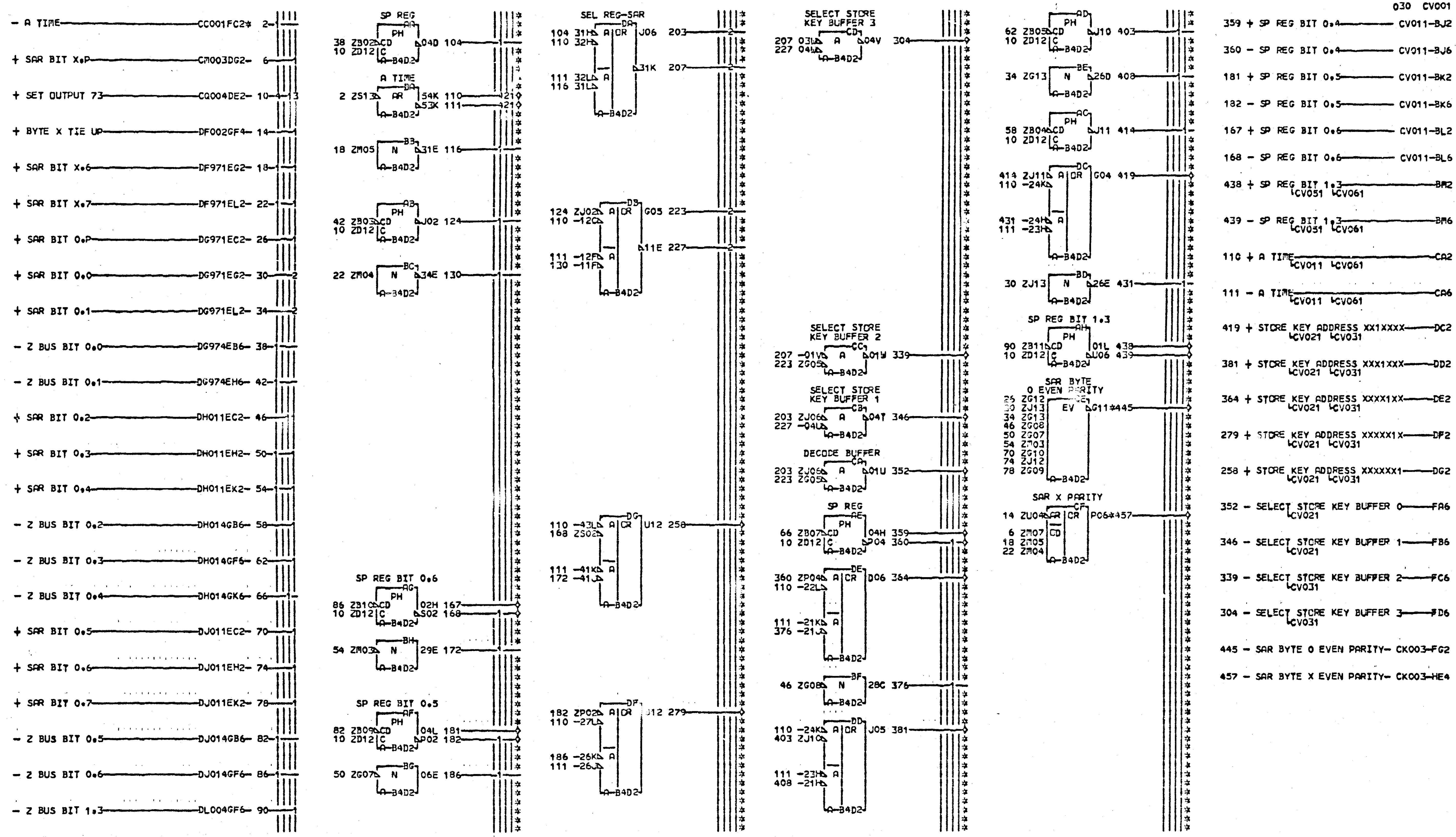
147 + DIAG SCOPE SYNC POINT 1 AA005-CL2

140 + DIAG SCOPE SYNC POINT 2 AA005-CN2

LOC. TYPE
A-B3M2 6818

PANEL CONTROL
E.C.-HISTORY——D-MACH.27RNB
312922
314419
FRAME 01
DATE LAST EC
10-06-76 315033
IBM CORP.SDD
P.N. 1750198
CU015
030

CU015
030 SIM TO PN 5997651 EC 310268



- A TIME CC001FC2# 2
+ SAR BIT X.P CM003DG2- 6
+ SET OUTPUT 73 CQ004DE2- 10
+ BYTE X TIE UP DF002GF4- 14
+ SAR BIT X.6 DF971EG2- 18
+ SAR BIT X.7 DF971EL2- 22
+ SAR BIT 0.P DG971EC2- 26
+ SAR BIT 0.0 DG971EG2- 30
+ SAR BIT 0.1 DG971EL2- 34
- Z BUS BIT 0.0 DG974EB6- 38
- Z BUS BIT 0.1 DG974EH6- 42
+ SAR BIT 0.2 DH011EC2- 46
+ SAR BIT 0.3 DH011EH2- 50
+ SAR BIT 0.4 DH011EK2- 54
- Z BUS BIT 0.2 DH014GB6- 58
- Z BUS BIT 0.3 DH014GF6- 62
- Z BUS BIT 0.4 DH014GK6- 66
+ SAR BIT 0.5 DJ011EC2- 70
+ SAR BIT 0.6 DJ011EH2- 74
+ SAR BIT 0.7 DJ011EK2- 78
- Z BUS BIT 0.5 DJ014GB6- 82
- Z BUS BIT 0.6 DJ014GF6- 86
- Z BUS BIT 1.3 DL004GF6- 90
SP REG
A TIME
SP REG BIT 0.6
SP REG BIT 0.5
SEL REG-SAR
SELECT STORE KEY BUFFER 3
SELECT STORE KEY BUFFER 2
SELECT STORE KEY BUFFER 1
DECODE BUFFER
SP REG
SP REG BIT 1.3
SAR BYTE 0 EVEN PARITY
SAR X PARITY
359 + SP REG BIT 0.4 CV011-BJ2
360 - SP REG BIT 0.4 CV011-BJ6
181 + SP REG BIT 0.5 CV011-BK2
182 - SP REG BIT 0.5 CV011-BK6
167 + SP REG BIT 0.6 CV011-BL2
168 - SP REG BIT 0.6 CV011-BL6
438 + SP REG BIT 1.3 CV051 CV061 BM2
439 - SP REG BIT 1.3 CV051 CV061 BM6
110 + A TIME CV011 CV061 CA2
111 - A TIME CV011 CV061 CA6
419 + STORE KEY ADDRESS XX1XXXX CV021 CV031 DC2
381 + STORE KEY ADDRESS XXX1XXX CV021 CV031 DD2
364 + STORE KEY ADDRESS XXXX1XX CV021 CV031 DE2
279 + STORE KEY ADDRESS XXXXX1X CV021 CV031 DF2
258 + STORE KEY ADDRESS XXXXXX1 CV021 CV031 DG2
352 - SELECT STORE KEY BUFFER 0 CV021 FA6
346 - SELECT STORE KEY BUFFER 1 CV021 FB6
339 - SELECT STORE KEY BUFFER 2 CV031 FC6
304 - SELECT STORE KEY BUFFER 3 CV031 FD6
445 - SAR BYTE 0 EVEN PARITY- CK003-FG2
457 - SAR BYTE X EVEN PARITY- CK003-HE4

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN.
2 RESISTOR
A-B4D2S13
445 A-B4P1E13
01A-B3P6E04
457 A-B4P1E11
01A-B3P6E02

LOC. TYPE
A-B4D2 6798

CCU STORAGE PROTECT SP REG AND STORE KEY ADDR GEN		
E.C.—HISTORY—D 312922 314419	MACH.27RNB FRAME 01	
DATE LAST EC 10-06-76 315053	IBM CORP.SDD P.N. 1750199	CV001 030

030 CV011

- SPARE GO FOR PROTECTION KEY — AU001GH4 — 2
- GO CSB 3 — CP001FG6 — 8
+ ALLOW INST — CP001FJ2 — 14
- GO CHAN 1 — CP001FK6 — 20
- GO CHAN 2 — CP001FM6 — 26
- PROG LEV 5 NEXT — CP002GK6 — 32
+ SP REG BIT 0.4 — CV001BJ2 — 38
- SP REG BIT 0.4 — CV001BJ6 — 44
+ SP REG BIT 0.5 — CV001BK2 — 50
- SP REG BIT 0.5 — CV001BK6 — 56
+ SP REG BIT 0.6 — CV001BL2 — 62
- SP REG BIT 0.6 — CV001BL6 — 68
+ A TIME — CV001CA2 — 74
- A TIME — CV001CA6 — 80

THIS PAGE IS FOR 3705-II ONLY.

38 -07RA A AD 06P 102
56 ZP02A
68 ZS02A
A-B4D2

2 ZD A N AA 06D 109
A-B4D2

38 07RA A AG 09Q 116
50 19FA
62 18EA
A-B4D2

14 ZD10 N AB 09E 123
A-B4D2

DECODE

44 ZP04A A AC 18F 131
50 -19FA
62 -18EA
A-B4D2

38 -07RA A AF 06R 137
50 -06GA
68 ZS02A
A-B4D2

38 -07RA A AE 09N 144
56 ZP02A
62 -09FA
A-B4D2

LOC. TYPE
A-B4D2 6798

SPARE

74 29KA A CR EE 27H 203
102 28KA
80 28HA A
109 26HA
A-B4D2

INSTRUCTION

116 33HA A CR EL 34K 217
74 34HA
123 34LA A
80 33LA
32 P05A
A-B4D2

CSB-3

131 29JA A CR EC 29L 231
74 29KA
8 M02A A
80 28HA
A-B4D2

CA NO. 1

74 44KA A CR EJ 42H 244
137 43KA
80 43HA A
20 S05A
A-B4D2

CA NO. 2

144 44JA A CR EG 44L 257
74 44KA
26 S04A A
80 43HA
A-B4D2

231 + PROT KEY ADDRESS 100 — CV041-EC2

203 + PROT KEY ADDRESS 011 — CV041-EE2

257 + PROT KEY ADDRESS 010 — CV041-EG2

244 + PROT KEY ADDRESS 001 — CV041-EJ2

217 + PROT KEY ADDRESS 000 — CV041-EL2

CCU STORAGE PROTECT
PROTECT KEY ADDRESS GENERATION
E.C.-HISTORY — MACH.27RNB

312922
314419

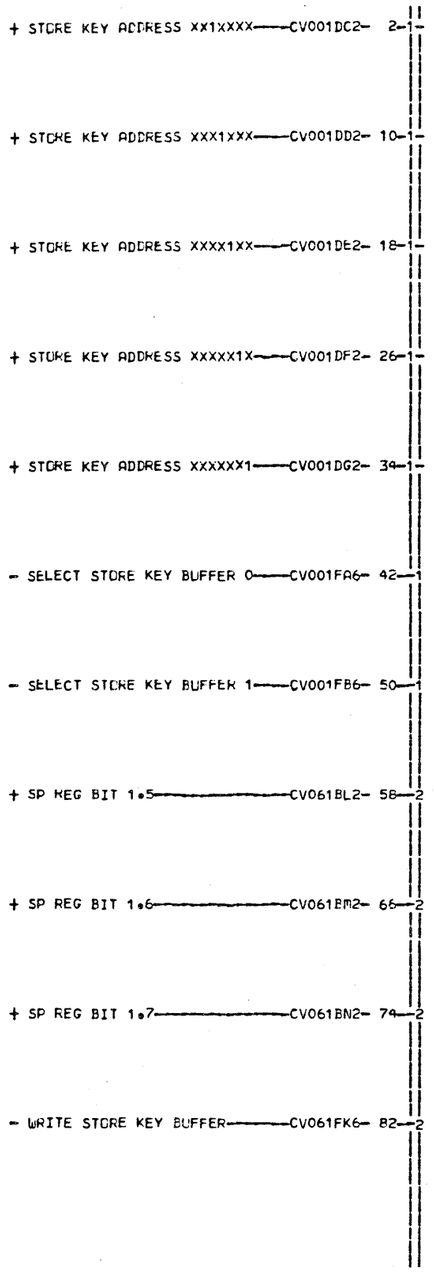
DATE 10-08-76 LAST EC 315093

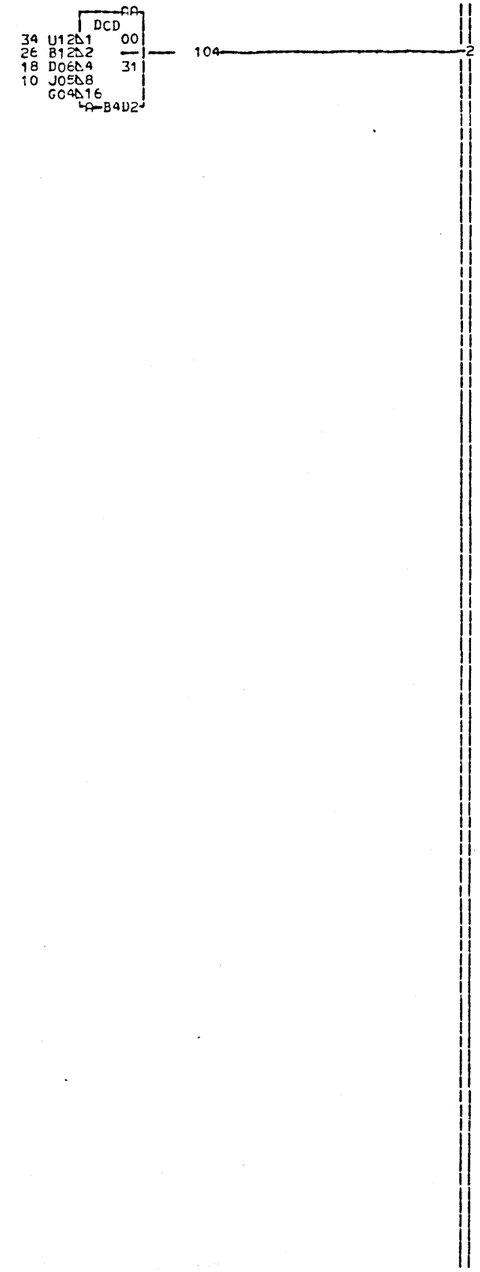
FRAME 01

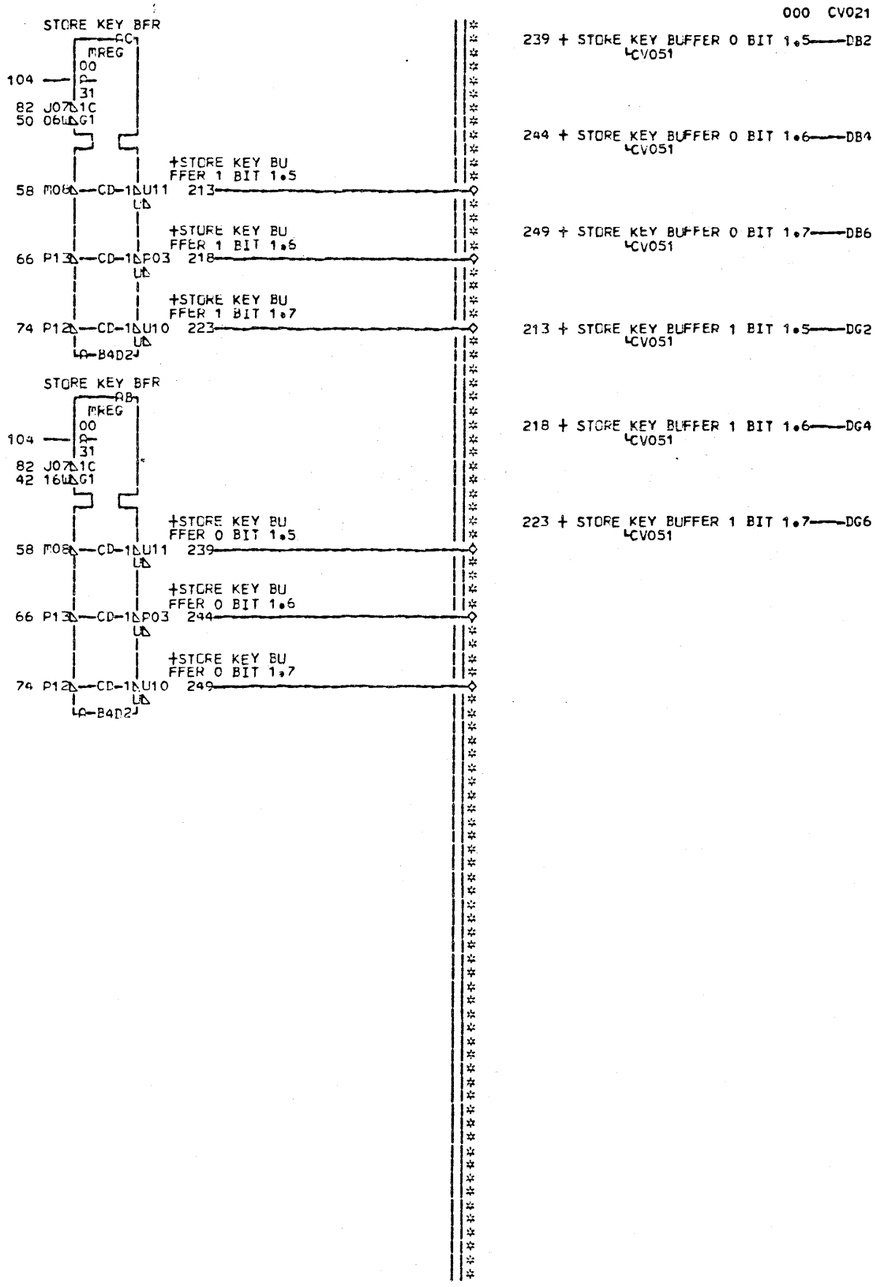
IBM CORP.SDD CV011

P.N. 1750200 030

CV011
030 SIM TO PN 5997653 EC 314416







CV021
000

CCU STORAGE PROTECT
STORE KEY BUFFER
E.C. HISTORY — B MCCH.27RNB
309521C
FRAME 01
IBM CORP.SDD | CV021
DATE LAST EC | P.N. 5997654 | 000
04-19-72 309585

+ STORE KEY ADDRESS XX1XXXX——CV001DC2- 2-1

+ STORE KEY ADDRESS XXX1XXX——CV001DD2- 10-1

+ STORE KEY ADDRESS XXXX1XX——CV001DE2- 18-1

+ STORE KEY ADDRESS XXXXX1X——CV001DF2- 26-1

+ STORE KEY ADDRESS XXXXXX1——CV001DG2- 34-1

- SELECT STORE KEY BUFFER 2——CV001FC6- 42-1

- SELECT STORE KEY BUFFER 3——CV001FD6- 50-1

+ SP REG BIT 1.5——CV061BL2- 58-2

+ SP REG BIT 1.6——CV061BM2- 66-2

+ SP REG BIT 1.7——CV061BN2- 74-2

- WRITE STORE KEY BUFFER——CV061FK6- 82-2

CV031
000

AA
DCD
34 U12 1 00
26 B12 2
18 D06 4 31
10 J05 8
G04 16
A-B4D2

104 ——— 2

STORE KEY BFR
AC
MREG
00
104 ——— P
31
82 J07 1C
50 11 G1

+STORE KEY BU
FFER 3 BIT 1.5
58 M08 ——CD-1 U11 213
UA

+STORE KEY BU
FFER 3 BIT 1.6
66 P13 ——CD-1 P03 218
UA

+STORE KEY BU
FFER 3 BIT 1.7
74 P12 ——CD-1 U10 223
UA
A-B4D2

STORE KEY BFR
AB
MREG
00
104 ——— P
31
82 J07 1C
42 21 G1

+STORE KEY BU
FFER 2 BIT 1.5
58 M08 ——CD-1 U11 239
UA

+STORE KEY BU
FFER 2 BIT 1.6
66 P13 ——CD-1 P03 244
UA

+STORE KEY BU
FFER 2 BIT 1.7
74 P12 ——CD-1 U10 249
UA
A-B4D2

239 + STORE KEY BUFFER 2 BIT 1.5——DB2
CV051

244 + STORE KEY BUFFER 2 BIT 1.6——DB4
CV051

249 + STORE KEY BUFFER 2 BIT 1.7——DB6
CV051

213 + STORE KEY BUFFER 3 BIT 1.5——DG2
CV051

218 + STORE KEY BUFFER 3 BIT 1.6——DG4
CV051

223 + STORE KEY BUFFER 3 BIT 1.7——DG6
CV051

LOC. TYPE
A-B4D2 6798

CCU STORAGE PROTECT
STORE KEY BUFFERS 2 AND 3
E.C. HISTORY——B MACH.27RNB
309521C
FRAME 01
IBM CORP.SDD CV031
DATE LAST EC
04-19-72 309545 P.N. 5997655 000

+ PROT KEY ADDRESS 100 ——— CV011EC2 — 2-3

+ PROT KEY ADDRESS 011 ——— CV011EE2 — 9-3

+ PROT KEY ADDRESS 010 ——— CV011EG2 — 16-3

+ PROT KEY ADDRESS 001 ——— CV011EJ2 — 23-3

+ PROT KEY ADDRESS 000 ——— CV011EL2 — 30-3

- FLOAT ——— CV041007 — 37-3

- FLOAT ——— CV041008 — 44-3

- FLOAT ——— CV041009 — 51-3

+ SP REG BIT 1.5 ——— CV061BL2 — 58-2

+ SP REG BIT 1.6 ——— CV061BM2 — 65-2

+ SP REG BIT 1.7 ——— CV061BN2 — 72-2

+ WRITE PROT KEY BUFFER ——— CV061FL2 — 79-6

PROT KEY BFR

AC MREG: 9 -37R A0, 16 -36R A1, 23 -36Q A2, 30 -38Q A3, 79 ZM11 C; 72 ZP12 —CD— A-B4D2 U+ ← PROT KEY BUFFER 0 BIT 1.7 J03 112

AB MREG: 9 -37R A0, 16 -36R A1, 23 -36Q A2, 30 -38Q A3, 79 ZM11 C; 65 ZP13 —CD— A-B4D2 U+ ← PROT KEY BUFFER 0 BIT 1.6 D13 127

AA MREG: 9 -37R A0, 16 -36R A1, 23 -36Q A2, 30 -38Q A3, 79 ZM11 C; 58 ZM08 —CD— A-B4D2 U+ ← PROT KEY BUFFER 0 BIT 1.5 J09 143

AF MREG: 37 ZP10 A0, 44 ZP07 A1, 51 ZM09 A2, 2 -38K A3, 79 ZM11 C; 72 ZP12 —CD— A-B4D2 U+ ← PROT KEY BUFFER 1 BIT 1.7 J03 158

AE MREG: 37 ZP10 A0, 44 ZP07 A1, 51 ZM09 A2, 2 -38K A3, 79 ZM11 C; 65 ZP13 —CD— A-B4D2 U+ ← PROT KEY BUFFER 1 BIT 1.6 D13 172

AD MREG: 37 ZP10 A0, 44 ZP07 A1, 51 ZM09 A2, 2 -38K A3, 79 ZM11 C; 58 ZM08 —CD— A-B4D2 U+ ← PROT KEY BUFFER 1 BIT 1.5 J09 186

030 CV041

143 + PROT KEY BUFFER 0 BIT 1.5 ——— DB6 ↳CV051

127 + PROT KEY BUFFER 0 BIT 1.6 ——— DD6 ↳CV051

112 + PROT KEY BUFFER 0 BIT 1.7 ——— DF6 ↳CV051

186 + PROT KEY BUFFER 1 BIT 1.5 ——— DH6 ↳CV051

172 + PROT KEY BUFFER 1 BIT 1.6 ——— DK6 ↳CV051

158 + PROT KEY BUFFER 1 BIT 1.7 ——— DM6 ↳CV051

LOC. TYPE
A-B4D2 6798

CV041
030 SIM TO PN 5997656 EC 309545

CCU STORAGE PROTECT
PROTECT KEY BUFFER
—E.C.—HISTORY—D MACH.27RNB

DATE	LAST EC		
09-29-76	313053	FRAME 01 IBM CORP.SDD P.N. 1757973	CV041 030

+ GATE INPUT 73 — CQ004DM2 — 2-6
+ SP REG BIT 1.3 — CV001BM2 — 6-3
- SP REG BIT 1.3 — CV001BM6 — 10-3
+ STORE KEY BUFFER 0 BIT 1.5 — CV021DB2 — 14-1
+ STORE KEY BUFFER 0 BIT 1.6 — CV021DB4 — 18-1
+ STORE KEY BUFFER 0 BIT 1.7 — CV021DB6 — 22-1
+ STORE KEY BUFFER 1 BIT 1.5 — CV021DG2 — 26-1
+ STORE KEY BUFFER 1 BIT 1.6 — CV021DG4 — 30-1
+ STORE KEY BUFFER 1 BIT 1.7 — CV021DG6 — 34-1
+ STORE KEY BUFFER 2 BIT 1.5 — CV031DB2 — 38-1
+ STORE KEY BUFFER 2 BIT 1.6 — CV031DB4 — 42-1
+ STORE KEY BUFFER 2 BIT 1.7 — CV031DB6 — 46-1
+ STORE KEY BUFFER 3 BIT 1.5 — CV031DG2 — 50-1
+ STORE KEY BUFFER 3 BIT 1.6 — CV031DG4 — 54-1
+ STORE KEY BUFFER 3 BIT 1.7 — CV031DG6 — 58-1
+ PROT KEY BUFFER 0 BIT 1.5 — CV041DB6 — 62-1
+ PROT KEY BUFFER 0 BIT 1.6 — CV041DD6 — 66-1
+ PROT KEY BUFFER 0 BIT 1.7 — CV041DF6 — 70-1
+ PROT KEY BUFFER 1 BIT 1.5 — CV041DH6 — 74-1
+ PROT KEY BUFFER 1 BIT 1.6 — CV041DK6 — 78-1
+ PROT KEY BUFFER 1 BIT 1.7 — CV041DM6 — 82-1

AC: OR * → 102 (DOT; 22, 34, 46, 58)
AB: OR * → 116 (DOT; 18, 30, 42, 54)
AA: OR * → 130 (DOT; 14, 26, 38, 50)
PROT KEY BIT 1.7 — AF: OR * → 146 (DOT; 70, 82)
PROT KEY BIT 1.6 — AE: OR * → 154 (DOT; 66, 78)
PROT KEY BIT 1.5 — AD: OR * → 162 (DOT; 62, 74)

STORE KEY TO INDATA BIT 1.7 — BD: A OR S10 204 (2 ZS07, 10 ZU06, 102 ZU10, A-B4D2)
STORE KEY BIT 1.7 — BA: N 33T 212 (102 ZU10, A-B4D2)
STORE KEY TO INDATA BIT 1.6 — BC: A OR S11 219 (2 ZS07, 10 ZU06, 116 ZP03, A-B4D2)
STORE KEY BIT 1.6 — AH: N 31U 227 (116 ZP03, A-B4D2)
STORE KEY TO INDATA BIT 1.5 — BB: A OR S12 234 (2 ZS07, 10 ZU06, 130 ZU11, A-B4D2)
STORE KEY BIT 1.5 — AG: N 31V 242 (130 ZU11, A-B4D2)
PROT KEY TO INDATA BIT 1.7 — BG: A OR S10 249 (2 ZS07, 6 -49D, 146 ZJ03, A-B4D2)
PROT KEY TO INDATA BIT 1.6 — BF: A OR S11 257 (2 ZS07, 6 -49D, 154 ZD13, A-B4D2)
PROT KEY TO INDATA BIT 1.5 — BE: A OR S12 265 (2 ZS07, 6 -49D, 162 ZJ09, A-B4D2)

162 + PROT KEY BIT 1.5 — CV061-BG4
154 + PROT KEY BIT 1.6 — CV061-BJ4
146 + PROT KEY BIT 1.7 — CV061-BL4
242 - STORE KEY BIT 1.5 — CV061-DA2
227 - STORE KEY BIT 1.6 — CV061-DC2
212 - STORE KEY BIT 1.7 — CV061-DE2
234 + STORE KEY TO INDATA BIT 1.5 — FA2 (CU012)
219 + STORE KEY TO INDATA BIT 1.6 — FC2 (CU012)
204 + STORE KEY TO INDATA BIT 1.7 — FE2 (CU012)
265 + PROT KEY TO INDATA BIT 1.5 — FG2 (CU012)
257 + PROT KEY TO INDATA BIT 1.6 — FJ2 (CU012)
249 + PROT KEY TO INDATA BIT 1.7 — FL2 (CU012)

EDGE CONN.
102 RESISTOR A-B4D2U10
116 RESISTOR A-B4D2P03
130 RESISTOR A-B4D2U11
146 RESISTOR A-B4D2J03
154 RESISTOR
162 RESISTOR A-B4D2D13
A-B4D2J09

LOC. TYPE
A-B4D2 6798

CCU STORAGE PROTECT INDATA GATING
E.C.-HISTORY — B — FACH.27RNB
309521C
FRAME 01
IBM CORP.SDD
DATE LAST EC
04-19-72 309545
P.N. 5997657
CV051
000

- I1 TIME — CC004EA6 — 2
- I2 TIME — CC004EE5 — 5
- T2 TIME — CC006DF9* — 8
- BAL+LA INST — CD001DK6 — 11
ALWAYS - OUT — CG001EF6 — 14
+ GO MAINT — CP001GD2 — 17
- LATCHED STORE — CQ001GL2 — 20
+ SET OUTPUT 73 — CQ004DE2 — 23
- SET OUTPUT 77 — CQ004FE6 — 26
- GATE INPUT 7E — CQ005FL6 — 29
- GATE STATUS TO DISPLAY B — CU001EJ6 — 32
+ RESET — CU010FM2 — 35
+ SP REG BIT 1.3 — CV001BM2 — 38
- SP REG BIT 1.3 — CV001BM6 — 41
+ A TIME — CV001CA2 — 44
- A TIME — CV001CA6 — 47
+ PROT KEY BIT 1.5 — CV051BG4 — 50
+ PROT KEY BIT 1.6 — CV051BJ4 — 53
+ PROT KEY BIT 1.7 — CV051BL4 — 56
- STORE KEY BIT 1.5 — CV051DA2 — 59
- STORE KEY BIT 1.6 — CV051DC2 — 62
- STORE KEY BIT 1.7 — CV051DE2 — 65
- Z BUS BIT 1.4 — DL004GK6 — 68
- Z BUS BIT 1.5 — DM004GB6 — 71
- Z BUS BIT 1.6 — DM004GF6 — 74
- Z BUS BIT 1.7 — DM004GK6 — 77

+ KEY EQUAL (AG)
53 ZD13 EV A U05 103 +
62 -23N
50 ZJ09 EV
59 -21N
56 ZJ03 EV
65 -22R
A-B4D2

ALLOW PROTECT (AH)
FL
23 ZD12 S U07 116 S
35 ZG03 R
A-B4D2

+ RESET LTCH (BE)
26 ZD09 A 08E 124
71 ZD03
A-B4D2

SP REG (PH)
68 ZD02 CD D11 146
23 ZD12 C
A-B4D2

ALLOW WRITE (BC)
AR FL11J 152
23 ZD12 S S
35 ZG03 OR R
44 -19C
A-B4D2

SP REG BIT 1.7 (AF)
PH
77 ZD05 CD P12 167
23 ZD12 C
A-B4D2

SP REG BIT 1.6 (AE)
PH
74 ZD04 CD P13 174
23 ZD12 C
A-B4D2

SP REG BIT 1.5 (AD)
PH
71 ZD03 CD M08 181
23 ZD12 C
A-B4D2

NO EFFECT (AB)
14 OR 203
103 DOT

8 ZG02 A (AA) 244
145 ZD11
152 -14R
A-B4D2

ALL 0S DET
ALL 1S DET
-ERR IF STORE (BA)
20 ZJ04 OR OR S03 305
203 ZJ05
116 ZJ07
59 -18N
62 -19N
65 -16N
50 ZJ09
53 ZD12
56 ZJ03
A-B4D2

BAD-KEY•ALLOW (BB)
203 ZJ05 OR 46F 320
116 ZJ07
A-B4D2

WRITE PROT KEY BUFFER (BH)
38 13N A M11 346
244
A-B4D2

WRITE STORE KEY BUFFER (BG)
41 ZU06 A WJ07 360
244
A-B4D2

PROTECTION ERROR (BF)
47 44C OR U09* 404
305 S03 41E 405
2 U03
47 44E
320 44D
5 M13
11 U02
47 43E
320 43F
A-B4D2

PROT CHK LTCH (FF)
504 ZB13 A OR
8 ZG02
124 -14F A 14D 423
35 -G03
423 -13E
A-B4D2

T2•NOT-MAINT - SET LTCH (BD)
8 ZG02 A 813* 504
17 ZS09
405 -16F
A-B4D2

PROT CHK TO DISPLAY B (CC)
423 -39E A OR P11 512
32 ZM12
A-B4D2

PROTECT CHK TO INDATA 1•3 (CB)
423 -36E A OR P09 532
29 ZS09
A-B4D2

PROT CHECK BID PROG LEV 1 (CA)
423 39C N OR M10 539
A-B4D2

181 + SP REG BIT 1•5 — BL2 (CV021 CV031 CV041)
174 + SP REG BIT 1•6 — BM2 (CV021 CV031 CV041)
167 + SP REG BIT 1•7 — BN2 (CV021 CV031 CV041)
504 - SET PROTECT CHK — CK007-EK2
404 + PROTECTION ERROR — CS002-FB2
360 - WRITE STORE KEY BUFFER — FK6 (CV021 CV031)
346 + WRITE PROT KEY BUFFER — CV041-FL2
539 + PROT CHECK BID PROG LEV 1 — GE2 (CP005)
532 + PROTECT CHK TO INDATA 1•3 — GF2 (CU012)
512 + PROT CHK TO DISPLAY B — AP013-GH2

THIS PAGE IS FOR 3705-II ONLY.

EDGE CONN•
8 RESISTOR
A-B4D2/602
404 A-B4Q1B13
01A-B3Q6B04
504 A-B4Q1A11
01A-B3Q6A02

LOC• TYPE
A-B4D2 6798

CCU STORAGE PROTECT
PROTECTION ERROR DECTION
—E•C•—HISTORY—D— MACH•27RNB
312922
314419
FRAME 01
IBM CORP•SDD | CV061
DATE LAST EC
10-06-75 315053 | P•N• 1750201 | 030

CV061
030 SIM TO PN 5997658 EC 310268