



DATA	ADDR	ROS
JUMPED PIN TO PIN		CONS. C3 CABLE
FLT PT.	CONS. C2	
CONS. C1 CABLE	JUMPED PIN TO PIN X02 TO X02	
ALL Y CONN. JUMPED PIN TO PIN Y02 TO Y02 TO Y02		
ALL Z CONN. JUMPED PIN TO PIN Z02 TO Z02 TO Z02		

4955 PROCESSOR CABLES
VIEWED FROM CARD SIDE

W	W	CONS. C3 CABLE	W
	X	CONS. C1 CABLE	X
X		NOT USED	
	Y	CONS. C2	Y
Y		NOT USED	
	Z		Z

4953 PROCESSOR
CABLE LOCATIONS

4955
CABLE CONNECTIONS

CONSOLE	PROCESS
C1	DATA X
C2	ADDR WL
C3	ROS W

C5 JUMPER C6

C3 CABLE CONNECTION

BASIC * CONSOLE	PROC* CARD
4953 C6 W	
**** *** ****	
4955 C6 ROS W	

* C3 CABLE FROM
C6 TO PROCESSOR

NOTE: For user information see Prolog.

Sequence		Part	EC 374831				
0410AA	1 of 2	6826696	7-1-78				

Sequence		Part	EC 374831				
0410AA	2 of 2	6826696	7-1-78				

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