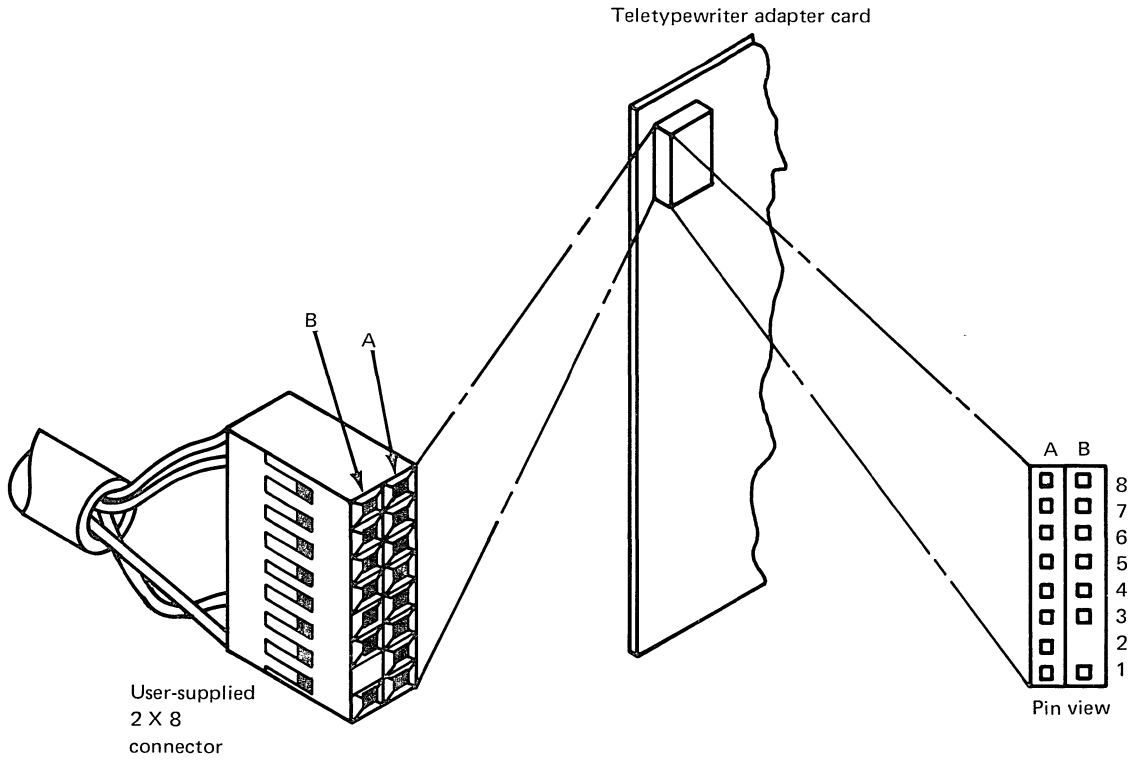
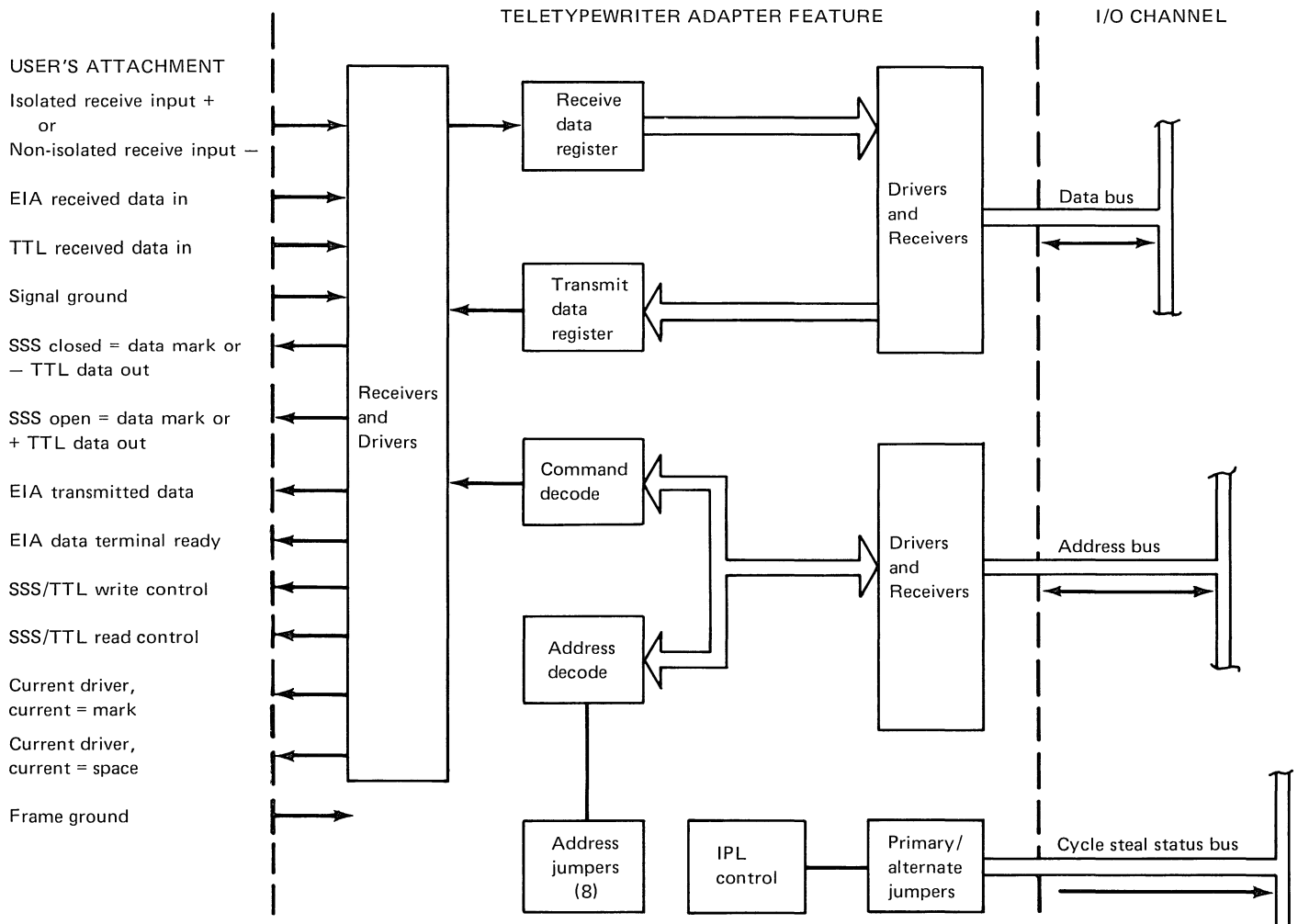


Signal Pin Assignment

Figure 4-8 shows how to identify pins on the cable connector. The following table is a list of the signals and their pin assignments.

Pin	*Signal*	*Pin*	*Signal*
A1	Isolated receive input +	B1	Non-isolated receive input-
A2	Current driver, current =mark	B2	(Polarization pin)
A3	Isolated receive input- or non-isolated receive input+	B3	Current driver, current= space
A4	EIA received data in	B4	TTL received data in
A5	Frame ground	B5	Signal ground
A6	EIA data terminal ready	B6	EIA transmitted data
A7	SSS closed=data mark or -TTLdata out	B7	SSS open=data mark or +TTL data out
A8	SSS/TTL write control	B8	SSS/TTL read control

Sequence		Part	EC 374831				
0640AA	2 of 2	6826702	7-1-78				



ABBREVIATIONS

TTL = transistor-transistor logic
EIA = Electronic Industries Assoication
SSS = solid state switch
IPL = initial program load