

001  
(ENTRY POINT A)  
THIS MAP SHOULD NOT BE ENTERED UNLESS AN  
ERROR HAS OCCURRED WHILE EXECUTING  
SYSTEM TEST AND THEN ONLY WHEN THE  
DEVICE TYPE FIELD IS EQUAL TO HEXADECIMAL  
411.

DOES RTN = 0000 ?

Y  
N

002  
DOES RTN = 0001 ?

Y  
N

003  
DOES RTN = 0002 ?

Y  
N

004  
DOES CKPT = 0000 ?

Y  
N

005  
DOES CKPT = 0001 ?

Y  
N

006  
DOES CKPT = 0002 ?

Y  
N

007  
DOES CKPT = 0003 ?

Y  
N

008  
DOES CKPT = 0004 ?

Y  
N

009  
DOES CKPT = 0005 ?

Y  
N

010  
DOES CKPT = 0006 ?

Y  
N

011  
Y N DOES CKPT = 0007 ?  
012  
Y N DOES CKPT = 0008 ?  
013  
Y N DOES CKPT = 0009 ?  
014  
A DATA COMPARE ERROR.  
INSPECT DEV4  
BITS 0-7 IS THE DATA EXPECTED.  
BITS 8-15 IS THE DATA RECEIVED.  
015  
A PURGE FRAME COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
016  
A RESET FRAME IN PROCESS COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
017  
RESIDUAL ADDRESS FAILED.  
DEV4 = EXPECTED ADDRESS.  
RSAD = ACTUAL ADDRESS.  
018  
A CYCLE STEAL STATUS COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
019  
A SET RECEIVE ENABLE BIT COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034, ENTRY POINT B.  
020  
A WRITE REQUEST OPERATION COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034, ENTRY POINT B.

021  
Y N AN ARM THE REQUEST SUBCHANNEL  
COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
022  
Y N A RESET RECEIVE ENABLE BIT COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
023  
Y N A SET FRAME IN PROCESS BIT COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
024  
Y N A SET BROADCAST RING ADDRESS COMMAND WAS  
EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
025  
Y N AN INVALID FUNCTION COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034, ENTRY POINT B.  
026  
Y N DOES CKPT = 0000 ?  
027  
Y N DOES CKPT = 0001 ?  
028  
Y N A PREPARE COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034,  
ENTRY POINT B.  
029  
Y N A READ ID COMMAND WAS EXECUTING.  
DEV3 = EXPECTED ID.  
DEV4 = RECEIVED ID.  
030  
Y N A RESET COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034, ENTRY POINT B.

A  
1

SERIES/I RING 1400 FEATURE  
SYSTEM TEST ERROR MAP  
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MAP 41E0-5

031  
DOES CKPT = 0000 ?  
Y N  
032  
A SET BYPASS COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034, ENTRY POINT B.  
033  
A PREPARE COMMAND WAS EXECUTING.  
GO TO PAGE 6, STEP 034, ENTRY POINT B.

SERIES/I RING 1400 FEATURE  
SYSTEM TEST ERROR MAP  
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MAP 41E0-6

034  
(ENTRY POINT B)  
DOES IO=07 ?  
Y N  
035  
DOES IO=06 ?  
Y N  
036  
DOES IO=05 ?  
Y N  
037  
DOES IO=03 ?  
Y N  
038  
DOES IO=02 ?  
Y N

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7 7 7 7 7 7  
N P Q R S T

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MAP 41E0-6

SYSTEM TEST ERROR MAP

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039  
DOES IO=01 ?  
Y  
N

040  
DEVICE NOT ATTACHED.

041  
DEVICE BUSY.

042  
BUSY AFTER RESET.

043  
COMMAND REJECT.

044  
INTERFACE DATA CHECK.

045  
CONTROLLER BUSY.

046  
DOES IN=02 ?  
Y  
N

047  
DOES IN=04 ?  
Y  
N

048  
DOES IN=03 ?  
Y  
N

049  
CONTROLLER END.

050  
DEVICE END RECEIVED.  
IS BIT 1 IN THE FLAGS FIELD OFF ?  
Y  
N

051  
INSPECT DEV4  
BITS 0-7 IS THE DATA EXPECTED.  
BITS 8-15 IS THE DATA RECEIVED.

052  
(ENTRY POINT D)

THIS MAP CANNOT DETERMINE THE PROBLEM.  
USE THE ERROR OUTPUT, YOU HAVE BEEN USING  
FOR THIS MAP, AS YOUR ERROR INDICATIONS  
AND GO TO MAP 0070 ENTRY POINT A.

053  
ATTENTION INTERRUPT.  
GO TO PAGE 8, STEP 058, ENTRY POINT E.

054  
IS BIT 0 OF THE ISB OFF ?  
Y  
N

055  
IS BIT 2 OF THE ISB OFF ?  
Y  
N

056  
NOT CORRECT LENGTH ERROR.

057  
IS CS-3 EQUAL TO FFFF ?  
Y  
N

SYSTEM TEST ERROR MAP

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058  
(ENTRY POINT E)

THE BITS IN CS-3 ARE AS FOLLOWS.  
BIT 00 = DEVICE NOT FOUND.  
BIT 01 = TRANSMIT BUFFER PARITY ERROR.  
BIT 02 = FRAME REFUSED.  
BIT 03 = RING REMOTE IPL OFF.

BIT 04 = RESERVED.  
BIT 05 = ABORT.  
BIT 06 = BUSY.  
BIT 07 = RECEIVE CRC ERROR.

BIT 08 = RING REMOTE IPL.  
BIT 09-11 = RESERVED.

BIT 12-14 = RESERVED.  
BIT 15 = RING DEGRADATION.

INSPECT CS-3 FOR ERROR BITS.  
ARE ALL ERROR BITS IN CS-3 OFF ?  
Y  
N

059  
ANALYZE THE ERROR BITS IN CS-3.

060  
THE BITS IN CS-4 ARE AS FOLLOWS.  
BIT 00 = RECEIVE ABORT.  
BIT 01 = TRANSMIT BAR OVERRUN.  
BIT 02 = RECEIVE CRC ERROR.  
BIT 03 = RECEIVE FRAME BUSY.

BIT 04 = FIRST CRC ERROR DETECT.  
BIT 05 = OA PARITY ERROR.  
BIT 06 = RECEIVE ENABLE.  
BIT 07 = RECEIVE FRAME IN DATA BUFFER.

BIT 08 = ODD POSITIVE ACKNOWLEDGE.  
BIT 09 = EVEN POSITIVE ACKNOWLEDGE.  
BIT 10 = PASS THRU ERROR.  
BIT 11 = CABLE OPEN SHORT CONDITION.

BIT 12 = CLEAR RING MODE ACTIVE.  
BIT 13 = BYPASS INVOKED.  
BIT 14 = TRANSMIT BUFFER PARITY ERROR.  
BIT 15 = TRANSMIT REQUEST TO RIC.

INSPECT CS-4 FOR ERROR BITS.  
ARE ALL ERROR BITS IN CS-4 OFF ?  
Y  
N

061  
ANALYZE THE ERROR BITS IN CS-4.

062  
GO TO PAGE 7, STEP 052,  
ENTRY POINT D.

063  
GO TO PAGE 7, STEP 052, ENTRY POINT D.

064  
IS BIT 1 OF THE ISB OFF ?  
Y  
N

065  
DELAYED COMMAND REJECT.

066  
IS BIT 2 OF THE ISB OFF ?  
Y  
N

067  
NOT CORRECT LENGTH ERROR.

068  
IS BIT 3 OF THE ISB OFF ?  
Y  
N

069  
DCB SPECIFICATION CHECK.

070  
IS BIT 4 OF THE ISB OFF ?  
Y  
N

071  
STORAGE DATA CHECK.

072  
IS BIT 5 OF THE ISB OFF ?  
Y  
N

X Y SERIES/1 RING 1400 FEATURE  
8 8 SYSTEM TEST ERROR MAP  
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MAP 41E0-9

073  
NOT VALID STORAGE ADDRESS.

074  
IS BIT 6 OF THE ISB OFF ?  
Y  
N

075  
PROTECT CHECK.

076  
IS BIT 7 OF THE ISB OFF ?  
Y  
N

077  
INTERFACE DATA CHECK.

078  
(ENTRY POINT C)  
IS BIT 0 OF THE FLAGS OFF ?  
Y  
N

079  
NOT EXPECTED INTERRUPT.

080  
IS BIT 5 OF THE FLAGS OFF ?  
Y  
N

081  
WRONG INTERRUPT LEVEL.

082  
IS BIT 6 OF THE FLAGS OFF ?  
Y  
N

083  
LOST INTERRUPT.

084  
GO TO PAGE 7, STEP 052, ENTRY POINT D.

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