

# COMMON MAGNETIC TAPE TEST PROGRAM

**Consists of:**

Test Program Description  
Test Program Listing  
Test Tape

B06-172M95R03A15  
06-172R03A13  
06-172M17R03

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COMMON MAGNETIC TAPE TEST PROGRAM DESCRIPTION

1. PURPOSE OF TEST

The Magnetic Tape Test Program tests the function of the Magnetic Tape and its associated interface. Special test and options are provided to enable measurement and isolation of a failure. This program also allows the testing of two devices at once.

Test 0

This test checks all data lines for correct data transfer with worst case data patterns. This test is mandatory.

Test 1

This test checks the ability of the device to write and read variable length records. The write-backspace-read feature is used with records varying in length from X'00'-X'01' to X'00'-X'FF'.

Test 2

This test checks the rewind and skip functions of the device.

Test 3

This test checks all device functions under device interrupt. Proper interrupt reception, interrupt queuing, and interrupt disarm and disable functions are all tested. Read only, write EOF continuous, and other options are provided. (See Appendix F).

Test 4

This test checks device overflow by write-long/read-short and write-short/read-long.

Test 5

This test checks the proper generation of Inter-Record Gaps. (Note that Prolonged repetition of this test may wear out the portion of the tape being used).

Test 6

This test checks the Cyclic Redundancy Check Character (CRC). This test applies to 9-Track 800 bpi magnetic tapes only.

Test 7

This is a user utility test which provides compatibility read only check, scope loop and data pattern selection. The user can select the number of bytes per record, number of records per file, and number of files. A WEOF option is provided to write EOF marks to the end of tape.

## 2. REQUIREMENTS OF MACHINE UNDER TEST

This program assumes that the programs listed below have been run without detecting an error.

For 16-Bit Processor

- Memory Test
- Processor Test

For 32-Bit Processor

- Series 32 Processor Tests
- Series 32 Memory Tests

Other Test Programs

- |   |        |
|---|--------|
| ● Common Teletype Basic Confidence Test | 06-004 |
| ● Common CRT Test                       | 06-146 |
| ● Common Line Printer Test              | 06-170 |
| ● Common Current Loop Interface Test    | 06-184 |
| ● Model-1100 CRT Test                   | 06-217 |
| ● Common Carousel 300 Test              | 06-183 |
| ● Model-550 Terminal Test               | 06-243 |

The magnetic tape must be mounted and the device placed 'ON-LINE'.

75 IPS must be on DMA Bus, 1600 bpi @ 75 IPS should not be run in read block/write-block mode.

Test 6 requires that the interface board be placed on an extender board. This allows hardware adjustments to be made to allow reading of CRC characters (see Section 5.1)

## 3. MINIMUM HARDWARE REQUIRED

Processor

- Model 7/16 Basic or equivalent
- Model 7/32 or equivalent

Minimum Memory

- 16K Bytes

Console Input Device (See Appendix A)

- Teletype or
- CRT on PASLA/PALM or
- Carousel 15, 35, 300,

List Device (See Appendix A)

- Teletype
- CRT on PASLA/PALM or
- LINE PRINTER or
- CAROUSEL 15, 35, 300

Object Input device or Multimedia loader

Device Under Test

The following tape systems can be tested with this program:

• 9-Track,	800 bpi Magnetic Tape	(M46-470)	45	IPS
• 9-Track,	1600 bpi Magnetic Tape	(M46-475)	45	IPS
• 7-Track,	800 bpi Magnetic Tape	(M46-474)	45	IPS
• 9-Track,	800 bpi Magnetic Tape	(M46-490)	75	IPS
• 9-Track,	800/1600 bpi Magnetic Tape	(M46-494)	75	IPS

Loading Procedure

Manually enter the following X'50' sequence into memory:

	<u>LOCATION</u>	<u>CONTENTS</u>
	X'30'	X'0000'
	X'32'	X'0000'
	X'34'	X'0000'
	X'36'	X'0050'
	X'50'	X'D500'
	X'52'	X'00CF'
	X'54'	X'4300'
	X'56'	X'0080'
800 bpi Magnetic Tape	X'78'	X'85A1'
Floppy Media Disc	X'78'	X'C186'
HSPTR/P	X'78'	X'1399'

#### 4.1 Multi Media Diagnostic Loading

To load this program from the Perkin-Elmer Multi Media Diagnostic System, refer to Publication Number 06-176A15.

#### 4.2 Program Execution

Execute at X'30' and when the processor halts, observe the CHKSUM byte displayed on the console display register D1 (for processors with display panel); (otherwise, check register). If it is zero, loading is complete; otherwise, repeat the loading procedure.

Refer to Appendix A and set up the addresses for the console input device and the bit device.

Address memory location X'A00' for a 32-Bit Processor.  
Address memory location X'A04' for a 16-Bit Processor.

Start program execution. The following title is output to the list device:

COMMON MAGNETIC TAPE TEST PROGRAM 06-172R03

5.0 OPERATING PROCEDURES

5.1 Normal Testing

To execute default tests, enter the following via the console device:

```
TEST (CR)
RUN (CR)
```

Tests 0, 1, 2, 3, 4, 5 are executed.

If no failure is detected, the list device output will be as shown in Appendix D, and the program returns to console mode after completion of Test 5. In the event of failures, refer to Section 5.3.

To interrupt and terminate a test, the user can either depress the BREAK key on the console device or take the device under test OFF-LINE. When either condition is detected, the test terminates and returns to console input mode. When the device under test is put off-line (DU), the message:

```
DEVICE OFF-LINE
DEV DDD STA SS
```

is printed. It is recommended that the tape be terminated properly; therefore, the DU type of test termination should not be used. During scope loop with Write (SCOPE=1, 2 or 3), the DU termination method is not available.

If a failure abnormally terminates the program, the program can be restarted at location X'A04' for 16-Bit Processors or X'A00' for 32-Bit Processors.

5.2 Optional Testing

Normally, the tests write a data file of 256 records and each record contains 256 bytes (except for Test 2). The number of records per file can be altered with option RECFIL. In tests 2 and 7, more than one file can be generated by option FILE and option BYTES can be used to vary the number of bytes per record in Tests 3 and 7 (see Appendix C). Inter-record gaps separate records and EOF marks separate files.

To select the mode of data transfer, option MODE must be specified. If MODE 0 is entered, both modes 1 and 2 (see Appendix C) are run in Tests 0, 1, 3, 4, and 7 with SCOPE 0. In all other tests, MODE 2 is used.

To test two devices at the same time, the user can enter the second device address by option DV2ADR. For single device testing, set DV2ADR to zero; otherwise, each selected test is executed twice, once on each device.

Each I/O device is assigned an interrupt level on the Model 8/32 or Series 3200. This level must be entered via option INTLEV. The same level is used for the selector channel and both devices.

Test 6 checks CRC generation. It can be executed only if the magnetic tape system is an 800 bpi 9-track system. The device interface board should be placed on an extender board to enable hardware adjustment. To execute Test 6, set option CRC and options DEVICE and TRACK to the appropriate value. When a file is generated on the tape, the message:

ADD CRC CAPACITOR AND EXECUTE

is printed on the list device and the processor is halted. Add a 0.022 uf capacitor between test points 39 and 40 and a jumper between test points 35 and 38 so the CRC character can be read (Refer to 02-277D08 and 02-277E03). To continue the test, depress the RUN button (or EXE). The capacitor and jumper must be removed upon termination of the test; therefore, this test must be selected alone.

Besides setting option CONTIN (see Appendix C), the selected tests can be continuously looped by turning the console device OFF-LINE. Since Tests 6 and 7 require console I/O, they must not be selected. Test 3 is executed under interrupts, and the user can specify individual operations to be tested through options WRITE, READ, BKSPAC, WEOF, and SKIP (see Appendix C). The test processes only one file, but the user can specify record length and file length through options BYTES and RECFIL (See Appendix C). If read only (See Appendix F) is specified, the user must make sure that the file begins and ends with a file mark. If DU option is set, the message:

TURN DEVICE OFF-LINE MOMENTARILY

is printed. The device under test must be turned OFF-LINE within 60 seconds after the message, but must not remain OFF-LINE for over 30 seconds.

Test 7 provides user utility through options READ, WRITE, BKSPAC, and WEOF. The user can test individual operations (see Appendix F). If the option DATA is set and the selected operation includes the write function, the message:

ENTER DATA:

is printed on the list device. The user can enter a string of up to 64 hexadecimal characters on the console input device.

Use CR to terminate the string and continue execution. If the buffer is full or 64 hexadecimal characters have been accepted, the test automatically continues. If only CR is entered after the message, the test generated buffer (256 bytes of data incremental from X'00' to X'FF') is used. No more data is requested after the first pass if the test is looped.

The user can also specify the number of files to be processed, the file length and record length through options FILES, RECFIL, and BYTES (see Appendix C). For the read only (see Appendix F) operation the user must make sure that there is a leading file mark on the tape and each file is terminated by a file mark. Attempts must not be made to read more files than exist on the tape.

Test 7 also provides scope loop option through option SCOPE (see Appendix C). Scope loops run continuously with no error check until EOT or termination by BREAK or DU.

SCOPE 1, 2, and 3 involve write operations (see Appendix C). In order to properly terminate the tape, the DU method of termination is not available. To terminate the tape before EOT is detected, BREAK must be depressed on the console device. In this case, the test terminates the tape with a file mark. (SCOPE 3 writes and backspaces over the same portion of the tape continuously).

SCOPE 4 performs "read only" continuously until EOT. If EOF is detected, the test pauses with the message:

EOF

If CR is depressed on the console device, the test terminates. If LF is depressed, the test continues reading until EOR or the next EOF. This procedure prevents reading beyond the last EOF on the tape. Reading a blank tape beyond the last EOF mark may cause the entire tape to run off the feeding reel.

SCOPE 5 performs skip EOF operation forward until EOT, and then skips reverse until BOT. It continues back and forth until terminated by BREAK or DU. It is recommended to fill the tape with EOF marks with the WEOF option, before performing this option.

### 5.3 Error Procedure

#### Error Recovery

If an error is encountered which is considered recoverable, the program logs an error message and retires 5 times. If it fails after 5 times, the message:

RECOVERY UNSUCCESSFUL

is printed and the test proceeds.

## Error Messages

The three types of error messages logged are:

Status Error: The following message is printed:

```
ERROR XXYY
DEV DDD STA SS
```

where: XX = Test number  
YY = Error number  
DDD = Device number  
SS = Device status

Data Error: The following message is printed:

```
ERROR XXYY
DEV DDD
```

Spurious interrupt error:

```
ERROR XXFN
DEV DDD STA SS
PSW PPPP LOC LLLL
```

where: XX = Test number  
N = 1 for arithmetic (32-bit) or  
fixed point arithmetic (16-bit) fault interrupt  
2 for illegal instruction interrupt  
3 for machine malfunction interrupt  
4 for spurious interrupt from external device  
5 for relocation/protection (32-bit) or  
floating point divide fault (16-bit) interrupt  
6 for device interrupt into wrong interrupt level  
7 for data format fault (alignment fault)  
DDD & SS = interrupting device address and statue received  
in case of 4 above  
PPPP = current PSW when interrupt is sensed (least  
significant 16 bit for 32-bit M/C)  
LLLL = current location when interrupt is sensed  
(least significant 16 bits for 32-bit M/C)

## 6. OTHER MESSAGES

MODE N

This message follows the error message for an error occuring during a data transfer.

N = mode number (see Appendix C)

```
DATA          DATA
WRITTEN       READ
AA            BB
```

This message is logged after data error #46. AA and BB are printed for each pair of unmatching data bytes.



CRC CHAR = AA

This message is printed in Test 6 after the first two CRC characters are read.

CRC CHAR EXPT'D = AA,READ = BB

This message is printed in Test 6 after error #48 is logged. AA and BB are the unmatching CRC characters.

DEVICE OFF-LINE  
DEV DDD STA SS

This message is printed whenever DU status is detected on the device under test. (see Section 5.1)

EOT

This message is printed whenever the test is terminated upon detection of EOT.

EOF

This message is printed upon detection of an EOF mark during read only scope loop. (see Section 5.2)

TURN DEVICE OFF-LINE MOMENTARILY  
(See Section 5.2)

ADD CRC CAPACITOR AND EXECUTE  
(See Section 5.2)

ENTER DATA  
(See Section 5.2)

## 7. Fault Isolation

For error 00, make sure that the device address is correct and the device interface is properly seated.

For NMTN errors (01 and 02), the device may be running away or stuck in an illegal mode. Initialize the device and restart the program.

Make certain that the tape used is good. If errors 10, 11, or 18 occur, change the tape and run test 0 with DUMP = 1.

If a data error occurs, observe the erroneous data bytes printed to establish a pattern of failure. Test 0 detects such data line failures.

If error 16 occurs, repeat Test 4 with DUMP = 1 and observe the data read. Failure can be in the read delay timing circuit.

For interrupt failures in Test 3, repeat Tests 0, 1, and 2. If no error occurs in Tests 0, 1, and 2, the failure is only in the interrupt generation circuit.

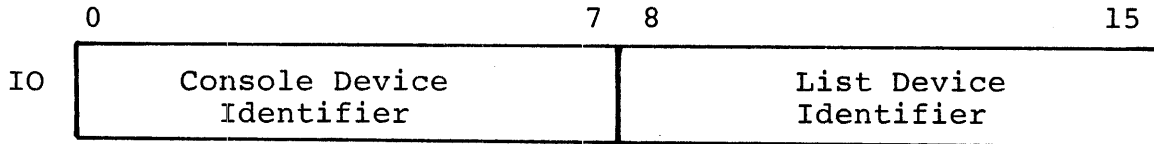
For other status errors, repeat the failing test with long files and records so each operation can be visually distinguished. Follow the program listing to determine exactly where the failure occurs.

The program puts a delimiter at the end of the read buffer before each read operation. Error 47 indicates the delimiter was destroyed by the read.

Scope loops can also be used to further isolate failures.

APPENDIX A  
USER DEVICE DEFINITION

The halfword labeled 'IO' (see the Program Listing) has the default value for CRT on a PASLA interface as the input/output console device. If the setup is different 'IO' must be changed as follows:



Console Device Identifier	Meaning
X'01'	GDT/CRT on PASLA/PALM interface, strapped for FDX operation and highest baud rate.
X'02'	TTY/GDT/CRT/Carousel 15/30 on Current Loop Interface
X'03'	Reserved. Interpreted as X'02'
X'04'	Carousel 300 on PASLA/PALM Interface, strapped for FDX operation and highest baud rate.
X'05'	Micro I/O Bus
X'00', X'06' -X'FF'	Reserved. Interpreted as X'02'.

List Device Identifier	Meaning
X'01'	GDT/CRT on PASLA/PALM Interface, strapped for FDX operation and highest baud rate.
X'02'	TTY/GDT/CRT/Carousel 15/30 on Current Loop Interface.
X'03'	Line Printer (Data Printer or Centronics) on Line Printer Interface.
X'04'	Carousel 300 on PASLA/PALM Interface, strapped For FDX operation and highest baud rate.
X'05'	Micro I/O Bus
X'00', X'06' -X'FF'	Reserved. Interpreted as X'02'.

APPENDIX A (continued)

1. The GDT, Terminals 550, 1100, 1200 or CRT, if used on PASLA/PALM interface, should be strapped for device addresses X'10' and X'11', for Receive and Transmit sides, respectively. If the addresses are different, then the halfword labeled 'PASLADR' (see the Program Listing) must be changed accordingly.
2. The Teletype or Current Loop Interface, if used, should be strapped for device address X'02'. If the address is different, the halfword labels 'CLIFADR' (see the Program Listing) must be changed accordingly.
3. The Line Printer, if used, should be strapped for device address X'62'. If the address is different, the halfword labeled 'LPADR' (see the Program Listing) must be changed accordingly.
4. The Carousel 300, if used, should be strapped for device addresses X'10' and X'11', for Receive and Transmit sides respectively. If the addresses are different, the halfword labeled 'C300ADR' (see the Program Listing) must be changed accordingly.
5. The Micro I/O Bus, if used, should be strapped for device address X'C0'. If the address is different, the halfword labeled MICROBUS (see the Program Listing) must be changed accordingly.

APPENDIX B  
COMMAND/OPTION INPUT METHOD

An asterisk (\*) is output to the console device to indicate that the program is waiting for user input. All option names must be typed in from the console followed by a space and the desired argument or arguments separated by commas. A carriage return CR must be typed to end every command/option input. An invalid command/option name or option value causes a question mark (?) followed by a carriage return CR, line feed (LF), and an asterisk (\*) to be output. If, during command/option entry, an error is made, it can be handled in two ways. The hash mark (#) can be typed to delete the entire line. This causes a carriage return CR, line feed (LF), and an asterisk (\*) to be output. The left arrow (←) can be typed to delete the previous character: or a string of characters can be deleted by typing a left arrow (←) for each character to be deleted.

APPENDIX C  
OPTIONS TABLE

OPTION	DEFAULT	TESTS	DESCRIPTION
BKSPAC	1	3,7	Selects backspace operation (see Note 3) 0 = no backspace 1 = perform backspace
BYTES	X'FF'	3,7	Number of bytes per record Minimum = 2 Maximum = X'400' (See Note 1)
COMPAR	1	3,7	Specifies data comparison 0 = no compare 1 = compare data
CONTIN	0	All	Enables the selected tests to be executed continuously until interrupted. 0 = normal execution 1 = continuous execution
CRC	0	6	Selects CRC check 0 = no CRC check 1 = perform CRC check
DATA	1	7	Specifies if external data pattern is to be requested. 0 = use program generated data pattern 1 = request for external data pattern
DEVADR	X'0085'	All	Specifies the physical device address of the device under test (must not be zero)
DEVICE	0	6	Selects 800 or 1600 bpi magnetic tape. 0 = 800 bpi drive 1 = 1600 bpi drive

APPENDIX C, (Continued)

OPTION	DEFAULT	TESTS	DESCRIPTION
DU	0	3	Tests DU interrupt 0 = no DU interrupt 1 = test DU interrupt
DUMP	0	0,1,3,4,7	Specifies read buffer dump 0 = no dump 1 - dump data buffer
DV2ADR	X'0000'	All	Specifies the physical and device address for the 2nd device to be tested. (Must be zero if only one device is under test).
FILES	1	1,7	Number of files to Write of Read Maximum = X'400' (See Note 1)
INTLEV	0	3	Specifies interrupt level of device (2) under test. The same level is assigned to both devices and SELCH.
IRG	X'10'	5	Number of times of read and back-space to be performed in gap-data check Maximum = X'FF' (See Note 1)
LOOP	0	All	Number of times the selected tests are to be repeated. Maximum = X'FFFF'.
MODE	2	All	Selects mode of operation 0 = selects both modes 1 = Read Block-Write Block 2 = SELCH mode Note: Mode 1 can not be used on the following tape drives: 1600 bpi, 45 IPS; 1600 bpi, 75 IPS; 800 bpi, 75 IPS.
NOMSG	0	All	Suppresses all messages except error messages. 0 = all messages 1 = only error messages

APPENDIX C, (Continued)

OPTION	DEFAULT	TESTS	DESCRIPTION
OPTION		All	Lists all option values selected. (See Note 2)
RDCRC	0	6	Specifies read CRC only 0 = Write and Read CRC 1 = Read CRC only
READ	1	3,7	Selects read operation 0 = no read 1 = perform read
RECFIL	X'100'	All	Number of records per file. Maximum = X'400' (See Note 1)
REPEAT	X'0003'	2	Number of skips to be performed. Maximum = X'FF' (See Note 1)
RSTART	0	All	Relocates the starting address of Read Buffer (See Note 4)
RUN		All	Starts Test.
SCOPE	0	7	Specifies scope loop 0 = no scope loop 1 = Write-Backspace-Read 2 = Write only 3 = Write-Backspace (avoid) 4 = Read only 5 = Skip
SELCH	X'00F0'	All	Specifies device address of selector channel.
SKIP	1	3	Selects skip operation (See Note 3) 0 = no skip 1 - perform skip
TEST	0,1,2,3,4,5	All	Selects test or tests to be executed (see Appendix E)



APPENDIX C (Continued)

OPTION	DEFAULT	TESTS	DESCRIPTION
CON		All	Gives control to CPU console. Legal only for series 16 & Series 32 processors.
TIMVAL	X'140'	All	Defines a 1 ms time for different models. Subroutine Init multiplies TIMVAL by 10 to obtain a basic delay time unit of 10 ms. X'D2' for 7/16 Basic, Model 74, or equivalent X'14D' for 7/16 HSAU (750 ns memory) X'134' for 7/16 HSAU (1000 ns memory) X'14A' for 6/16 MOS X'14D' for 8/16 (750 ns Memory) and equivalent X'134' for 6/16 (1000 ns Memory) and equivalent X'EB' for 7/32 (750 ns Memory) X'D2' for 7/32 (1000 ns Memory) X'DA' for 8/32 X'133' for Models 80, 85, and 60 X'C8' for Models 70, 50, and 55 X'260' for 3220 with CACHE X'1A4' for 3240 with CACHE X'14D' for Series 16 processors
TRACK	9	All	Defines number of tracks for the device. 7 = seven track drive 9 = nine track drive
WEOF	0	3,7	Write EOF mark continuously until EOT (See Note 3) 0 = Write/Read records 1 = Write EOF only
WRITE	1	3,7	Selects write operation (See Note 3) 0 = no write 1 = perform write

APPENDIX C (Continued)

OPTION	DEFAULT	TESTS	DESCRIPTION
WSTART	0	All	Relocates the starting address of Write Buffer (See Note 4)

NOTES

1. Minimum is 1. If 0 is entered, it is defaulted to 1.
2. A page of 20 options is listed at a time on the list device. At the end of each full page, the LF key must be depressed to continue listing the next page. If CR is depressed, the listing is terminated. The BREAK key is used to stop listing on any device.
3. Also see Appendix F.
4. If not entered, the Read buffer and Write buffer are defaulted to values within test program memory.

If RSTART or WSTART is entered, the relocated buffer is guarded against being located in the test program. If the user attempts to relocate the Read or Write buffers in the test program, one of the following error messages is printed:

ERROR: READ BUFFER IN TEST MEMORY  
or

ERROR: WRITE BUFFER IN TEST MEMORY

If the Read or Write buffers are relocated so that they overlap, an error message is printed as follows:

ERROR: READ BUFFER IN WRITE BUFFER

APPENDIX D  
Expected Result Table

Approx. Time to run on a  
7/16 BASIC with Default  
Options using a 45 IPS/800BPI  
MAG TAPE UNIT.

*TEST	
*RUN	
TEST 00	
NO ERROR	4.5 min.
TEST 01	
NO ERROR	0.75 min.
TEST 02	
NO ERROR	1.25 min.
TEST 03	
NO ERROR	1.75 min.
TEST 04	
NO ERROR	1.75 min
TEST 05	
NO ERROR	0.25 min
END OF TEST	

APPENDIX E

8	9	10	11	12	13	14	15
ERR	EOF	ET	NMTN	BSY	EX	EOM	DU

STATUS BYTE OF MAGNETIC TAPE CONTROLLER

ERROR TABLE

<u>ERROR NUMBER</u>	<u>TEST APPLICABLE</u>	<u>DESCRIPTION</u>
00	All	SELCH (ESELCH) or Magnetic Tape Drive device address does not return SYNC.
01	All	NMTN bit does not set within given time limit.
02	All	NMTN bit does not set after a REWIND operation.
04	All	EOM bit does not set within given time limit.
05	All	EOF bit does not set or EX and ERR bits set after a WRITE-END-OF-FILE-MARK operation.
06	0,2,6	EOF bit does not set or EX and ERR bits set after a READ operation.
07	0,2,3,5	EOF bit does not set or EX and ERR bits set after a skip and backspace operation.
08	0,1,2,3,4,5,7	EX bit sets after a BACKSPACE-RECORD operation.
09	2,3	ET bit does not set after completing REWIND operation.
10	All	EX bit sets after a WRITE-RECORD operation.
11	All	EX bit sets after a READ-RECORD operation.
12	0,1,2,4,5,6,7	DU, EX, BSY, EOM bit(s) set after a READ-BLOCK COMMAND (WB OR WBR).
13	0,1,2,4,5,6,7	DU, EX, BSY, EOM bit(s) set after a READ-BLOCK COMMAND (RB or RBR).

APPENDIX E, (Continued)

<u>ERROR NUMBER</u>	<u>TEST APPLICABLE</u>	<u>DESCRIPTION</u>
14	0,1,2,4,5,6,7	Terminating address of data transfer through SELCH (ESELCH) is not equal to the expected value (WRITE Mode).
15	0,1,2,4,5,6,7	Terminating address of data transfer through SELCH (ESELCH) is not equal to the expected value (READ Mode).
16	4	No error is detected when reading a written record with inaccurate record size.
17	4	ERR bit does not set after read of only part of a written record.
18	4	ERR bit does not set after reading a written record of over-size record length.
19	5	Tape does not stop at expected position after a BACKSPACE-RECORD operation.
20	3	No interrupt generated after a REWIND operation.
21	3	No interrupt generated after a WRITE-END-OF-FILE-MARK operation.
22	3	No interrupt generated when EOM and NMTN bits set.
23	3	No interrupt generated when NMTN bit sets after a WRITE-END-OF-FILE-MARK operation.
24	3	No interrupt generated after a BACKSPACE-FILE operation.
25	3	No interrupt generated after a BACKSPACE-RECORD operation.
26	3	No interrupt generated after a WRITE-BLOCK operation (WB or WBR).
27	3	No interrupt generated after a READ-BLOCK operation (RB or RBR).
28	3	No interrupt generated after data transfer through SELCH (ESELCH) terminates, in write mode.

APPENDIX E, (Continued)

<u>ERROR NUMBER</u>	<u>TEST APPLICABLE</u>	<u>DESCRIPTION</u>
29	3	No interrupt generated after data transfer through SELCH (ESELCH) terminates, in read mode.
30	3	No interrupt generated after SKIP-FILE-FORWARD operation.
31	3	No interrupt generated after SKIP-FILE-REVERSE operation.
32	3	No interrupt generated when tape drive is turned OFF-LINE.
33	3	DU bit does not set after tape drive is turned OFF-LINE.
34	3	No interrupt generated when tape drive is turned ON-LINE.
35	3	DU bit does not reset after tape drive is turned ON-LINE.
37	3	Interrupt cannot be queued while NMTN bit changes from 0 to 1 with magnetic tape drive interrupt enable and PSW changes from '70F0' to 30F0'.
38	3	Interrupt generated after issuing DISARM command to magnetic tape drive.
39	3	Interrupt generated after issuing DISABLE command to magnetic tape drive.
46	0,1,2,3,4,5,7	Read buffer does not match with write buffer.
47	0,1,2,3,4,5,7	Delimiter between read and write buffer is not equal to expected value.
48	6	CRC parity check error.
50	All	Write protect sets.
51	6	CRC checkword of zero expected.

APPENDIX F  
Optional Testing Table

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TEST 3

FUNCTIONS OPTIONS	WRITE EOF CONTINUOUS	WRITE ONLY	READ ONLY	WRITE BACKSPACE	WRITE BACKSPACE READ	WRITE REWIND READ	WRITE SKIP	READ SKIP	WRITE BACKSPACE SKIP	WRITE BACKSPACE READ SKIP	WRITE REWIND READ SKIP
WRITE	X	X	0	X	1	1	X	0	X	1	1
READ	X	0	1	0	1	1	0	1	0	1	1
WEOF	1	0	X	0	0	0	0	X	0	0	0
BKSPAC	X	0	X	1	1	0	0	X	1	1	0
SKIP	X	0	0	0	0	0	1	1	1	1	1

TEST 7

FUNCTIONS OPTIONS	WRITE EOF * CONTINUOUS	WRITE ONLY	READ ONLY	WRITE BACKSPACE	WRITE BACKSPACE READ	WRITE SKIP REVERSE READ
WRITE	X	X	0	X	1	1
READ	X	0	1	0	1	1
WEOF	1	0	X	0	0	0
BKSPAC	X	0	X	1	1	0

\* No error check for write EOF continuous in Test 7 (Scope check)

To obtain the desired function, each option specified on the left must be set to the value shown in the function column (note that an 'X' indicates that the option may be either '0' or '1').

F-1/F-2

APPENDIX G  
Related Documents

Test Program Listing	06-172M96R02
Test Program Paper Tape	06-J72M17R02
Magnetic Tape System Instruction Manuals	
9-Track, 800 bpi M46-470	29-503
9-Track, 1600 bpi M46-475	29-503
7-Track, 800 bpi M46-474	29-295
9-Track, 800 bpi M46-490	29-503
9-Track, 800/1600 bpi M46-494	29-503



PROG= CMT172 ASSEMBLED BY CAL 03-066R07-00 (32-BIT)

1	CMT172	PROG COMMON MAGNETIC TAPE TEST PROGRAM 06-172R03A13	CMT00010
2		WIDTH 120	CMT00030
3		CROSS	CMT00040
4		NLSTC	CMT00050
5		ERLST	CMT00060
6	*		CMT00070
7	*		CMT00080
8	*	*****	CMT00090
9	*	COPYRIGHT BY PERKIN ELMER CORPORATION MAY 1979 *	CMT00100
10	*		CMT00110
11	*	COMMON MAGNETIC TEST PROGRAM 06-172R03	CMT00120
12	*		CMT00130
13	*	PROGRAM USES THE COMMON INSTRUCTION SET	CMT00140
14	*		CMT00150
15	*	THIS PROGRAM TESTS THE MAGNETIC TAPE SYSTEM, AND THE	CMT00160
16	*	ASSOCIATED INTERFACES	CMT00170
17	*	THE PROGRAM CONSISTS OF 8 TESTS, WITH TEST 7 BEING	CMT00180
18	*	THE UTILITY TEST PROVIDING SCOPE LOOP.	CMT00190
19	*	THERE ARE 29 OPTIONS AVAILABLE TO THE USER AND 51	CMT00200
20	*	ERROR MESSAGES TO ENABLE ISOLATION OF A MALFUNCTION	CMT00210
21	*	TO THE HARDWARE LEVEL. ERROR RECOVERY IS PROVIDED	CMT00220
22	*	FOR CERTAIN DATA TRANSFER ERRORS.	CMT00230
23	*		CMT00240
24	*	THE PROGRAM REQUIRES EITHER 7/16 BASIC OR EQUIVALENT	CMT00250
25	*	PROCESSOR, OR 7/32 OR EQUIVALENT PROCESSOR WITH 16K	CMT00260
26	*	BYTES OF MEMORY. OPTIONS AND RUN COMMAND ARE TO BE	CMT00270
27	*	ENTERED VIA A CONSOLE DEVICE. EITHER ONE OR TWO	CMT00280
28	*	DEVICES CAN BE TESTED AT THE SAME TIME.	CMT00290
29	*		CMT00300
30	*	THE 06-172M17 TAPE IS AN ABSOLUTE TAPE WITH A FRONT-	CMT00310
31	*	END BOOT LOADER	CMT00320
32	*		CMT00330
33	*	TEST 0	CMT00340
34	*	TESTS ALL DATA LINES FOR CORRECT DATA TRANSFER WITH	CMT00350
35	*	WORST CASE DATA PATTERNS. THIS TEST IS MANDATORY,	CMT00360
36	*	AND IS EXECUTED AT LEAST ONCE.	CMT00370
37	*		CMT00380
38	*	TEST 1	CMT00390
39	*	TESTS THE ABILITY OF THE DEVICE TO WRITE AND READ	CMT00400
40	*	VARIABLE LENGTH RECORDS.	CMT00410
41	*		CMT00420
42	*	TEST 2	CMT00430
43	*	TESTS THE REWIND AND SKIP FUNCTION OF THE DEVICE	CMT00440
44	*		CMT00450
45	*	TEST 3	CMT00460
46	*	TESTS ALL DEVICE FUNCTIONS UNDER DEVICE INTERRUPT.	CMT00470
47	*	PROPER INTERRUPT RECEPTION, INTERRUPT QUEUING AND	CMT00480
48	*	INTERRUPT DISARM & DISABLE FUNCTIONS ARE ALL CHECKED.	CMT00490
49	*		CMT00500
50	*	TEST 4	CMT00510
51	*	THIS TEST IS DESIGNED TO TEST DEVICE OVERFLOW BY	CMT00520
52	*	WRITE-LONG READ-SHORT AND WRITE SHORT READ LONG	CMT00530
53	*		CMT00540

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54 * TEST 5 * CMT00550
55 * THIS TEST CHECKS THE PROPER GENERATION OF INTER-RECORD * CMT00560
56 * GAPS. (NOTE: PROLONGED REPETITION OF THIS TEST MAY * CMT00570
57 * WEAR THE FRONT PORTION OF THE TAPE.) * CMT00580
58 * * CMT00590
59 * TEST 6 * CMT00600
60 * THIS TEST CHECKS THE CYCLIC REDUNDANCY CHECK (CRC) * CMT00610
61 * CHARACTER. * CMT00620
62 * * CMT00630
63 * TEST 7 * CMT00640
64 * THIS IS A USER UTILITY TEST, PROVIDING COMPATIBILITY * CMT00650
65 * READ ONLY CHECK, SCOPE LOOP AND DATA PATTERN * CMT00660
66 * SELECTION. THE USER CAN SELECT NUMBER OF BYTES PER * CMT00670
67 * RECORD, NUMBER OF RECORDS PER FILE AND NUMBER OF * CMT00680
68 * FILES A WEOF OPTION IS PROVIDED TO WRITE EOF MARKS * CMT00690
69 * TO THE END OF TAPE. * CMT00700
70 * * CMT00710
71 * ANY COMBINATION OF THIS TESTS CAN BE SELECTED AS A * CMT00720
72 * STRING AND CAN BE LOOPED OR RUN CONTINUOUSLY. * CMT00730
73 * ***** * CMT00740
74 * * CMT00750
75 * * CMT00760
0000 0000 76 R0 EQU 0 CMT00770
0000 0001 77 R1 EQU 1 CMT00780
0000 0002 78 R2 EQU 2 CMT00790
0000 0003 79 R3 EQU 3 CMT00800
0000 0004 80 R4 EQU 4 CMT00810
0000 0005 81 R5 EQU 5 CMT00820
0000 0006 82 R6 EQU 6 CMT00830
0000 0007 83 R7 EQU 7 CMT00840
0000 0008 84 R8 EQU 8 CMT00850
0000 0009 85 R9 EQU 9 CMT00860
0000 000A 86 R10 EQU 10 CMT00870
0000 000B 87 R11 EQU 11 CMT00880
0000 000C 88 R12 EQU 12 CMT00890
0000 000D 89 R13 EQU 13 CMT00900
0000 000E 90 R14 EQU 14 CMT00910
0000 000E 91 RET EQU 14 CMT00920
0000 000F 92 R15 EQU 15 CMT00930
0000 000F 93 LINK EQU 15 CMT00940
0000 0004 94 CHAR EQU 4 * CMT00950
0000 0005 95 STAT EQU 5 * CMT00960
0000 0006 96 DEV EQU 6 * CMT00970
0000 0007 97 SELCH EQU 7 * CMT00980
98 * * CMT00990
99 * BOOTLOADER WITH CHKSUM CMT01000
100 * * CMT01010
0000R 101 CRC X'80' CMT01020
0080 2421 102 LIS R2,1 CMT01030
0082 2303 103 BS BOOT CMT01040
0084 3F00 104 DC Z(PSWSAVE) CURRENT PSW SAVE POINTER(32-BIT M/C) CMT01050
0086 3F08 105 DC Z(RSAVE) REGISTER SAVE POINTER(32-BIT M/C) CMT01060
0088 C810 0A00 106 BOOT LHI R1,ORIGIN1 R1 = ADR( FIRST BYTE OF TEST PROG ) CMT01070
008C C830 363A 107 LHI R3,LNZB+1 R3 = ADR( LAST NON-ZERO BYTE + 1 ) CMT01080
0090 4030 0022 108 STH R3,X'22' REGISTER SAVE POINTER (16-BIT M/C) CMT01090

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0094	2731	109	SIS	R3,1	R3 = ADR( LAST NON-ZERO BYTE )	CMT01100
0096	C860 0037	110	MN	LHI R6,X'37'	R6 = CHKSUM BYTE = X'MN'	CMT01110
009A	D340 0078	111		LB R4,X'78'	INPUT DEV ADR	CMT01120
009E	DE40 0079	112		OC R4,X'79'		CMT01130
00A2	9D45	113	LEADER	SSR R4,R5		CMT01140
00A4	2091	114		BTBS 9,1	DU,BSY	CMT01150
00A6	9845	115		RDR R4,R5		CMT01160
00A8	0855	116		LDAR R5,R5		CMT01170
00AA	2234	117		BZS LEADER	IGNORE LEADER	CMT01180
00AC	D251 0000	118	LOAD	STB R5,0(R1)	STORE 1ST NON-ZERO & SUBSEQUENT BYTE	CMT01190
00B0	D351 0000	119		LB R5,0(R1)	RELOAD DATA BYTE TO	CMT01200
00B4	0765	120		XAR R6,R5	GENERATE CHKSUM	CMT01210
00B6	9481	121		EXBR R8,R1		CMT01220
00B8	9828	122		WHR R2,R8	DISPLAY MEMORY ADDRESS	CMT01230
00BA	9C45	123		SSR R4,R5		CMT01240
00BC	2091	124		BTBS 9,1	DU,BSY	CMT01250
00BE	9F45	125		RDR R4,R5		CMT01260
00C0	C110 00AC	126		BXLE R1,LOAD	LOAD TILL LAST BYTE	CMT01270
00C4	9486	127		EXBR R8,R6		CMT01280
00C6	9828	128		WHR R2,R8	FINAL CHKSUM	CMT01290
00C8	2478	129	LDWT	LIS R7,8		CMT01300
00CA	917C	130		SLLS R7,12	R7 = X'8000'	CMT01310
00CC	9557	131		EPSR R5,R7	HALT PROCESSOR.	CMT01320
00CE	2203	132		BS LDWT		CMT01330

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

00D0		134	ORG	X'AO0'		CMT01350
0A00	4300 0A30	135	ORIGIN1	B START1	START HERE FOR 32-BIT PROCESSOR	CMT01360
	0C00 0A04	136	ORIGIN2	EQU *		CMT01370
0A04		137	IFZ	ADC-2		CMT01380
0A04	4300 0A46	138	B	START2	START HERE FOR 16-BIT PROCESSOR	CMT01390
0A08	4300 0A5E	139	ORIGIN3	B START3	SPECIAL 32-BIT PROCESSOR START	CMT01400
0A0C	4300 0A62	140	ORIGIN4	B START4		CMT01410
		141		ELSE		CMT01420
		145		ENDC		CMT01460
		146	*			CMT01470
		147	*-----*			CMT01480
		148	* TEST CONSTANTS			CMT01490
		149	*			CMT01500
0A10	0101	150	IO	DC X'0101'	I/O DEVICE(S) IDENTIFIER	CMT01510
0A12	1011	151	PASLADR	DC X'1011'	PASLA/PALM READ/WRITE ADDRESSES	CMT01520
0A14	0202	152	CLIFADR	DC X'0202'	CURRENT LOOP INTERFACE R/W ADDRESSES	CMT01530
0A16	6262	153	LPADR	DC X'6262'	LINE PRINTER ADDRESS	CMT01540
0A18	1011	154	C300ADR	DC X'1011'	CAROUSEL 300/PASLA ADDRESSES	CMT01550
0A1A	0000	155	MICROBUS	DC X'0000'	MICROBUS ADDRESS	CMT01560
0A1C	0000	156		DCX 0	PROVISION FOR SPECIAL DEVICE	CMT01570
		157	*			CMT01580
		158	* IO =	0101 FOR CRT ON PASLA		CMT01590
		159	*	0202 FOR TELETYPE, CAROUSEL 15/30		CMT01600
		160	*	XX03 FOR LINE PRINTER		CMT01610
		161	*	0404 FOR CAROUSEL 300		CMT01620
		162	*	0505 FOR MICROBUS		CMT01630
		163	*			CMT01640
0A1E	0140	164	TIME	DC X'140'	CONSTANT FOR 1 MS DELAY(X'C8'-MOD70)	CMT01650
0A20	0000	165		DCX 0	RESERVED	CMT01660
0A22	70F0	166	PSW	DCX 70F0	PSW USED IN PROGRAM	CMT01670
0A24	30F0	167	PSW2	DCX 30F0	PSW USED IN EXEC	CMT01680
0A26	0000	168		DCX 0	RESERVED	CMT01690
0A28	0000	169		DCX 0	RESERVED	CMT01700
0A2A	0000	170		DCX 0	RESERVED	CMT01710
0A2C	0000	171		DCX 0	RESERVED	CMT01720
0A2E	0000	172		DCX 0	RESERVED	CMT01730
		173	*-----*			CMT01740
		174	*			CMT01750
0A30	0711	175	START1	XAR R1,R1		CMT01760
0A32	4010 0030	176		STH R1,X'30'	DISABLE INT AT PROCESSOR LEVEL	CMT01770
0A36	4820 0A24	177		LH R2,PSW2		CMT01780
0A3A	4020 0032	178		STH R2,X'32'	SELECT REG SET 15	CMT01790
0A3E		179		IFZ ADC-2		CMT01800
0A3E	2521	180		LCS R2,1		CMT01810
0A40	4020 16C4	181		STH R2,MOD32	SET MODEL 32 PROCESSOR FLAG	CMT01820
0A44	230E	182		BS ST		CMT01830
0A46	3711	183	START2	XAR R1,R1		CMT01840
0A48	4010 16C4	184		STH R1,MOD32	RESET MOD 32 PROCESSOR FLAG	CMT01850
0A4C	4810 0A24	185		LH R1,PSW2		CMT01860
		186		ENDC		CMT01870
0A50	C820 0A66	187	ST	LHI R2,START		CMT01880
0A54	4010 0034	188		STH R1,X'34'		CMT01890
0A58	4020 0036	189		STH R2,X'36'	II INT NEW PSW LOC	CMT01900

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

0A5C	0000	190		DCX	0	TAKE AN ILLEGAL INSTRUCTION INT	CMT01910	
		191	*				CMT01920	
0A5E	4300 0A30	192	START3	B	START1	INSERT SPECIAL ROUTINE HERE	CMT01930	
0A62		193		IFZ	ADC-2		CMT01940	
0A62	4300 0A46	194	START4	B	START2	INSERT SPECIAL ROUTINE HERE	CMT01950	
		195		ENDC			CMT01960	
		196	*				CMT01970	
0A66	D310 0A10	197	START	LB	R1,I0	GET I/O IDENTIFIERS	CMT01980	
0A6A	D320 0A11	198		LB	R2,I0+1		CMT01990	
0A6E	2436	199		LIS	R3,6	IDENTIFIER CAN BE 1,2,3,4,5	CMT02000	
0A70	0513	200		CLHR	R1,R3		CMT02010	
0A72	2182	201		BLS	IO.OK1	BRANCH IF KB IDENTIFIER OK	CMT02020	
0A74	2411	202		LIS	R1,1	OTHERWISE FORCE IT TO BE PASLA	CMT02030	
0A76	0523	203	IO.OK1	CLHR	R2,R3		CMT02040	
0A78	2182	204		BLS	IO.OK2	SAME TEST FOR LIST DEVICE	CMT02050	
0A7A	2421	205		LIS	R2,1		CMT02060	
0A7C	D210 0A10	206	IO.OK2	STB	R1,I0	REESTABLISH VALUES	CMT02070	
0A80	D220 0A11	207		STB	R2,I0+1		CMT02080	
0A84	D362 16F4	208		LB	R6,CONRQ2S(R2)		CMT02090	
0A88	4060 16D8	209		STH	R6,PASFLG2	SET PASLA FLAG (LIST DEVICE)	CMT02100	
0A8C	0866	210		LDAR	R6,R6		CMT02110	
0A8E	2336	211		BZS	IO.OK3	SKIP IF NOT PASLA	CMT02120	
0A90	9121	212		SLHLS	R2,1		CMT02130	
0A92	D302 0A11	213		LB	R0,I0+1(R2)		CMT02140	
0A96	DE02 16E8	214		OC	R0,CON2ND(R2)	ISSUE 2ND COMMAND (LIST DEVICE)	CMT02150	
		215	*				CMT02160	
0A9A	41F0 1304	216	IO.OK3	BAL	LINK,SETKB	ESTABLISH KEYBOARD DEVICE	CMT02170	
0A9E	9310	217		LBR	R1,R0	(R1) = 1,2,4,5	CMT02180	
0AA0	9111	218		SLHLS	R1,1	(R1) = 2,4,6,A	CMT02190	
0AA2	4831 0A10	219		LH	R3,I0(R1)		CMT02200	
0AA6	4030 16DA	220		STH	R3,CONADR	SET UP CONSOLE DEVICE ADDRESS	CMT02210	
0AAA	4821 16DC	221		LH	R2,CONRD(R1)		CMT02220	
0AAE	4020 16DC	222		STH	R2,CONRD	SET UP R/W COMMANDS	CMT02230	
0AB2	4821 16E8	223		LH	R2,CON2ND(R1)		CMT02240	
0AB6	4020 16E8	224		STH	R2,CON2ND	2ND CMD; ENABLE READ CMD	CMT02250	
0ABA	9011	225		SRHLS	R1,1		CMT02260	
0ABC	D341 16F4	226		LB	R4,CONRQ2S(R1)		CMT02270	
0ACO	D240 16F4	227		STB	R4,CONRQ2S	CONSOLE REQUEST TO SEND	CMT02280	
0AC4	4040 16D6	228		STH	R4,PASFLG	SET PASLA FLAG (CONSOLE)	CMT02290	
0AC8	0844	229		LDAR	R4,R4		CMT02300	
0ACA	2333	230		BFFS	3,3	SKIP IF NOT PASLA	CMT02310	
0ACC	9422	231		EXBR	R2,R2		CMT02320	
0ACE	9E32	232		OCR	R3,R2	ISSUE 2ND COMMAND (CONSOLE)	CMT02330	
		233	*				CMT02340	
0ADO	41F0 1360	234		BAL	LINK,LCORE	SET UP LOW CORE	CMT02350	
0AD4	2400	235		LIS	R0,0		CMT02360	
0AD6	40C0 1704	236		STH	R0,WASDU	RESET 'DEVICE UNAVAILABLE' FLAG	CMT02370	
0ADA	41F0 11A2	237		BAL	LINK,CRLF		CMT02380	
0ADE	C8E0 1978	238		LHI	R5,TITLE		CMT02390	
0AE2	41F0 112A	239		BAL	R15,PRINT	PRINT TEST PROGRAM TITLE	CMT02400	
		240	*-----*					CMT02410
		241	*	KEYBOARD INPUT ROUTINE			CMT02420	
		242	*				CMT02430	

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

	0000	OAE6	243	OPTIN	EQU	*			CMT02440
OAE6	41F0	11A2	244		BAL	LINK,CRLF	CR,LF TO LIST DEVICE		CMT02450
	0000	OAEA	245	OPTIN1	EQU	*			CMT02460
OAEA	4820	0A24	246		LH	R2,PSW2			CMT02470
OAEA	9512		247		EPSR	R1,R2	NO INT. REG SET 15		CMT02480
OAF0	41F0	1304	248		BAL	LINK,SEIKB	ESTABLISH CONSOLE		CMT02490
OAF4	D340	17B4	249		L3	R4,AMSG	OUTPUT AN * TO INDICATE		CMT02500
OAF8	41F0	11B0	250		BAL	LINK,OUTCHR	COMMAND MODE ESTABLISHED		CMT02510
OAFc	2541		251		LCS	R4,1	X'FF'		CMT02520
OAFE	41F0	11B0	252		BAL	LINK,OUTCHR			CMT02530
OB02	C8C0	1250	253		LHI	R12,QUESTN	SET UP R12 FOR ERR ROUTINE		CMT02540
OB06	C800	2020	254		LHI	RO,X'2020'	BLANK OUT COMMAND BUFFER		CMT02550
OB0A	4000	3E40	255		STH	RO,OPTBUF	WHICH WILL CONTAIN OPTION		CMT02560
OB0E	40C0	3E42	256		STH	RO,OPTBUF+2	NAME		CMT02570
OB12	4000	3E44	257		STH	RO,OPTBUF+4			CMT02580
OB16	0711		258		XAR	R1,R1	CLEAR OPTBUF INDEX		CMT02590
OB18	41F0	121C	259	RDCHR	BAL	R15,GETCHR	GET A CHAR IN R4		CMT02600
OB1C	C540	0060	260		CLHI	R4,X'60'	UPPER CASE ALPHA ?		CMT02610
OB20	2183		261		BLS	RDCHARO	BRANCH IF NO.		CMT02620
OB22	C840	0020	262		SHI	R4,X'20'	CONVERT TO LOWER CASE		CMT02630
OB26	C540	0023	263	RDCHARO	CLHI	R4,X'23'	IS IT # ?		CMT02640
OB2A	4330	OAE6	264		BE	OPTIN			CMT02650
OB2E	C540	005F	265		CLHI	R4,X'5F'	LEFT ARROW, UNDERLINE OR DELETE ?		CMT02660
OB32	2139		266		BYES	RDCHR1			CMT02670
OB34	2711		267		SIS	R1,1	YES, DECREMENT INDEX		CMT02680
OB36	021C		268		BMR	R12	BUFFER UNDERFLOW; PRINT '??'		CMT02690
OB38	C8C0	0020	269		LHI	RO,X'20'			CMT02700
OB3C	D201	3E40	270		STB	RO,OPTBUF(R1)			CMT02710
OB40	4300	0B18	271		B	RDCHR			CMT02720
OB44	C540	000D	272	RDCHR1	CLHI	R4,X'0D'	IS IT CR ?		CMT02730
OB48	233C		273		BES	LOOKUP	YES, TRY MATCH		CMT02740
OB4A	C540	0020	274		CLHI	R4,X'20'	IS IT A BLANK?		CMT02750
OB4E	2339		275		BES	LOOKUP	YES, TRY MATCH		CMT02760
OB50	C510	0006	276		CLHI	R1,6	7 CHARACTERS INPUT ?		CMT02770
OB54	038C		277		BMLR	R12	IF YES, ERROR		CMT02780
OB56	D241	3E40	278		STB	R4,OPTBUF(R1)	STORE CURRENT BYTE		CMT02790
OB5A	2611		279		AIS	R1,1	BUMP BUFFER INDEX		CMT02800
OB5C	4300	0B18	280		B	RDCHR	READ NEXT CHARACTER		CMT02810
			281		-----				CMT02820
			282	*	OPTION MATCH ROUTINE				CMT02830
			283	*					CMT02840
OB60	C810	17B6	284	LOOKUP	LHI	R1,OPT	LOAD ADDRESS OF OPTION TABLE		CMT02850
OB64	0733		285	LOOK1	XAR	R3,R3	CLEAR BUFFER INDEX		CMT02860
OB66	08E1		286		LDAR	R6,R1	SET OPTION WORD INDEX		CMT02870
OB68	4856	0000	287	LOOK2	LH	R5,0(R6)			CMT02880
OB6C	021C		288		BMR	R12	IF MINUS, THEN NO MATCH = ERROR		CMT02890
OB6E	4553	3E40	289		CLH	R5,OPTBUF(R3)	COMPARE TO OPTBUF HW		CMT02900
OB72	2333		290		BES	LOOK3			CMT02910
OB74	261C		291		AIS	R1,12			CMT02920
OB76	2209		292		BS	LOOK1			CMT02930
OB78	2622		293	LOOK3	AIS	R3,2	TRY NEXT HW		CMT02940
OB7A	26E2		294		AIS	R6,2			CMT02950
OB7C	C530	0006	295		CLHI	R3,6	3 MATCHING HW FOUND ?		CMT02960

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

OB80	208C	296	BLS	LOOK2		CMT02970
		297	*			CMT02980
OB82	C510 1936	298	CLHI	R1,RUN	RUN COMMAND ?	CMT02990
OB86	4330 0D86	299	BE	RUNIT		CMT03000
OB8A	C510 192A	300	CLHI	R1,OPTION	OPTION CMD ?	CMT03010
OB8E	4230 0CFC	301	BNE	LOOK4	NO, LOOK FURTHER	CMT03020
		302				CMT03030
		303	*	* TO PROCESS INPUT COMMAND 'OPTION'		CMT03040
OB92	4820 1932	304	LH	R2,OPTION+8	CHECK FOR SPECIAL ROUTINE	CMT03050
OB96	0232	305	BNZR	R2	LINK TO ROUTINE	CMT03060
OB98	C830 17B6	306	OPTRTN	LHI R3,TEST	RETURN HERE	CMT03070
OB9C	C8E0 0C22	307	LHI	R14,OPTCMD8		CMT03080
OBA0	41F0 11A2	308	BAL	LINK,CRLF		CMT03090
OBA4	0722	309	OPTCMD	XAR R2,R2	RESET COUNTER	CMT03100
OBA6	D342 17B6	310	OPTCMD1	LB R4,OPT(R2)	TO PRINT TEST	CMT03110
OBAA	41F0 11B0	311	BAL	LINK,OUTCHR		CMT03120
OBAA	2621	312	AIS	R2,1		CMT03130
OB80	C520 0006	313	CLHI	R2,6		CMT03140
OB84	2087	314	BLS	OPTCMD1		CMT03150
OB86	C840 0020	315	LHI	R4,C' '		CMT03160
OB8A	41F0 11B0	316	BAL	LINK,OUTCHR	OUTPUT 1 SPACE	CMT03170
OB8E	0755	317	XAR	R5,R5	TO PRINT SELECTED TEST NUMBERS	CMT03180
OB80	4050 16C2	318	STH	R5,FIRST		CMT03190
OB84	4823 0006	319	LH	R2,6(R3)	FIRST TEST WORD	CMT03200
OB88	2440	320	OPTCMD2	LIS R4,0	START WITH TEST 0	CMT03210
OB8A	4040 3E3C	321	STH	R4,TEMP		CMT03220
OB8E	9121	322	OPTCMD3	SLHLS R2,1		CMT03230
OB80	4380 0C02	323	BNC	OPTCMD7		CMT03240
OB84	4040 3E3C	324	OPTCMD4	STH R4,TEMP	OPTION VALUE FOUND.	CMT03250
OB88	4800 16C2	325	LH	R0,FIRST	IS IT FIRST ?	CMT03260
OB8C	2335	326	BZS	OPTCMD5		CMT03270
OB8E	C840 002C	327	LHI	R4,C' ',	NO, OUTPUT COMMA	CMT03280
OB82	41F0 11B0	328	BAL	LINK,OUTCHR		CMT03290
OB86	40F0 16C2	329	OPTCMD5	STH LINK,FIRST		CMT03300
OB8A	0855	330	LDAR	R5,R5	TEST VALUE FROM SECOND HW	CMT03310
OB8E	2335	331	BZS	OPTCMD6	NO	CMT03320
OB82	C840 0031	332	LHI	R4,C' '1'	YES,OUTPUT '1'	CMT03330
OB86	41F0 11B0	333	BAL	LINK,OUTCHR		CMT03340
OB8A	4840 3E3C	334	OPTCMD6	LH R4,TEMP	RESTORE R4	CMT03350
OB8E	D344 171C	335	LB	R4,HEXTAB(R4)	CONVERT	CMT03360
OB82	41F0 11B0	336	BAL	LINK,OUTCHR	OUTPUT 0-F	CMT03370
OC02	4840 3E3C	337	OPTCMD7	LH R4,TEMP	RESTORE	CMT03380
OC06	2641	338	AIS	R4,1	INCREMENT TEST #	CMT03390
OC08	4040 3E3C	339	STH	R4,TEMP		CMT03400
OC0C	C540 0010	340	CLHI	R4,16		CMT03410
OC10	4280 0BCE	341	BL	OPTCMD3		CMT03420
OC14	0855	342	OPTCMD71	LDAR R5,R5	DONE ?	CMT03430
OC16	023E	343	BNZR	R14		CMT03440
OC18	4823 0008	344	LH	R2,8(R3)	SECOND TEST WORD	CMT03450
OC1C	2451	345	LIS	R5,1	R5 = 1 FOR SECOND TEST HW	CMT03460
OC1E	4300 0BC8	346	B	OPTCMD2		CMT03470
		347				CMT03480
		348	*	* TO OUTPUT OTHER OPTION NAMES & VALUES		CMT03490





## EXEC - ETPF R03P2 (W/CONDITIONAL ASSEMBLY)

OCBE	41F0	10DA	402	BAL	LINK,R5HEX	WRITE OPTION VALUE IN HEX (4 DIGITS)	CMT04030
OCC2	2661		403	AIS	R6,1	INCPMENT LINE COUNTER.	CMT04040
OCC4	C560	0014	404	CLHI	R6,20	PAGE FULL ?	CMT04050
OCC8	218C		405	BLS	OPTCMD12	NO	CMT04060
OCCA	0766		406	XAR	R6,R6	INITIALIZE LINE COUNT	CMT04070
OCCE	41F0	11B0	407	LIS	R4,X'0'	OUTPUT NULL	CMT04080
OCDE	2440		408	BAL	LINK,OUTCHR	TO CONSOLE	CMT04090
OCD2	41F0	121C	409	OPTCMD11	BAL LINK,GETCHR		CMT04100
OCD6	274D		410	SIS	R4,13	CR ?	CMT04110
OCD8	4330	OAE6	411	BZ	OPTIN	TO ACCEPT NEXT COMMAND	CMT04120
OCDC	2643		412	AIS	R4,3	LF ?	CMT04130
OCDE	2036		413	BNZS	OPTCMD11	IF YES, PRINT NEXT PAGE	CMT04140
OCEO	41F0	11A2	414	OPTCMD12	BAL LINK,CRLF		CMT04150
OCE4	41F0	126A	415	BAL	LINK,TSTBRK	EXIT IF 'BREAK' PRESSED.	CMT04160
OCE8	2626		416	AIS	R2,6		CMT04170
OCEA	C520	192A	417	CLHI	R2,OPTEND2	ALL PRINTING OPTIONS DONE ?	CMT04180
OCCE	4280	OCA0	418	BL	OPTCMD9	NO,LOOP FOR NEXT ONE	CMT04190
OCF2	2440		419	LIS	R4,X'0'	OUTPUT NULL	CMT04200
OCF4	41F0	11B0	420	BAL	LINK,OUTCHR	TO CONSOLE	CMT04210
OCF8	4300	OAEA	421	B	OPTIN1	TO ACCEPT NEXT COMMAND	CMT04220
			422				CMT04230
			423	*	TO PROCESS COMMANDS OTHER THAN 'TEST', 'OPTION'.		CMT04240
			424	*			CMT04250
OCFC	C510	17B6	425	LOOK4	CLHI R1,TEST	'TEST' OPTION ?	CMT04260
OD00	4330	OD34	426	BE	TESTOP		CMT04270
OD04	C510	1942	427	CLHI	R1,CON		CMT04280
OD08	2134		428	BNES	LOOK4A		CMT04290
OD0A	8800		429	DCX	8800		CMT04300
OD0C	4300	OAE6	430	B	OPTIN		CMT04310
OD10	274D		431	LOOK4A	SIS R4,13	OPT FOLLOWED BY CR ?	CMT04320
OD12	033C		432	BZR	R12	YES, ERROR	CMT04330
OD14	41E0	1068	433	BAL	R14,OPTVAL	GET OPTION VALUE IN R6	CMT04340
OD18	274D		434	SIS	R4,13	TERMINATED BY CR ?	CMT04350
OD1A	023C		435	BNZR	R12	IF NO, BRANCH	CMT04360
OD1C	48E1	0008	436	LH	R14,8(R1)	GET OPTION CHECK ROUTINE ADDRESS	CMT04370
OD20	2332		437	BZS	LOOK5		CMT04380
OD22	01FE		438	BALR	R15,R14	LINK OPTION CHECK ROUTINE	CMT04390
	0000	OD24	439	LOOK5	EQU *	RETURN HERE	CMT04400
OD24	4061	0006	440	STH	R6,6(R1)	STORE OPTION VALUE	CMT04410
OD28	4300	OAE6	441	B	OPTIN	TO ACCEPT NEXT COMMAND	CMT04420
			442	*			CMT04430
OD2C	C560	0400	443	ADR	CLHI R6,X'400'	(R6) = 10 BIT DEVICE ADDRESS	CMT04440
OD30	028F		444	BLR	R15	RETURN TO LOOK5	CMT04450
OD32	030C		445	BR	R12		CMT04460
			446				CMT04470
			447	*	TEST OPTION PROCESS ROUTINE		CMT04480
			448	*			CMT04490
OD34	274D		449	TESTOP	SIS R4,13	'TEST' FOLLOWED BY (CR) ?	CMT04500
OD36	2138		450	BNZS	ISTOP1		CMT04510
OD38	4800	1952	451	LH	RO,DEFTTESTS	YES, SET TEST OPTION TO	CMT04520
OD3C	4000	17BC	452	STH	RO,TEST+6	FIRST TEST WORD	CMT04530
OD40	4800	1954	453	LH	RO,DEFTTESTS+2	ALL DEFAULT TESTS IN PROGRAM	CMT04540
OD44	4000	17BE	454	STH	RO,TEST+8	SECOND TEST WORD	CMT04550

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

0D48	4300 0AE6	455	B	OPTIN	TO ACCEPT NEXT COMMAND	CMT04560
		456	*			CMT04570
0D4C	4E50 1950	457	TSTOP1	LH R5,MAXTST		CMT04580
0D50	2470	458		LIS R7,0	TEST BIT ACCUMULATORS	CMT04590
0D52	2480	459		LIS R8,0		CMT04600
0D54	41E0 1068	460	TSTOP2	BAL R14,OPTVAL	GET OPTION VALUE IN R6	CMT04610
0D58	0556	461		CLAR R5,R6		CMT04620
0D5A	029C	462		BLR R12	ERROR: INVALID TEST NUMBER	CMT04630
0D5C	C560 0010	463		CLHI R6,16	R6 < 16 ?	CMT04640
0D60	2385	464		BNLS TSTOP3	NO	CMT04650
0D62	41E0 10B2	465		BAL R14,UNARY	GET UNARY OPERAND IN R3	CMT04660
0D66	0673	466		OAR R7,R3	SET CURRENT BIT	CMT04670
0D68	2306	467		BS TSTOP4		CMT04680
0D6A	C560 0010	468	TSTOP3	SHI R6,16	R6 = 0-F	CMT04690
0D6E	41E0 10B2	469		BAL R14,UNARY		CMT04700
0D72	0683	470		OAR R8,R3	SET CURRENT BIT	CMT04710
0D74	274D	471	TSTOP4	SIS R4,13	TERMINATED BY CR ?	CMT04720
0D76	4230 0D54	472		BNZ TSTOP2		CMT04730
0D7A	4C70 17BC	473		STH R7,TEST+6	STORE VALID SELECTED TESTS	CMT04740
0D7E	4C80 17BE	474		STH R8,TEST+8		CMT04750
0D82	43C0 0AE6	475		B OPTIN	TO ACCEPT NEXT COMMAND	CMT04760
		476	*-----*			CMT04770
		477	*			CMT04780
	0700 0D86	478	RUNIT	EQU *		CMT04790
0D86	41F0 11A2	479		BAL LINK,CRLF		CMT04800
0D8A	4300 0A10	480		LH R0,IO		CMT04810
0D8E	4000 3E46	481		STH R0,IOSAVE	RESTORE USER'S I/O CHOICE	CMT04820
0D92	41F0 11A2	482		BAL LINK,CRLF		CMT04830
0D96	41F0 19A6	483		BAL LINK,INIT	LINK USER INITIALIZATION ROUTINE	CMT04840
	0000 0D9A	484	INITRET	EQU *	RETURN HERE	CMT04850
0D9A	C7FF	485		XAR R15,R15		CMT04860
0D9C	40F0 1706	486		STH R15,WASDU1		CMT04870
0DA0	24CF	487		LIS R0,15	TO FIND HIGHEST SELECTED TEST NO.	CMT04880
0DA2	4810 17BE	488		LH R1,TEST+8	CHECK SECOND TEST HW	CMT04890
0DA6	9011	489	KEEP1	SRLS R1,1		CMT04900
0DA8	218B	490		BCS FOUND1	R0 = F-0	CMT04910
0DAA	2701	491		SIS R0,1		CMT04920
ODAC	2213	492		BNMS KEEP1	TRY NEXT DIGIT	CMT04930
ODAE	24CF	493		LIS R0,15	INITIALIZE AGAIN	CMT04940
0DB0	4810 17BC	494		LH R1,TEST+6	CHECK FIRST TEST HW	CMT04950
0DB4	9011	495	KEEP2	SRLS R1,1		CMT04960
0DB6	2186	496		BCS FOUND1+4	R0 = F-0 = TEST #	CMT04970
0DB8	2701	497		SIS R0,1		CMT04980
0DBA	2213	498		BNMS KEEP2	LOOP	CMT04990
0DBC	030C	499		BR R12	TEST NOT SELECTED	CMT05000
0DBE	CA00 0010	500	FOUND1	AHI R0,16	ADJUST TEST # FOR SECOND HW	CMT05010
0DC2	4000 1702	501		STH R0,SELTST	HIGHEST SELECTED TEST NUMBER	CMT05020
		502	*			CMT05030
		503	* RESET TEST PARAMETERS			CMT05040
		504	*			CMT05050
0DC6	0700	505		XAR R0,R0		CMT05060
0DC8	40C0 16FE	506		STH R0,ISITERR	RESET ERROR FLAG	CMT05070
0DCC	40C0 1708	507		STH R0,TOTAL	RESET TOTAL	CMT05080

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

ODDO	4000 170A	508	STH	RO,TOTERR	RESET TOTERR	CMT05090
ODD4	4000 1704	509	STH	RO,WASDU	RESET WASDU	CMT05100
ODD8	C810 3030	510	LHI	R1,C'00'		CMT05110
ODDC	4010 1732	511	STH	R1,MTESTNO	RESET THESE FLAGS TO C'00'	CMT05120
ODE0	4010 173C	512	STH	R1,ETESTNO		CMT05130
ODE4	4010 173E	513	STH	R1,ERRNO		CMT05140
ODE8	41F0 1360	514	BAL	LINK,LCORE	SET UP LOW CORE	CMT05150
		515	*			CMT05160
		516	*	START SELECTION FROM TEST 0		CMT05170
		517	*			CMT05180
ODEC	0700	518	KEEP3	XAR RO,RO		CMT05190
ODEE	4000 170C	519	STH	RO,BTESTNO	RESET BINARY TEST NUMBER	CMT05200
ODF2	4000 1710	520	STH	RO,NEXTST	RESET NEXT TEST #	CMT05210
		521	*			CMT05220
		522	*	TO FIND THE NEXT SELECTED TEST.		CMT05230
		523	*			CMT05240
ODF6	4820 1710	524	KEEP4	LH R2,NEXTST	GET NEXT TEST #	CMT05250
ODFA	2408	525	KEEP41	LIS RO,8		CMT05260
ODFC	910C	526		SLHLS RO,12	RO = X'8000'	CMT05270
ODFE	CC02 0000	527		SRHL RO,0(R2)	RO = NEXT TEST BIT	CMT05280
OE02	C520 0010	528		CLHI R2,X'10'	NEXT TEST < 16	CMT05290
OE06	2185	529		BLS KEEP42		CMT05300
OE08	4400 17BE	530		NH RO,TEST+8	LOOK AT TEST HW 2	CMT05310
OE0C	2137	531		BNZS KEEP5		CMT05320
OE0E	2304	532		BS KEEP43		CMT05330
OE10	4400 17BC	533	KEEP42	NH RO,TEST+6	LOOK AT TEST HW 1	CMT05340
OE14	2133	534		BNZS KEEP5		CMT05350
OE16	2621	535	KEEP43	AIS R2,1		CMT05360
OE18	220F	536		BS KEEP41	LOOP FOR NEXT TEST #	CMT05370
OE1A	4020 170C	537	KEEP5	STH R2,BTESTNO	CURRENT TEST #	CMT05380
OE1E	0812	538		LDAR R1,R2	R1 = TEST # IN BINARY	CMT05390
OE20	2621	539		AIS R2,1		CMT05400
OE22	4020 1710	540		STH R2,NEXTST		CMT05410
OE26	2402	541		LIS RO,2	SET DIGITS TO PRINT = 2	CMT05420
OE28	C820 1732	542		LHI R2,MTESTNO	R2 = A(MTESTNO)	CMT05430
OE2C	41F0 1102	543		BAL LINK,HEXASC	STORE TEST # IN ASCII @ MTESTNO	CMT05440
OE30	4820 1732	544		LH R2,MTESTNO		CMT05450
OE34	4020 173C	545		STH R2,ETESTNO	STORE TEST # IN ASCII @ ETESTNO	CMT05460
OE38	41F0 126A	546		BAL LINK,TSTBRK	TEST BREAK	CMT05470
OE3C	C850 172C	547		LHI R5,TSTMSG		CMT05480
OE40	41F0 112A	548		BAL LINK,PRINT	PRINT 'TEST NN'	CMT05490
OE44	0700	549		XAR RO,RO		CMT05500
OE46	4000 1700	550		STH RO,NOERR	RESET ERROR FLAG	CMT05510
OE4A	4000 170E	551		STH RO,COUNT	RESET COUNT	CMT05520
OE4E	4810 0A24	552	KEEP6	LH R1,PSW2	DISABLE INTERRUPTS	CMT05530
OE52	9501	553		EPSR RO,R1		CMT05540
OE54	4820 170C	554		LH R2,BTESTNO	R2 = TEST #	CMT05550
OE58	9121	555		SLLS R2,LADC		CMT05560
OE5A	4812 1956	556		LDA R1,TESTS(R2)		CMT05570
OE5E	0301	557		BR R1	GO TO TEST MODULE	CMT05580
		558	*	-----		CMT05590
		559	*			CMT05600
		560	*	TEST MODULE END ROUTINE		CMT05610

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

			561	*					CMT05620
			562	TSTEND	EQU	*			CMT05630
OE60	0000	OE60	563		LH	R1,PSW2			CMT05640
OE64	4810	0A24	564		EPSR	RO,R1	DISABLE INT @ PROCESSOR LEVEL		CMT05650
OE66	9501		565		LH	RO,COUNT			CMT05660
OE6A	4800	170E	566		AIS	RO,1	INCREMENT COUNT		CMT05670
OE6C	2601		567		STH	RO,COUNT			CMT05680
OE70	4000	170E	568		CLH	RO,LOOP+6	IF COUNT > LOOP,		CMT05690
OE74	4500		569		BNLS	KEEP7	GO TO NEXT TEST MODULE		CMT05700
OE76	2385		570		BAL	LINK,TSTBRK	IF BREAK GO TO OPTIN		CMT05710
OE7A	41F0	126A	571		B	KEEP6	OTHERWISE, REPEAT SAME TEST		CMT05720
OE7E	4300	0E4E	572	KEEP7	LH	RO,NOERP	LOOK @ ERROR FLAG		CMT05730
OE82	4800	1700	573		BNZS	KEEP71			CMT05740
OE84	2135		574		LHI	R5,NOERMSG			CMT05750
OE88	C850	1752	575		BAL	LINK,PRINT	PRINT "NO ERROR"		CMT05760
OE8C	41F0	112A	576	KEEP71	LH	R1,BTESTNO	SET TEST #		CMT05770
OE90	4810	170C	577		CLH	R1,SELTST	IS THE LAST SELECTED TEST DONE ?		CMT05780
OE94	4510	1702	578		BL	KEEP4	NO, GO SELECT NEXT TEST		CMT05790
	4280	0DF6	579	*					CMT05900
			580	*			ALL THE SELECTED TESTS ARE NOW RUN		CMT05810
			581	*					CMT05820
			582	ABORT	EQU	*		COME HERE TO ABORT TEST SEQUENCE.	CMT05830
OE98	0000	OE98	583		NOP				CMT05840
OE9C	4200	0000	584		BAL	LINK,TSTDU	RETURN WITH R1 = DU BIT		CMT05850
OEAO	41F0	12DE	585		BNZ	KEEP9	IF DU, DISPLAY TOTAL		CMT05860
OEAA	4230	0EC9	586		LH	R1,WASDU1	WAS IT EVER ?		CMT05870
OEAB	4810	1706	587		BNZ	KEEP10	YES, PRINT TOTAL, TOTERR		CMT05880
OEAC	4230	0E10	588		BAL	LINK,TSTBRK			CMT05890
OEBO	41F0	126A	589		LH	R1,CONTIN+6	IF CONTIN = 1,		CMT05900
OEBA	4810	17EC	590		BNZ	KEEP3	GO TO TEST 0		CMT05910
OEBC	4230	0DEC	591		BAL	LINK,SETKB	KB DEVICE = LIST DEVICE		CMT05920
OEBC	41F0	1304	592		LHI	R5,EOTMSG			CMT05930
OECC	C850	17A4	593		BAL	LINK,PRINT	'END OF TEST'		CMT05940
OECC	41F0	112A	594		B	OPTIN			CMT05950
OECC	4300	0AE6	595						CMT05960
			596	*			ROUTINE INCREMENTS,DISPLAYS & CHECKS 'TOTAL'		CMT05970
			597	*					CMT05980
OECC	4010	1704	598	KEEP9	STH	R1,WASDU	SET 'WASDU' FLAG		CMT05990
OECC	4810	1708	599		LH	R1,TOTAL	INCREMENT TOTAL		CMT06000
OEEO	2611		600		AIS	R1,1			CMT06010
OEED	4010	1708	601		STH	R1,TOTAL			CMT06020
OEED	2421		602	KEEP91	LIS	R2,1			CMT06030
OEED	DE20	16D5	603		OC	R2,INCR	DISPLAY: INCREMENTAL MODE		CMT06040
OEED	4800	170A	604		LH	RO,TOTERR			CMT06050
OEED	9400		605		EXBR	RO,RO			CMT06060
OEED	9820		606		WHR	R2,RO	DISPLAY TOTERR		CMT06070
OEED	9401		607		EXBR	RO,R1	FORMAT FOR DISPLAY		CMT06080
OEED	9820		608		WHR	R2,RO	DISPLAY TOTAL		CMT06090
OEED	DE20	16D4	609		OC	R2,NORM	DISPLAY: NORMAL MODE		CMT06100
OEED	C510	7FFF	610		CLHI	R1,X'7FFF'	TOTAL < MAX RETAINABLE ?		CMT06110
OEED	2389		611		BNLS	HALT9			CMT06120
OEED	4800	170C	612		LH	RO,BTESTNO	RO = CURRENT TEST #		CMT06130
OEED	4500	1702	613		CLH	RO,SELTST	IS IT LAST TEST ?		CMT06140

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

0EFA	4280	0DF6	614	BL	KEEP4	NO, GO TO NEXT TEST	CMT06150
0EFE	4300	0DEC	615	B	KEEP3	GO TO TEST 0	CMT06160
			616	*			CMT06170
0F02	C810	090F	617	HALT9	LHI R1,X'80F'		CMT06180
0F06	9114		618	SLHLS	R1,4	(R1) = X'80F0'	CMT06190
0F08	9521		619	EPSR	R2,R1	HALT PROCESSOR	CMT06200
			620	*			CMT06210
			621	*	WHEN EXE/RUN IS PRESSED, PRINT TOTAL & TOTERR		CMT06220
			622	*			CMT06230
0FOA	41F0	12DE	623	BAL	LINK,TSTDU	SEE IF LIST DEV IS ON	CMT06240
0FOE	2036		624	BNZS	HALT9	NO, HALT	CMT06250
0F10	0700		625	KEEP10	XAR RO,RO		CMT06260
0F12	40C0	1704	626	STH	RO,WASDU	RESET FLAG	CMT06270
0F16	41F0	11A2	627	BAL	LINK,CRLF		CMT06280
0F1A	C850	1742	628	LHI	R5,TOTMSG		CMT06290
0F1E	4050	16FE	629	STH	R5,ISITERR		CMT06300
0F22	41F0	112A	630	BAL	LINK,PRINT	PRINT 'TOTAL TOTERR'	CMT06310
0F26	2404		631	LIS	RO,4	TO PRINT 4 HEX DIGITS	CMT06320
0F28	4850	1708	632	LH	R5,TOTAL		CMT06330
0F2C	41F0	10DA	633	BAL	LINK,R5HEX	PRINT TOTAL IN HEX	CMT06340
0F30	2434		634	LIS	R3,4		CMT06350
0F32	C840	0020	635	LHI	R4,C'	SPACE	CMT06360
0F36	41F0	11B0	636	KEEP101	BAL LINK,OUTCHR	OUTPUT IT	CMT06370
0F3A	2731		637	SIS	R3,1		CMT06380
0F3C	2023		638	BPS	KEEP101	4 TIMES	CMT06390
0F3E	2404		639	LIS	RO,4	TO PRINT 4 HEX DIGITS	CMT06400
0F40	4850	170A	640	LH	R5,TOTERR		CMT06410
0F44	41F0	10DA	641	BAL	LINK,R5HEX	PRINT TOTERR IN HEX	CMT06420
0F48	4300	0AE6	642	B	OPTIN	GO TO BEGINNING	CMT06430
			643	*	*****		CMT06440
			644	*	ERROR ROUTINES	(OVERRIDE NOMSG OPTION)	CMT06450
			645	*			CMT06460
0F4C	D000	3FC8	646	ERR	STM RO,ERRSAVE	STORE REGISTERS	CMT06470
0F50	4120	0FB2	647	BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	CMT06480
0F54	41E0	0FE4	648	BAL	RET,ERR1	PRINT 'ERROR TTNN'	CMT06490
0F58	0700		649	ERRCOM2	XAR RO,RO		CMT06500
0F5A	4000	16FE	650	STH	RO,ISITERR	RESET ERROR FLAG	CMT06510
0F5E	4820	0A24	651	LH	R2,PSW2	***	CMT06520
0F62	9502		652	EPSR	RO,R2		CMT06530
0F64	D100	3FC8	653	LM	RO,ERRSAVE	RESTORE REGISTERS	CMT06540
0F68	030F		654	BR	LINK	RETURN TO TEST	CMT06550
0F6A	4000	173E	655	ERRD	STH RO,ERRNO	SAVE ERROR NUMBER	CMT06560
0F6E	D000	3FC8	656	STM	RO,ERRSAVE	STORE REGISTERS	CMT06570
0F72	4120	0FB2	657	BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	CMT06580
0F76	41E0	0FE4	658	BAL	RET,ERR1	PRINT 'ERROR TTNN'	CMT06590
0F7A	41E0	0FEE	659	BAL	RET,ERRD1	PRINT 'DEV DDD'	CMT06600
0F7E	4300	0F58	660	B	ERRCOM2		CMT06610
0F82	D000	3FC8	661	ERRDS	STM RO,ERRSAVE	STORE REGISTERS	CMT06620
0F86	41E0	33F2	662	BAL	RET,ERRDSA	SET UP ERROR NUM AND STATUS BYTE **	CMT06630
0F8A	4120	0FB2	663	BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	CMT06640
0F8E	41E0	0FE4	664	BAL	RET,ERR1	PRINT 'ERROR TTNN'	CMT06650
0F92	41E0	101E	665	BAL	RET,ERRDS1	PRINT 'DEV DDD STA SS'	CMT06660
0F96	4300	0F58	666	B	ERRCOM2		CMT06670

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

OF9A	D000	3FC8	667	ERRALL	STM	R0,ERRSAVE	STORE REGISTERS	CMT06680
OF9E	4120	0FB2	668		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	CMT06690
OFA2	41E0	0FE4	669		BAL	RET,ERR1	PRINT 'ERROR TTNN'	CMT06700
OFA6	41E0	101E	670		BAL	RET,ERRDS1	PRINT 'DEV DDD STA SS'	CMT06710
OFAA	41E0	1044	671		BAL	RET,ERRPL1	PRINT 'PSW PPPP LOC LLLL'	CMT06720
OFAE	43C0	0F58	672		B	ERRCOM2		CMT06730
			673	*				CMT06740
			674	*		COMMON ERROR ROUTINE		CMT06750
			675	*				CMT06760
OFB2	4020	0FCC	676	ERRCOM	STH	R2,COMRET		CMT06770
OFB6	4810	0A24	677		LH	R1,PSW2		CMT06780
OFBA	95C1		678		EPSR	R0,P1	DISABLE INT. @ PROCESSOR LEVEL	CMT06790
OFBC	41F0	12DE	679		BAL	LINK,TSTDU	GET LIST DEVICE DU BIT IN R1	CMT06800
OFC0	2137		680		BNZS	ERRCOM1	BRANCH IF OFF-LINE	CMT06810
OFC2	4020	16FE	681		STH	R2,ISITERR	SET ERROR FLAG	CMT06820
OFC6	4020	1700	682		STH	R2,NOERP		CMT06830
OFCA	43C0	0FCA	683		B	*	GO, PRINT ERROR MESSAGE	CMT06840
	0000	0FCC	684	COMPET	EQU	*-2		CMT06850
			685	*				CMT06860
OFCE	4810	170A	686	ERRCOM1	LH	R1,TOTERR	LIST DEVICE IS OFF	CMT06870
OFD2	2611		687		AIS	P1,1		CMT06880
OFD4	4010	170A	688		STH	R1,TOTERR	INCREMENT TOTERR	CMT06890
OFD8	C510	7FFF	689		CLHI	R1,X'7FFF'	TOTERR < MAX RETAINABLE ?	CMT06900
OFDC	4280	0ED6	690		BL	KEEP91	NO, ABORT CURRENT TEST & GOTO NEXT	CMT06910
OFEO	4300	0F02	691		B	HALT9	YES, HALT PROCESSOR	CMT06920
			692	-----				CMT06930
			693	*		MESSAGE PRINT ROUTINES	(DO NOT OVERRIDE NOMSG OPTION)	CMT06940
			694	*				CMT06950
			695	*		TO PRINT 'ERROR TTNN'		CMT06960
			696	*				CMT06970
OFF4	C850	1736	697	ERR1	LHI	R5,ERRMSG		CMT06980
OFF8	41F0	112A	698		BAL	LINK,PRINT	PRINT 'ERROR TTNN'	CMT06990
			699	*			TT = TEST #, NN = ERROR #	CMT07000
OFEC	030E		700		BR	RET	RETURN	CMT07010
			701	*				CMT07020
			702	*		TO PRINT 'DEV DDD'		CMT07030
			703	*				CMT07040
OFEE	2403		704	ERRD1	LIS	R0,3	SET UP DIGITS = 3	CMT07050
OFF0	4810	16D0	705		LH	R1,ERRDEV	R1 = ERROR DEV # IN BINARY	CMT07060
OFF4	C820	1770	706		LHI	R2,ASCDEV2		CMT07070
OFF8	41F0	1102	707		BAL	LINK,HEXASC	CONVERT IT TO ASCII	CMT07080
OFFC	C850	176C	708		LHI	R5,DEVMSG2		CMT07090
1000	41F0	112A	709		BAL	LINK,PRINT	PRINT 'DEV DD'	CMT07100
1004	030E		710		BR	RET	RETURN	CMT07110
			711	*				CMT07120
			712	*		TO PRINT 'STA SS'		CMT07130
			713	*				CMT07140
1006	2402		714	ERRS1	LIS	R0,2	SET UP DIGITS = 2	CMT07150
1008	D310	16D2	715		LB	R1,ERRSTA	R1 = ERROR STATUS	CMT07160
100C	C820	1768	716		LHI	R2,ASCISTA		CMT07170
1010	41F0	1102	717		BAL	LINK,HEXASC	CONVERT IT TO ASCII	CMT07180
1014	C850	1764	718		LHI	R5,STAMSG		CMT07190
1018	41F0	112A	719		BAL	LINK,PRINT	PRINT 'STA SS'	CMT07200

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

101C	030E	720	BR	RET	RETURN		CMT07210
		721	*				CMT07220
		722	*	TO PRINT 'DEV DDD STA SS'			CMT07230
		723	*				CMT07240
101E	2403	724	ERRDS1	LIS R0,3	SET UP DIGITS = 3		CMT07250
1020	4810 16D0	725	LH	R1,ERRDEV	R1 = ERROR DEV #		CMT07260
1024	C820 1760	726	LHI	R2,ASCIDEV			CMT07270
1028	41F0 1102	727	BAL	LINK,HEXASC	CONVERT IT TO ASCII		CMT07280
102C	2402	728	LIS	R0,2	SET UP DIGITS = 2		CMT07290
102E	D310 16D2	729	LB	R1,ERRSTA	R1 = EPROR STATUS		CMT07300
1032	C820 1768	730	LHI	R2,ASCISTA			CMT07310
1036	41F0 1102	731	BAL	LINK,HEXASC	CONVERT IT TO ASCII		CMT07320
103A	C850 175C	732	LHI	R5,DEVMSG			CMT07330
103E	41F0 112A	733	BAL	LINK,PRINT	PRINT 'DEV DD STA SS'		CMT07340
1042	030E	734	BR	RET	RETURN		CMT07350
		735	*				CMT07360
		736	*	TO PRINT 'PSW PPPP LOC LLLL'			CMT07370
		737	*				CMT07380
1044	2404	738	ERRPL1	LIS R0,4	SET UP DIGITS = 4		CMT07390
1046	4810 16CA	739	LH	R1,OPSW	R1 = OLD PSW		CMT07400
104A	C820 177A	740	LHI	R2,ASCIPSW			CMT07410
104E	41F0 1102	741	BAL	LINK,HEXASC	CONVERT IT TO ASCII		CMT07420
1052	4810 16CE	742	LH	R1,OLOC	R1= OLD LOC		CMT07430
1056	C820 1784	743	LHI	R2,ASCILOC			CMT07440
105A	41F0 1102	744	BAL	LINK,HEXASC	CONVERT IT TO ASCII		CMT07450
105E	C850 1776	745	LHI	R5,PSWMSG			CMT07460
1062	41F0 112A	746	BAL	LINK,PRINT	PRINT 'PSW PPPP LOC LLLL'		CMT07470
1066	030E	747	BR	RET	RETURN		CMT07480
		748	*	*****			CMT07490
		749	*	TO OBTAIN OPTION VALUE IN R6	(16 BITS, TARGT 16)		CMT07500
		750	*				CMT07510
1068	0766	751	OPTVAL	XAR R6,R6	INITIALIZE ACCUMULATOR		CMT07520
106A	41F0 121C	752	BAL	R15,GETCHR	GET A CHAR IN R4		CMT07530
106E	24FF	753	OPTVAL0	LIS R15,15			CMT07540
1070	D44F 171C	754	OPTVAL1	CLB R4,HEXTAB(R15)	SCAN TABLE		CMT07550
1074	2334	755	BES	OPTVAL2	MATCH		CMT07560
1076	27F1	756	SIS	R15,1			CMT07570
1078	2214	757	BNMS	OPTVAL1			CMT07580
107A	030C	758	BR	R12	ERROR; VALUE NOT IN TABLE.		CMT07590
107C	4890 16C4	759	OPTVAL2	LH R3,MOD32	.	**	CMT07600
1080	2133	760	BNZS	OPTVAL5	.	**	CMT07610
1082	9164	761	SLLS	R6,4	.	**	CMT07620
1084	2302	762	BS	OPTVAL6	.	**	CMT07630
1086	1164	763	OPTVAL5	DC X'1164'	.	**	CMT07640
1088	066F	764	OPTVAL6	OAR R6,R15	.	**	CMT07650
108A	41F0 121C	765	OPTVAL3	BAL R15,GETCHR	GET NEXT CHAR		CMT07660
108E	C540 005F	766	CLHI	R4,X'5F'	IS IT LEFT ARROW ?		CMT07670
1092	2138	767	BNES	OPTVAL4			CMT07680
1094	4890 16C4	768	LH	R9,MOD32	.	**	CMT07690
1098	2133	769	BNZS	OPTVAL7	.	**	CMT07700
109A	9064	770	SRLS	R6,4	.	**	CMT07710
109C	2302	771	BS	OPTVAL8	.	**	CMT07720
109E	1064	772	OPTVAL7	DC X'1064'	.	**	CMT07730

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10A0	220B	773	OPTVAL8	BS	OPTVAL3	.	**	CMT07740	
10A2	C540 000D	774	OPTVAL4	CLHI	R4,13	EXIT IF CR		CMT07750	
10A6	033E	775		BER	R14			CMT07760	
10A8	C540 002C	776		CLHI	R4,X'2C'	OR COMMA		CMT07770	
10AC	4230 106E	777		BNE	OPTVAL0	LOOP TO PROCESS		CMT07780	
10B0	030E	778		BR	R14	RETURN		CMT07790	
		779	-----						CMT07800
		780	* TO CONVERT (R6) FROM BINARY TO UNARY PATTERN, IN R3						CMT07810
		781	*						CMT07820
10B2	2431	782	UNARY	LIS	R3,1	INITIALIZE		CMT07830	
10B4	C560 000F	783	UNARY1	CLHI	R6,15	DONE ?		CMT07840	
10B8	033E	784		BER	R14	RETURN		CMT07850	
10BA	0A33	785		AAR	R3,R3	NO. SHIFT R3.		CMT07860	
10BC	2661	786		AIS	R6,1	INCREMENT COUNTER		CMT07870	
10BE	2205	787		BS	UNARY1			CMT07880	
		788	-----						CMT07890
		789	* TO PROVIDE # OF MILLISECONDS DELAY SPECIFIED BY R0						CMT07900
		790	*						CMT07910
10C0	D000 3F08	791	TIMER	STM	R0,RSAVE	SAVE REGISTERS		CMT07920	
10C4	2410	792		LIS	R1,0			CMT07930	
10C6	2421	793		LIS	R2,1			CMT07940	
10C8	4830 0A1E	794		LH	R3,TIME	R3 = TIME CONSTANT FOR 1 MS DELAY		CMT07950	
10CC	C110 10CC	795		BXLE	R1,*			CMT07960	
10D0	2701	796		SIS	R0,1			CMT07970	
10D2	2037	797		BNZS	TIMER+4	LOOP TILL SPECIFIED DELAY		CMT07980	
10D4	D100 3F08	798		LM	R0,RSAVE	RESTORE REGISTERS		CMT07990	
10D8	030F	799	TIMT	BR	LINK	RETURN		CMT08000	
		800	-----						CMT08010
		801	* R5HEX PRINTS CONTENTS OF R5 IN HEX						CMT08020
		802	* PRINTS UPTO 4 DIGITS (8 DIGITS, TARGT 32)						CMT08030
		803	*						CMT08040
10DA	D000 3F08	804	R5HEX	STM	R0,RSAVE	STORE REGISTERS		CMT08050	
10DE	0820	905		LDAR	R2,R0	R2 = # OF DIGITS TO BE PRINTED		CMT08060	
10E0	2721	806		SIS	R2,1			CMT08070	
10E2	211D	807		BMS	R5XB			CMT08080	
10E4	9122	808		SLLS	R2,2	R2 = 4(DIGITS-1)		CMT08090	
10E6	0845	809	R5X	LDAR	R4,R5			CMT08100	
10E8	CC42 0000	810		SRAL	R4,0(R2)			CMT08110	
10EC	C440 000F	311		NHI	R4,15	R4 = HEX DIGIT		CMT08120	
10F0	D344 171C	812		LB	R4,HEXTAB(R4)			CMT08130	
10F4	41F0 11B0	813	R5XA	BAL	R15,OUTCHR			CMT08140	
10F8	2724	814		SIS	R2,4			CMT08150	
10FA	221A	815		BNMS	R5X	LOOP TILL ALL DIGITS		CMT08160	
10FC	D100 3F08	916	R5XB	LM	R0,RSAVE	RESTORE REGISTERS		CMT08170	
1100	030F	817		BR	LINK	RETURN		CMT08180	
		818	-----						CMT08190
		819	* TO CONVERT HEXADECIMAL DATA IN R1 TO ASCII CHAR & STORE @ 0(P2)						CMT08200
		820	*						CMT08210
1102	D000 3F08	821	HEXASC	STM	R0,RSAVE	STORE REGISTERS		CMT08220	
1106	0830	922		LDAR	R3,R0	R3 = DIGITS		CMT08230	
1108	9132	923		SLLS	R3,2			CMT08240	
110A	2734	924		SIS	R3,4	R3 = 4(DIGITS)-4		CMT08250	
110C	0841	825	HEXASC1	LDAR	R4,R1	R4 = HEX DATA		CMT08260	



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110E	CC43 0000	826	SRAL	R4,0(R3)		CMT08270
1112	C440 000F	827	NHI	R4,15	R4 = HEX DIGIT TO BE CONVERTED	CMT08280
1116	D344 171C	828	LB	R4,HEXTAB(R4)		CMT08290
111A	D242 0000	829	STB	R4,0(R2)	STORE ASCII CHAR	CMT08300
111E	2621	830	AIS	R2,1		CMT08310
1120	2734	831	SIS	R3,4		CMT08320
1122	221B	832	BNMS	HEXASC1	LOOP TILL ALL DIGITS	CMT08330
1124	D100 3F08	833	LM	R0,RSAVE	RESTORE REGISTERS	CMT08340
1128	030F	834	BR	LINK	RETURN	CMT08350
		835	*-----*			CMT08360
		836	* TO PRINT THE ASCII MESSAGE			CMT08370
		837	*			CMT08380
112A	D000 3F08	838	PRINT	STM R0,RSAVE	STORE REGISTERS	CMT08390
112E	41F0 12DE	839	BAL	LINK,TSTDU		CMT08400
1132	2335	840	BZS	P1		CMT08410
1134	4010 1704	841	STH	R1,WASDU	SET FLAG	CMT08420
1138	4300 1198	842	B	PRINT5	EXIT	CMT08430
113C	4820 1704	843	P1	LH R2,WASDU		CMT08440
1140	4330 116E	844	BZ	P3		CMT08450
1144	C810 0140	845	LHI	R1,X'140'	DELAY CONSTANT	CMT08460
1148	C800 1000	846	LHI	R0,X'1000'		CMT08470
114C	2701	847	SIS	R0,1		CMT08480
114E	2031	848	BTBS	3,1		CMT08490
1150	2711	849	SIS	R1,1		CMT08500
1152	2035	850	BTBS	3,5	LOOP TILL TIMEOUT	CMT08510
1154	0744	851	XAR	R4,R4		CMT08520
1156	4040 1704	852	STH	P4,WASDU		CMT08530
115A	2541	853	LCS	R4,1	CHARACTER = X'FF'	CMT08540
115C	4040 1706	854	STH	R4,WASDU1		CMT08550
1160	2434	855	LIS	R3,4		CMT08560
1162	41F0 11B0	856	P2	BAL LINK,OUTCHR		CMT08570
1166	2731	857	SIS	R3,1		CMT08580
1168	2023	858	BPS	P2		CMT08590
116A	4300 0F10	859	B	KEEP10	PRINT TOTAL, TOTERR	CMT08600
116E	4800 17F8	860	P3	LH R0,NOMSG+6		CMT08610
1172	2335	861	BZS	PRINT2	NO, PRINT ALL MESSAGES	CMT08620
1174	4800 16FE	862	LH	R0,ISITERR		CMT08630
1178	4330 1198	863	BZ	PRINT5	NOT AN ERROR MSG. EXIT	CMT08640
		864	*			CMT08650
117C	D345 0900	865	PRINT2	L3 R4,J(R5)	GET A MESSAGE BYTE	CMT08660
1180	41F0 11B0	866	BAL	LINK,OUTCHR	OUTPUT IT	CMT08670
1184	274D	867	SIS	R4,13	CR ?	CMT08680
1186	2333	868	BZS	PRINT3	MSG OVER	CMT08690
1188	2651	869	AIS	R5,1		CMT08700
118A	2207	870	BS	PRINT2	LOOP FOR NEXT CHAR	CMT08710
118C	244A	871	PRINT3	LIS R4,10	LF	CMT08720
118E	41F0 11B0	872	BAL	LINK,OUTCHR	LF	CMT08760
1192	2541	873	LCS	R4,1	DEL	CMT08770
1194	41F0 11B0	874	PRINT3B	BAL LINK,OUTCHR	TERMINAL CHARACTER	CMT08800
1198	41F0 126A	875	PRINT5	BAL LINK,ISTBRK		CMT08810
119C	D100 3F08	876	LM	R0,RSAVE	RESTORE REGISTERS	CMT08820
11A0	030F	877	BR	LINK	RETURN	CMT08830
		878	*-----*			CMT08840

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			879	*	SMALL SUPPORT ROUTINES		CMT08850
			880	*			CMT08860
			881	*	TO OUTPUT CR,LF TO LIST DEVICE		CMT08870
			882	*			CMT08880
11A2	D000	3F08	883	CRLF	STM R0,RSAVE	STORE REGISTERS	CMT08890
11A6	244D		884		LIS R4,13		CMT08900
11A8	41F0	11B0	885		BAL LINK,OUTCHR	OUTPUT CR	CMT08910
11AC	4300	118C	886		B PRINT3	LINE FEED, RESTORE, RETURN	CMT08920
			887	*	-----		CMT08930
			888	*	TO OUTPUT A CHARACTER TO THE LIST DEVICE		CMT08940
11B0	40F0	1218	889	OUTCHR	STH R15,OUT1+2	SAVE RETURN ADDRESS	CMT08950
11B4	D300	3E47	890		LB R0,IOSAVE+1		CMT08960
11B8	2704		891		SIS R0,4		CMT08970
11BA	4230	11EC	892		BNZ OUTCHR2	BRANCH IF NOT CAROUSEL	CMT08980
11BE	4000	121A	893	OTC.	STH R0,PAUSE		CMT08990
11C2	41F0	12DE	894	OTC.0	BAL LINK,TSTDU	ON LINE ?	CMT09000
11C6	4230	1212	895		BNZ OUT0	NO, BRANCH	CMT09010
11CA	9D01		896		SSR R0,R1	GET CAROUSEL STATUS	CMT09020
11CC	2385		897		BFFS 8,OTC.1	BRANCH IF CHAR. IS TO BE READ	CMT09030
11CE	4810	121A	898		LH R1,PAUSE	PAUSED NOW ?	CMT09040
11D2	2038		899		BNZS OTC.0	YES, LOOP	CMT09050
11D4	230C		900		BS OUTCHR2	NO, GO OUTPUT CHARACTER	CMT09060
	0000	11D6	901	OTC.1	EQU *		CMT09070
11D6	9B01		902		RDR R0,R1	GET CAROUSEL CHARACTER	CMT09080
11D8	C410	007F	903		NHI R1,X'7F'		CMT09090
11DC	CE10	0012	904		SHI R1,X'12'	DC2 ?	CMT09100
11E0	2336		905		BZS OUTCHR2	YES, BRANCH	CMT09110
11E2	2712		906		SIS R1,2	DC4 ?	CMT09120
11E4	4330	11BE	907		BZ OTC.	YES, GO SET PAUSE FLAG	CMT09130
11E8	4300	11C2	908		B OTC.0	NO, GO WAIT FOR DC2	CMT09140
	0000	11EC	909	OUTCHR2	EQU *		CMT09150
11EC	4010	121A	910		STH R1,PAUSE	RESET FLAG	CMT09160
11F0	41F0	12DE	911		BAL LINK,TSTDU	OFF-LINE ?	CMT09170
11F4	213F		912		BNZS OUT0	BRANCH IF OFF-LINE	CMT09180
11F6	4110	134A	913		BAL R1,SETUP	SET UP FOR OUTPUT	CMT09190
11FA	9D01		914	OTC.4	SSR R0,R1	WAIT FOR NOT BUSY	CMT09200
11FC	2138		915		BTFS 3,OUT0	BRANCH IF OFF-LINE	CMT09210
11FE	C510	000C	916		CLHI R1,12	PASLA OFFLINE ?	CMT09220
1202	2338		917		BES OUT0	BRANCH: YES.	CMT09230
1204	C310	0008	918		THI R1,8	BUSY ?	CMT09240
1208	2037		919		BNZS OTC.4	WAIT FOR NOT BUSY.	CMT09250
120A	9A04		920		WDR R0,R4	OUTPUT DATA BYTE	CMT09260
120C	9D01		921		SSR R0,R1		CMT09270
120E	2081		922		BTBS 8,1	WAIT FOR NOT BUSY.	CMT09280
1210	2303		923		BS OUT1		CMT09290
1212	4010	1704	924	OUT0	STH R1,ASDU	SET FLAG	CMT09300
1216	4300	1216	925	CUT1	B *	RETURN AS SET UP ABOVE	CMT09310
121A	0000		926	PAUSE	DCX 0	SET DURING TRANSMISSION PAUSE	CMT09320
			927	*	-----		CMT09330
			928	*	TO GET A CHAR FROM KEYBOARD (IN REG R4)		CMT09340
			929	*			CMT09350
121C	4140	1312	930	GETCHR	BAL R4,KBREAD	PUT KB DEVICE IN READ MODE	CMT09360
1220	9D04		931		SSR R0,R4		CMT09370

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

1222	021F	932	BTCR	1, LINK	IF DU, RETURN	CMT09390
1224	2082	933	BTBS	8, 2	IF BUSY, LOOP	CMT09390
1226	D400 0A1A	934	CLB	R0, MICROBUS	IS IT BICROBUS ?	CMT09400
122A	2333	935	BES	ECHO1	YES, BRANCH	CMT09410
122C	9B04	936	RDR	R0, R4	READ A CHAR IN R4	CMT09420
122E	2303	937	BS	ECHO		CMT09430
1230	9B04	938	ECHO1	RDR R0, R4		CMT09440
1232	9A04	939	WDR	R0, R4		CMT09450
		940	* TO ECHO RECEIVED CHARACTERS TO CONSOLE DEVICE IN FDX MODE			CMT09460
1234	D390 16DC	941	ECHO	LB R9, CONRD		CMT09470
1238	C590 00A9	942	CLHI	R9, X'A9'	CAROUSEL ?	CMT09480
123C	2137	943	BNES	ECHRTN	DO NOT ECHO	CMT09490
123E	D390 16DB	944	LB	R9, CONADR+1		CMT09500
1242	DD90 16D3	945	SS	R9, SINK		CMT09510
1246	2082	946	BTBS	8, 2		CMT09520
1248	9A94	947	WDR	R9, R4	ECHO RECEIVED BYTE	CMT09530
124A	C440 007F	948	ECHRTN	NHI R4, X'7F'	REMOVE PARITY BIT	CMT09540
124E	030F	949	BR	LINK	RETURN	CMT09550
		950	*-----*			CMT09560
		951	* TO OUTPUT '?' TO CONSOLE			CMT09570
		952	*			CMT09580
1250	41F0 11A2	953	QUESTN	BAL LINK, CRLF		CMT09590
1254	40F0 16FE	954	STH	LINK, ISITERR	SET FLAG	CMT09600
1258	C850 17B2	955	LHI	R5, QMSG		CMT09610
125C	41F0 112A	956	BAL	LINK, PRINT	PRINT '?'	CMT09620
1260	0700	957	XAR	R0, R0		CMT09630
1262	4000 16FE	958	STH	R0, ISITERR		CMT09640
1266	4300 0AEA	959	B	OPTIN1	TO ACCEPT COMMAND INPUT	CMT09650
		960	*-----*			CMT09660
		961	* IF BREAK KEY DEPRESSED, GO TO 'OPTIN' OR (BRKVECT); ELSE RETURN.			CMT09670
		962	*			CMT09680
126A	D000 3F48	963	TSTBRK	STM R0, RSAVE+64	STORE REGISTERS	CMT09690
126E	40F0 12DC	964	STH	LINK, BRKRTN		CMT09700
1272	D300 16DA	965	LB	R0, CONADR	GET KEYBOARD DEVICE ADDR	CMT09710
1276	9D01	966	SSR	R0, R1		CMT09720
1278	C310 0020	967	THI	R1, X'20'	'BREAK' KEY PRESSED ?	CMT09730
127C	4330 12D0	968	BZ	TSTBRK3	NO. EXIT	CMT09740
1280	D320 0A10	969	LB	R2, IO		CMT09750
1284	C520 0005	970	CLHI	R2, 5	IS IT MICROBUS ?	CMT09760
1288	213D	971	BNES	TSTBRK4	NO, BRANCH	CMT09770
	0000 128A	972	TSTBRK5	EQU *		CMT09780
128A	9B02	973	RDR	R0, R2		CMT09790
128C	9D01	974	TSTBRK5A	SSR R0, R1		CMT09800
128E	C310 0020	975	THI	R1, X'20'		CMT09810
1292	4330 12C4	976	BZ	TSTBRK2		CMT09820
1296	C810 7FFF	977	LHI	R1, X'7FFF'		CMT09830
129A	2711	978	SIS	R1, 1		CMT09840
129C	2031	979	BTBS	3, 1		CMT09850
129E	4300 128A	980	B	TSTBRK5		CMT09860
	0000 12A2	981	TSTBRK4	EQU *		CMT09870
12A2	4820 16D6	982	LH	R2, PASFLG	PASLA ?	CMT09880
12A6	233B	983	BZS	TSTBRK1	BRANCH IF NO.	CMT09890
12A8	C310 0008	984	THI	R1, 8	ALREADY ACKNOWLEDGED ?	CMT09900

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

12AC	4230	12D0	985	BNZ	TSTBRK3	BRANCH IF YES	CMT09910
12B0	9802		986	RDR	RO,R2		CMT09920
12B2	9D01		987	SSR	RO,R1		CMT09930
12B4	2281		988	BFBS	8,1		CMT09940
12B6	0822		989	LDAR	R2,R2	ZERO CHARACTER ?	CMT09950
12B8	213C		990	BNZS	TSTBRK3	BRANCH: JUST FRAMING ERROR	CMT09960
12BA	2305		991	BS	TSTBRK2		CMT09970
12BC	9D01		992	TSTBRK1	SSR RO,R1		CMT09980
12BE	C310	0020	993	THI	R1,X'20'		CMT09990
12C2	2033		994	BTBS	3,3	WAIT FOR BREAK KEY RELEASE	CMT10000
12C4	48F0	16FC	995	TSTBRK2	LH R15,BRKVECT	CHECK FOR SPECIAL ROUTINE	CMT10010
12C8	4330	0AE6	996	BZ	OPTIN	BRK W/NO VECTOR: TO EXEC.	CMT10020
12CC	40F0	12DC	997	STH	R15,BRKRTN	SET UP FOR EXIT	CMT10030
12D0	2400		998	TSTBRK3	LIS RO,0		CMT10040
12D2	4000	16FC	999	STH	RO,BRKVECT	DELETE VECTOR AFTER ONE SHOT.	CMT10050
12D6	D100	3F48	1000	LM	RO,RSRVE+64	RESTORE REGISTERS	CMT10060
12DA	4300	12DA	1001	B	*	RETURN TO PROGRAM	CMT10070
	0000	12DC	1002	BRKRTN	EQU *-2		CMT10080
			1003	*	-----		CMT10090
			1004	*	SEE IF LIST DEVICE OFF-LINE (R1, CC NON-ZERO IF OFF)		CMT10100
			1005	*			CMT10110
12DE	D310	3E47	1006	TSTDU	LB R1,IOSAVE+1	GET LIST DEVICE IDENTIFIER	CMT10120
12E2	9111		1007	SLHLS	R1,1	(R1) = 2,4,6,8,A	CMT10130
12E4	D301	0A10	1008	LB	RO,IO(R1)	GET LIST DEVICE ADDRESS	CMT10140
12E8	9D01		1009	SSR	RO,R1		CMT10150
12EA	4880	16D8	1010	LH	R8,PASFLG2		CMT10160
12EE	2338		1011	BZS	TSTDU1	BRANCH IF LIST DEVICE NOT PASLA	CMT10170
12F0	C410	00FC	1012	NHI	R1,X'FC'		CMT10180
12F4	C510	000C	1013	CLHI	R1,X'0C'	BSY & EX SET ?	CMT10190
12F8	2133		1014	BNES	TSTDU1	BRANCH IF PASLA ON-LINE	CMT10200
12FA	0811		1015	LDAR	R1,R1		CMT10210
12FC	030F		1016	BR	LINK	PASLA OFF-LINE	CMT10220
12FE	C410	0001	1017	TSTDU1	NHI R1,1	(R1) = DU BIT	CMT10230
1302	030F		1018	BR	LINK	RETURN	CMT10240
			1019	*	-----		CMT10250
			1020	*	TO DIRECT INPUT AND OUTPUT TO CONSOLE DEVICE		CMT10260
			1021	*			CMT10270
1304	D300	0A10	1022	SETKB	LB RO,IO	GET KEYBOARD DEVICE	CMT10280
1308	9410		1023	EXBR	R1,RO		CMT10290
130A	0610		1024	OAR	R1,RO		CMT10300
130C	4010	3E46	1025	STH	R1,IOSAVE	KB DEVICE = LIST DEVICE	CMT10310
1310	030F		1026	BR	LINK	RETURN	CMT10320
			1027	*	-----		CMT10330
			1028	*	TO PUT KEYBOARD DEVICE IN READ MODE		CMT10340
			1029	*			CMT10350
1312	D300	16DA	1030	KBREAD	LB RO,CONADR		CMT10360
1316	DE00	16DC	1031	OC	RO,CONRD		CMT10370
131A	DB00	16D3	1032	RD	RO,SINK		CMT10380
131E	4890	16D6	1033	LH	R9,PASFLG	PASLA ?	CMT10390
1322	4200	1322	1034	NOP	*	FOR SPECIAL KB DEVICE	CMT10400
1326	0334		1035	TTYGET	BZR R4	RETURN	CMT10410
1328	DE00	16F4	1036	OC	RO,CONRQ2S		CMT10420
132C	0304		1037	BR	R4	RETURN	CMT10430

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

		1038	*-----*			CMT10440
		1039	* TO SET UP KEYBOARD DEV TO READ WITH INT ENABLED			CMT10450
		1040	*			CMT10460
132E	D000 3F08	1041	KBRD STM R0,RSAVE	SAVE REGISTERS		CMT10470
1332	D300 16DA	1042	LB R0,CONADR	GET KB DEV ADR		CMT10480
1336	4810 16D6	1043	LH R1,PASFLG	PASLA ?		CMT10490
133A	2333	1044	BZS KBRD1			CMT10500
133C	DE00 16F4	1045	OC R0,CONRQ2S			CMT10510
1340	DE00 15E9	1046	KBRD1 OC R0,CONENPD	CONSOLE : ENABLE, READ		CMT10520
1344	D100 3F08	1047	LM R0,RSAVE	RESTORE REGISTERS		CMT10530
1348	030F	1048	BR LINK	RETURN		CMT10540
		1049	*-----*			CMT10550
		1050	* LIST DEVICE SET UP ROUTINE			CMT10560
		1051	*			CMT10570
134A	4010 135E	1052	SETUP STH R1,SET.RTN			CMT10580
134E	D310 3E47	1053	LB R1,IOSAVE+1	GET LIST DEVICE IDENTIFIER		CMT10590
1352	9111	1054	SLHLS R1,1	HW INDEX		CMT10600
1354	D301 0A11	1055	LB R0,IO+1(R1)	GET LIST DEVICE ADDRESS		CMT10610
1358	DE01 16DD	1056	OC R0,CONWRT(R1)			CMT10620
135C	4300 135C	1057	B *	RETURN		CMT10630
	0000 135E	1058	SET.RTN EQU *-2			CMT10640
		1059	* *****			CMT10650
		1060	* LOW CORE SET UP ROUTINE			CMT10660
		1061	*			CMT10670
1360	0711	1062	LCORE XAR R1,R1			CMT10680
1362	2422	1063	LIS R2,2			CMT10690
1364	C830 004E	1064	LHI R3,X'4E'			CMT10700
1368	0700	1065	XAR R0,R0			CMT10710
136A	4001 0000	1066	ZERO1 STH R0,0(R1)			CMT10720
136E	C110 135A	1067	BXLE R1,ZERO1	ZERO CORE FROM 0 THRU X'4F'		CMT10730
1372	C810 0080	1068	LHI R1,X'80'			CMT10740
1376	C830 00CE	1069	LHI R3,X'CE'			CMT10750
137A	4001 0000	1070	ZEP02 STH R0,0(R1)			CMT10760
137E	C110 137A	1071	BXLE R1,ZERO2	ZERO CORE FROM X'80' THRU X'CF'		CMT10770
1382	C800 14B8	1072	LHI R0,XI32	INTERRUPT HANDLER ROUTINE		CMT10780
1386	C830 08CE	1073	LHI R3,X'8CE'			CMT10790
138A	4001 0000	1074	ZERO3 STH R0,0(R1)			CMT10800
138E	C110 138A	1075	BXLE R1,ZERO3	SET UP INT SERVICE POINTER TABLE		CMT10810
1392	C830 15C4	1076	LHI R3,II			CMT10820
1396	4030 0036	1077	STH R3,X'36'	ILL INST INT NEW PSW LOC		CMT10830
139A	C840 15DE	1078	LHI R4,MM			CMT10840
139E	4040 003E	1079	STH R4,X'3E'	M. M. INT NEW PSW LOC		CMT10850
13A2	C830 1590	1080	LHI R3,AF			CMT10860
13A6	4030 004E	1081	STH R3,X'4E'	ARITHMETIC FAULT NEW PSW LOC(32-BIT)		CMT10870
13AA	C830 1579	1082	LHI R3,ORFAULT	DATA FORMATFAULT		CMT10880
13AE	4030 00CE	1083	STH R3,X'CE'			CMT10890
		1084	*	FIXED PT DIVIDE FAULT NEW PSW LOC		CMT10900
13B2	C840 3F08	1085	LHI R4,RSAVE			CMT10910
13B6		1086	IFZ ADC-2			CMT10920
13B6	4810 16C4	1087	LH R1,MOD32			CMT10930
13BA	4230 13DC	1088	BNZ LCORE32			CMT10940
		1089	*			CMT10950
		1090	* SET UP LOW CORE FOR 16 BIT MACHINE			CMT10960

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		1091	*				CMT10970	
13BE	4040 0022	1092		STH	R4,X'22'	REG SAVE POINTER	CMT10980	
13C2	C830 157E	1093		LHI	R3,FP		CMT10990	
13C6	4030 002E	1094		STH	R3,X'2E'	FLOATING PT FAULT INT NEW PSW LOC	CMT11000	
13CA	4850 0A24	1095		LH	R5,PSW2		CMT11010	
13CE	4050 0044	1096		STH	R5,X'44'	HW EXT INT NEW PSW STATUS	CMT11020	
13D2	C850 14AA	1097		LHI	R5,XI16		CMT11030	
13D6	4050 0046	1098		STH	R5,X'46'	EXT INT NEW PSW LOC	CMT11040	
13DA	030F	1099		BR	LINK		CMT11050	
		1100			ENDC		CMT11060	
		1101	*				CMT11070	
		1102	*	SET UP LOW CORE FOR 32 BIT MACHINE			CMT11080	
		1103	*				CMT11090	
13DC	4040 0086	1104	LCORE32	STH	R4,X'86'	REG SAVE POINTER	CMT11100	
13E0	C840 3F00	1105		LHI	R4,PSWSAVE	PPF PSW SAVE AREA	CMT11110	
13E4	4040 0084	1106		STH	R4,X'84'	POINTNER	CMT11120	
13E8	C830 1586	1107		LHI	R3,RP		CMT11130	
13EC	4030 0096	1108		STH	R3,X'96'	RELOC/PROTECT INT NEW PSW LOC	CMT11140	
13F0	D310 16DA	1109		LB	R1,CONADP	LOAD CONSOLE I/O ADDRESS	CMT11150	
13F4	0A11	1110		AAR	R1,R1		CMT11160	
13F6	C800 1414	1111		LHI	R0,KBINT0	R0 = A(KEYBOARD INT HANDLER)	CMT11170	
13FA	4001 00D0	1112		STH	R0,X'D0'(R1)	STORE @ X'D0'+2(KB DEV ADR)	CMT11180	
13FE	0711	1113		XAR	R1,R1	TO SET UP SERVICE POINTER TABLE	CMT11190	
1400	C830 14B8	1114		LHI	R3,XI32		CMT11200	
1404	4821 1970	1115	LCORE32A	LH	R2,DEVSADR(R1)	GET DEV ADR FROM TABLE	CMT11210	
1408	021F	1116		BMR	LINK	DONE. RETURN	CMT11220	
140A	0A22	1117		AAR	R2,R2		CMT11230	
140C	4032 00D0	1118		STH	R3,X'D0'(R2)	STORE @ X'D0'+2(DEV ADR)	CMT11240	
1410	2612	1119		AIS	R1,2		CMT11250	
1412	2267	1120		BS	LCORE32A		CMT11260	
		1121	-----					CMT11270
		1122	*	KEYBOARD INTERRUPT HANDLER			CMT11280	
		1123	*				CMT11290	
1414	C330 0020	1124	KBINT0	THI	R3,X'20'	IS BREAK KEY DEPRESSED ?	CMT11300	
1418	4330 145C	1125		BZ	KBINT1	NO	CMT11310	
141C	D300 0A10	1126		LB	R0,I0		CMT11320	
1420	C500 0005	1127		CLHI	R0,5	IS IT MICROPUS ?	CMT11330	
1424	213C	1128		BNES	KBINT0B	NO, BRANCH	CMT11340	
1426	DE20 16E6	1129		OC	R2,MREADC	YES, ISSUE READ	CMT11350	
142A	9D23	1130		SSR	R2,R3		CMT11360	
142C	2081	1131		BTBS	0,1		CMT11370	
142E	9B24	1132	KBINT0C	RDR	R2,R4	KNOCK DOWN BREAK	CMT11380	
1430	9D23	1133		SSR	R2,R3		CMT11390	
1432	C330 0020	1134		THI	R3,X'20'	BREAK STILL THERE ?	CMT11400	
1436	2034	1135		BNZS	KBINT0C	YES, KNOCK IT DOWN AGAIN	CMT11410	
1438	4300 1498	1136		B	RETOPSW	NO, RETURN ON OLD PSW	CMT11420	
	00C0 143C	1137	KBINT0B	EQU	*		CMT11430	
143C	4850 16D6	1138		LH	R5,PASFLG	CONSOLE ON PASLA ?	CMT11440	
1440	2339	1139		BZS	KBINT0A	BRANCH IF NO.	CMT11450	
1442	9B24	1140		RDR	R2,R4		CMT11460	
1444	9D23	1141		SSR	R2,R3		CMT11470	
1446	2281	1142		BFBS	0,1		CMT11480	
1448	0844	1143		LDAR	R4,R4		CMT11490	



## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

14C2	D230 16D2	1197		STB	R3,INTSTA	STORE INTERRUPTING DEVICE STATUS	CMT12030
14C6		1198		IFZ	ADC-2		CMT12040
14C6	4850 16C4	1199		LH	R5,MOD32		CMT12050
14CA	2135	1200		BNZS	XI32A		CMT12060
14CC	4800 0040	1201		LH	R0,X'40'	16-BIT OLD PSW	CMT12070
14D0	4810 0042	1202		LH	R1,X'42'		CMT12080
		1203		ENDC			CMT12090
14D4	4000 16CA	1204	XI32A	STH	R0,OPSW	STORE OLD PSW STATUS	CMT12100
14D8	4010 16CE	1205		STH	R1,OLOC	STORE OLD PSW LOC	CMT12110
14DC		1206		IFZ	ADC-2		CMT12120
14DC	0855	1207		LDAR	R5,R5	MOD32 = 0 ?	CMT12130
14DE	233A	1208		BZS	XI16A	BRANCH IF YES.	CMT12140
		1209		ENDC			CMT12150
14E0	4820 0A24	1210		LH	R2,PSW2		CMT12160
14E4	9512	1211		EPSR	R1,R2	SELECT USER REGISTER SET	CMT12170
14E6	D000 3E48	1212		STM	R0,INTSAV	SAVE USER REGISTERS	CMT12180
14EA	4820 16D0	1213		LH	R2,INTDEV		CMT12190
14EE	48A0 16C6	1214		LH	R10,INTPSW		CMT12200
		1215	*				CMT12210
14F2	0755	1216	XI16A	XAR	R5,R5		CMT12220
14F4	4865 1970	1217	XI1	LH	R6,DEVSADR(R5)	GET DEV ADDR FROM TABLE	CMT12230
14F8	4210 1544	1218		BM	XIERR	TABLE OVERFLOW.	CMT12240
14FC	0562	1219		CLAR	R6,R2	COMPARE INTERRUPTING DEVICE ADDRESS	CMT12250
14FE	2333	1220		BES	XI2		CMT12260
1500	2652	1221		AIS	R5,2		CMT12270
1502	2207	1222		BS	XI1		CMT12280
1504	4865 196A	1223	XI2	LH	R6,DEVINT(R5)	GET INTERRUPT HANDLER ADDRESS	CMT12290
1508	4330 1544	1224		BZ	XIERR	INTERRUPT NOT EXPECTED	CMT12300
150C	4060 1542	1225		STH	R6,XIEXIT		CMT12310
		1226	*				CMT12320
1510		1227		IFZ	ADC-2		CMT12330
1510	4860 16C4	1228		LH	R6,MOD32	32-BIT MACHINE ?	CMT12340
1514	2339	1229		BZS	XI3	BRANCH IF NO.	CMT12350
		1230		ENDC			CMT12360
1516	9051	1231		SRLS	R5,1		CMT12370
1518	90A4	1232		SRLS	R10,4		CMT12380
151A	C4A0 000F	1233		NHI	R10,15		CMT12390
151E	D4A5 1966	1234		CLB	R10,INTLVL(R5)	CHECK PROPER INTERRUPT LEVEL	CMT12400
1522	4230 1554	1235		BNE	LVLERR		CMT12410
		1236	*				CMT12420
1526	4860 16CE	1237	XI3	LH	R6,OLOC	GET PSW AT TIME OF INTERRUPT	CMT12430
152A	C560 10C4	1238		CLHI	R6,TIMER+4		CMT12440
152E	2187	1239		BLS	XI4	WAS INTERRUPT IN TIMER ROUTINE ?	CMT12450
1530	C560 10D8	1240		CLHI	R6,TIMXT		CMT12460
1534	2384	1241		BNLS	XI4	BRANCH IF NO.	CMT12470
1536	D100 3F08	1242		LM	R0,RSAVE	RESTORE FROM 'TIMER' ENTRY	CMT12480
153A	2303	1243		BS	XI5		CMT12490
153C	D100 3E48	1244	XI4	LM	R0,INTSAV	RESTORE FROM XI16/XI32 ENTRY	CMT12500
1540	4300 1540	1245	XI5	B	*	AND GO TO INTERRUPT HANDLER	CMT12510
	0000 1542	1246	XIEXIT	EQU	*-2		CMT12520
		1247	*				CMT12530
		1248	*			EXTERNAL INTERRUPT ERROR ROUTINE	CMT12540
		1249	*				CMT12550



## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

1544	C860 4634	1250	XIERR	LHI	R6,C'F4'	ERROR # F4	CMT12560
1548	4060 173E	1251		STH	R6,ERRNO		CMT12570
154C	41F0 0F9A	1252		BAL	LINK,ERRALL	'ERROR XXF4', 'DEV DDD STA SS'	CMT12580
		1253	*			'PSW PPPP LOC LLLL'	CMT12590
1550	4300 0AEA	1254		B	OPTIN1	TO ENTER COMMAND MODE	CMT12600
		1255	*	-----			CMT12610
		1256	*	* DEVICE INTERRUPTED IN WRONG INTERRUPT LEVEL			CMT12620
		1257	*				CMT12630
1554	C860 4636	1258	LVLERR	LHI	R6,C'F6'	ERROR # F6	CMT12640
1558	4060 173E	1259		STH	R6,ERRNO		CMT12650
155C	D3AA 171C	1260		LB	R10,HEXTAB(R10)	CONVERT TO ASCII	CMT12660
1560	D2A0 17A0	1261		STB	R10,ERRLVL	AND STORE ERROR LEVEL IN MESSAGE	CMT12670
1564	41F0 0F9A	1262		BAL	LINK,ERRALL	'ERROR XXF6', 'DEV DDD STA SS'	CMT12680
		1263	*			'PSW PPPP LOC LLLL'	CMT12690
1568	C850 178A	1264		LHI	R5,INTLVLM		CMT12700
156C	4050 16FE	1265		STH	R5,ISITERR	SET FLAG TO OVERRIDE NOMSG OPTION	CMT12710
1570	41F0 112A	1266		BAL	LINK,PRINT	'INTERRUPTED IN LEVEL '4'	CMT12720
1574	4300 0AEA	1267		B	OPTIN1	ENTER COMMAND MODE.	CMT12730
		1268	*	-----			CMT12740
		1269	*	* SPURIOUS INTERRUPT HANDLERS			CMT12750
		1270	*				CMT12760
		1271	*				CMT12770
1578	C820 4637	1272	FORFAULT	LHI	R2,C'F7'		CMT12780
157C	2307	1273		BS	RP+4		CMT12790
157E		1274		IFZ	ADC-2		CMT12800
		1275	*	* FLOATING-PT ARITH FAULT INT TRAP (16 BIT PROCESSOR)			CMT12810
		1276	*				CMT12820
157E	48F0 0028	1277	FP	LH	R14,X'28'	OLD PSW (16-BIT PROCESSOR)	CMT12830
1582	48F0 002A	1278		LH	R15,X'2A'	OLD LOC	CMT12840
		1279		ENDC			CMT12850
		1280	*				CMT12860
		1281	*	* RELOCATION/PROTECTION INT TRAP			CMT12870
		1282	*				CMT12880
1586	C820 4635	1283	RP	LHI	R2,C'F5'		CMT12890
158A	4020 173E	1284		STH	R2,ERRNO	SET ERROR # F5	CMT12900
158E	230C	1285		BS	COMM		CMT12910
		1286	*				CMT12920
		1287	*	* ARITHMETIC FAULT INT (32-BIT PROCESSOR) TRAP			CMT12930
		1288		IFZ	ADC-2		CMT12940
1590		1289	*	* FIXED-PT DIVIDE FAULT INT (16-BIT PROCESSOR) TRAP			CMT12950
		1290		ENDC			CMT12960
		1291	*				CMT12970
1590	C820 4631	1292	AF	LHI	R2,C'F1'		CMT12980
1594	4020 173E	1293		STH	R2,ERRNO	SET ERROR # F1	CMT12990
1598		1294		IFZ	ADC-2		CMT13000
1598	4820 16C4	1295		LH	R2,MOD32		CMT13010
159C	2135	1296		BNZS	COMM		CMT13020
159E	48E0 0048	1297		LH	R14,X'48'	OLD PSW (16-BIT PROCESSOR)	CMT13030
15A2	48F0 004A	1298		LH	R15,X'4A'	OLD LOC (16-BIT PROCESSOR)	CMT13040
		1299		ENDC			CMT13050
15A6	40E0 16CA	1300	COMM	STH	R14,OPSW		CMT13060
15AA	40F0 16CE	1301		STH	R15,OLOC		CMT13070
15AE	4800 0A24	1302	COMM1	LH	R0,PSW2		CMT13080

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

15B2	9520	1303	EPSR	R2,R0	NO INT. , REG SET 15	CMT13090
15B4	41F0 0F4C	1304	BAL	LINK,ERF	PRINT 'ERROR XXFN'	CMT13100
15B9	40F0 16FE	1305	STH	LINK,ISITERR	FORCE PRINT	CMT13110
15BC	41E0 1044	1306	BAL	RET,ERREPL1	PRINT 'PSW PPPP LOC LLLL'	CMT13120
15C0	4300 0AFA	1307	B	OPTIN1	ENTER COMMAND MODE	CMT13130
		1308	*			CMT13140
		1309	*	ILLEGAL INSTRUCTION INTERRUPT TRAP		CMT13150
		1310	*			CMT13160
15C4	C820 4632	1311	II	LHI R2,C'F2'		CMT13170
15C8	4020 173E	1312	STH	R2,ERRNO	SET ERROR # F2	CMT13180
15CC		1313	IFZ	ADC-2		CMT13190
15CC	4820 16C4	1314	LH	R2,MOD32		CMT13200
15D0	2135	1315	BNZS	II32		CMT13210
15D2	49E0 0030	1316	LH	R14,X'30'	OLD PSW	CMT13220
15D6	49F0 0032	1317	LH	R15,X'32'	OLD LOC	CMT13230
		1318	ENDC			CMT13240
15DA	4300 15A6	1319	II32	B COMM		CMT13250
		1320	*			CMT13260
		1321	*	MACHINE MALFUNCTION INTERRUPT TRAP		CMT13270
		1322	*			CMT13280
15DE	95AA	1323	MM	EPSR R10,R10	CAPTURE MMINT PSW	CMT13290
15E0	C820 4633	1324	LHI	R2,C'F3'		CMT13300
15E4	4020 173E	1325	STH	R2,ERRNO	SET ERROR # F3	CMT13310
15E8	48E0 0022	1326	LH	R14,X'22'	OLD PSW ( 32-BIT PROCESSOR)	CMT13320
15EC	48F0 0026	1327	LH	R15,X'26'	OLD LOC	CMT13330
15F0		1328	IFZ	ADC-2		CMT13340
15F0	4820 16C4	1329	LH	R2,MOD32		CMT13350
15F4	2135	1330	BNZS	MM32		CMT13360
15F6	48E0 0038	1331	LH	R14,X'38'	OLD PSW (16 BIT PROCESSOR)	CMT13370
15FA	48F0 003A	1332	LH	R15,X'3A'	OLD LOC	CMT13380
		1333	ENDC			CMT13390
15FE	C4E0 FFF0	1334	MM32	NHI R14,X'FFF0'		CMT13400
1602	C4A0 000F	1335	NHI	R10,X'000F'		CMT13410
1606	06EA	1336	OAR	R14,R10		CMT13420
1608	40E0 16CA	1337	STH	R14,OPSW		CMT13430
160C	40F0 16CE	1338	STH	R15,CLOC		CMT13440
1610		1339	IFZ	ADC-2		CMT13450
1610	C810 7FFF	1340	LHI	R1,X'7FFF'		CMT13460
1614	2711	1341	MM16	SIS R1,1		CMT13470
1616	2021	1342	BPS	MM16		CMT13480
		1343	ENDC			CMT13490
1618	C800 080F	1344	LHI	R0,X'080F'		CMT13500
161C	9104	1345	SLHLS	R0,4	R0 = X'30F0'	CMT13510
161E	9520	1346	EPSR	R2,R0	HALT PROCESSOR	CMT13520
		1347	*			CMT13724
		1348	*	WHEN EXE/RUN IS DEPRESSED, ERROR MSG IS PRINTED.		CMT13726
		1349	*			CMT13728
1620	D320 0A10	1350	MMCOM1	LB R2,I0	GET INPUT DEVICE POINTER	CMT13530
1624	2725	1351	SIS	R2,5	IS IT MICRO I/O BUS	CMT13540
1626	2334	1352	BZS	MMCOM1A	YES, BRANCH	CMT13550
1628	4820 16D6	1353	LH	R2,PASFLG	IS CONSOLE ON PASLA	CMT13560
162C	233B	1354	BZS	MMCOM2	NO, BRANCH	CMT13570
162E	D320 16DB	1355	MMCOM1A	LB R2,CONADR+1	GET CONSOLE TRANSMIT'R ADRES	CMT13580

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

1632	DE20	16E8	1356	OC	R2,CON2ND	ISSUE 2ND/RESET COMMAND	CMT13590
1636	D320	16DA	1357	LB	R2,CONADR	GET RECEIVER ADDRESS	CMT13600
163A	DE20	16DC	1358	OC	R2,CONRD	OUTPUT PEAD CMD	CMT13610
163E	DB20	16D3	1359	RD	R2,SINK	DUMMY READ TO SET BRUSY	CMT13620
			1360	*			CMT13630
1642	D320	0A11	1361	MMCOM2	LB R2,IO+1	GET LIST DEVICE POINTER	CMT13640
1646	2725		1362	SIS	R2,5	IS IT ON MICRO I/O BUS	CMT13650
1648	2334		1363	BZS	MMCOM2A	YES, BRANCH	CMT13660
164A	4820	16D8	1364	LH	R2,PASFLG2	IS LIST DEVICE ON PASLA	CMT13670
164E	233E		1365	BZS	MMCOM3	NO, BRANCH	CMT13680
1650	D310	0A11	1366	MMCOM2A	LB R1,IO+1	YES,GET LIST DEVICE POINTER	CMT13690
1654	D320	0A10	1367	LB	R2,IO		CMT13700
1658	0521		1368	CLAR	R2,R1	CONSOLE = LIST DEVICE	CMT13710
165A	2338		1369	BES	MMCOM3	YES, BRANCH	CMT13720
165C	9111		1370	SLHLS	R1,1		CMT13730
165E	D321	0A11	1371	LB	R2,IO+1(R1)	GET LIST DEVICE TRANSMIT ADDRESS	CMT13740
1662	DE21	16E8	1372	OC	R2,CON2ND(R1)	ISSUE 2ND/RESET CMD	CMT13750
1666	DE21	16DD	1373	OC	R2,CONWRT(R1)	ISSUE LIST WRITE CMD	CMT13760
			1374	*			CMT13770
166A	4300	15AE	1375	MMCOM3	B COMM1		CMT13800
			1376	*			CMT13810
			1377	*			CMT13820
166E	48F0	16C4	1378	MACHNUM	LH R15,MOD32	.	** CMT13830
1672	4230	1694	1379		BNZ BUFCHA32	.	** CMT13840
1676	0777		1380	BUFCHA16	XHR R7,R7	STORE ADDRESS OF WBUFF OR RBUFF	** CMT13850
1678	D271	000B	1381		STB R7,11(R1)	IN OPTION/COMMAND TABLE	** CMT13860
167C	4061	0006	1382		STH R6,6(R1)	.	** CMT13870
1680	C510	17CE	1383		CLHI R1,MWRITE	TEST IF WSTART OR RSTART	** CMT13880
1684	2334		1384		BES WSTORE	.	** CMT13890
1686	4060	3630	1385	RSTORE	STH R6,RADDRS	.	** CMT13900
168A	2303		1386		BS ROPTIN2	.	** CMT13910
168C	4060	362C	1387	WSTORE	STH R6,WADDRS	.	** CMT13920
1690	4300	0AE6	1388	ROPTIN2	B OPTIN	.	** CMT13930
1694	0876		1389	BUFCHA32	LHR R7,R6	STORES NEW ADRS FOR 32 BIT MACHIN**	CMT13940
1696	4071	0006	1390		STH R7,6(R1)	STORE 1ST 16 BITS OF NEW ADDRESS	** CMT13950
169A	1068		1391		DC X'1068'	NOTE: THESE ARE SRLS INSTRUCTIONS**	CMT13960
169C	1068		1392		DC X'1068'	THEY PERFORM FULLWORD SHIFTS	** CMT13970
169E	C460	000F	1393		NHI R6,X'F'	.	** CMT13980
16A2	D261	000B	1394		STB R5,11(R1)	STORE 1ST 4 BITS OF NEW ADDRESS	** CMT13990
16A6	C510	17CE	1395		CLHI R1,MWRITE	.	** CMT14000
16AA	2336		1396		BES WSTORE1	.	** CMT14010
16AC	4070	3632	1397	RSTORE1	STH R7,RADDRS+2	STORE RBUFF ADDRESS IN RADDRS	** CMT14020
16B0	D260	3631	1398		STB R6,RADDRS+1	.	** CMT14030
16B4	2305		1399		BS ROPTIN1	.	** CMT14040
16B6	4070	362E	1400	WSTORE1	STH R7,WADDRS+2	STORE WBUFF ADDRESS IN WADDRS	** CMT14050
16BA	D260	362D	1401		STB R6,WADDRS+1	.	** CMT14060
16BE	4300	0AE6	1402	ROPTIN1	B OPTIN	.	** CMT14070
			1403	*	*****		CMT14080
			1404	*	ETPE CONSTANTS & TABLES		CMT14090
			1405	*			CMT14100
16C2	0000		1406	FIRST	DCX 0		CMT14110
16C4	0000		1407	MOD32	DCX 0	FLAG FOR 32-BIT M/C(NON-ZERO)	CMT14120
16C6	0000		1408	INTPSW	DCX 0	(FOR 32-BIT M/C ONLY)	CMT14130

## EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

16C8		1409		ALIGN 8			CMT14140
		1410	*	-----			CMT14150
16C8	0000	1411	OPSW32	DCX 0		OLD PSW STORAGE AREA	CMT14160
16CA	0000	1412	OPSW	DCX 0			CMT14170
16CC	0000	1413		DCX 0			CMT14180
16CE	0000	1414	OLOC	DCX 0			CMT14190
		1415	*	-----			CMT14200
16D0	0000	1416	INTDEV	DCX 0		INTERRUPTING DEV ADR	CMT14210
	0000 16D0	1417	ERRDEV	EQU INTDEV		ERROR DEVICE #	CMT14220
16D2	00	1418	INTSTA	DB 0		INTERRUPTING DEV STATUS	CMT14230
	0000 16D2	1419	ERRSTA	EQU INTSTA		ERRONEOUS STATUS	CMT14240
16D3	00	1420	SINK	DB 0		BIT BUCKET	CMT14250
16D4	80	1421	NOP*	DB X'80'			CMT14260
16D5	40	1422	IVCR	DB X'40'			CMT14270
16D6		1423		DB *			CMT14280
16D6	0000	1424	PASFLG	DCX 0		SET WHEN CONSOLE ON PASLA/PALM*	CMT14290
16D8	0000	1425	PASFLG2	DCX 0		SET WHEN LIST DEVICE ON PASLA	CMT14300
		1426	*	-----			CMT14310
		1427	*	ETPE IO COMMANDS			CMT14320
		1428	*				CMT14330
16DA	0000	1429	CONADR	DCX 0		CONSOLE DEVICE ADDRESS	CMT14340
		1430	*				CMT14350
16DC	0000	1431	CONRD	DCX 0		CONSOLE READ/WRITE COMMANDS	CMT14360
	0000 16DD	1432	CONWRT	EQU CONRD+1			CMT14370
16DE	A1A3	1433	CRTPD	DCX A1A3		FOR CRT	CMT14380
16E0	A4C8	1434	CLIFRD	DCX A4D8		* CURRENT LOOP INTERFACE	CMT14390
16E2	0080	1435	LPWPT	DCX 0080		* LINE PRINTER	CMT14400
16E4	A1A3	1436	CARRD	DCX A1A3		* CAROUSEL 300	CMT14410
16E6	8202	1437	MREADC	DCX 8202		* MICROBUS	CMT14420
		1438	*				CMT14430
16E8	0000	1439	CON2ND	DCX 0		2ND COMMAND; ENABLE READ COMMAND	CMT14440
	0000 16E9	1440	CONENRD	EQU CON2ND+1			CMT14450
16EA	EE71	1441	CRT2ND	DCX EE71		FOR CRT	CMT14460
16EC	0064	1442	CLIF2ND	DCX 0064		* CURRENT LOOP INTERFACE	CMT14470
16EE	0000	1443		DCX 0		* DUMMY HW FOR LP	CMT14480
16F0	F069	1444	CAR2ND	DCX F069		* CAROUSEL 300	CMT14490
16F2	0000	1445		DCX 0		* DUMMY HW FOR MICROBUS	CMT14500
		1446	*				CMT14510
16F4	00	1447	CONPQ2S	DB 0		CONSOLE REQUEST TO SEND CMD	CMT14520
16F5	33	1448	CRTPQ2S	DB X'33'		FOR CRT	CMT14530
16F6	00	1449		DB 0		* DUMMY BYTE FOR CLI	CMT14540
16F7	00	1450		DB 0		* DUMMY BYTE FOR LP	CMT14550
16F8	23	1451	CARPQ2S	DB X'23'		* CAROUSEL 300	CMT14560
16F9	00	1452		DB 0		* DUMMY BYTE FOR MICROBUS	CMT14570
16FA		1453		DB *			CMT14580
		1454	*	-----			CMT14590
16FA	1498	1455	KBINT	DC Z(RETOPS*)		KEYBOARD INT RETURN ADR	CMT14600
16FC	0000	1456	BRKVECT	DC Z(0)		BREAK KEY VECTOR	CMT14610
16FE	0000	1457	ISITERR	DCX 0			CMT14620
1700	0000	1458	NOERR	DCX 0			CMT14630
1702	0000	1459	SELTST	DCX 0		HIGHEST SELECTED TEST #	CMT14640
1704	0000	1460	WASDU	DCX 0		1 IF KEYBOARD DEVICE WAS OFF	CMT14650
1706	0000	1461	WASDU1	DCX 0		NON-ZERO IF TOTAL,TOTERR TO PRINT	CMT14660



EXEC - ETPE R03P2 (W/CONDITIONAL ASSEMBLY)

17B2	3F0D	1493	QMSG	DC	X'3F0D'
17B4	2A0D	1494	AMSG	DC	X'2A0D'

CHT14980  
CHT14990











## SUBROUTINE INIT

		1546	*-----			CMT15510
		1547	*			CMT15520
		1548	* SUBROUTINE INIT			CMT15530
		1549	* THIS ROUTINE INITIALIZES THE TEST. IT IS CALLED BY	*		CMT15540
		1550	* ETPE IT CHECKS FOR FALSE SYNC FROM DEVICES REQUESTED.	*		CMT15550
		1551	* AND DO THE NORMAL HOUSE CLEANING.	*		CMT15560
		1552	* IF THE TEST IS EXECUTED THE FIRST TIME AFTER LOADING,	*		CMT15570
		1553	* IT ALSO FORCES THE EXECUTION OF TEST 0 AND SET UP THE	*		CMT15580
		1554	* 10MS TIMER CONSTANT	*		CMT15590
		1555	*			CMT15600
		1556	* CALLING SEQUENCE:	*		CMT15610
		1557	* BAL R15,INIT	*		CMT15620
		1558	* *****	*		CMT15630
		1559	*			CMT15640
19A6	4800 1924	1560	INIT LH R0,TIMVAL+6	GET TIMVAL OPTION FOR 1MS DELAY		CMT15650
19AA	2410	1561	LIS R1,0			CMT15660
19AC	2440	1562	LIS R4,0			CMT15670
19AE	2421	1563	LIS R2,1			CMT15680
19B0	2439	1564	LIS R3,9			CMT15690
19B2	0A40	1565	INIT.1 AHR R4,R0	LOOP TO GET VALUE FOR 10MS		CMT15700
19B4	C110 19B2	1566	BXLE R1,INIT.1	DELAY IN R4		CMT15710
19B8	4040 0A1E	1567	STH R4,TIME	STORE 10MS DELAY TIME		CMT15720
19BC	48F0 16C4	1568	LH R15,MOD32			CMT15730
19C0	4330 19E4	1569	BZ TESTAB			CMT15740
19C4	D360 17CD	1570	TESTAA2 LB R6,MREAD+11			CMT15750
19C8	0866	1571	LHR R6,R6			CMT15760
19CA	4230 1A10	1572	BNZ TESTAA1			CMT15770
19CE	4860 3632	1573	TESTAA0 LH R6,RADDRS+2			CMT15780
19D2	0866	1574	LHR R6,R6			CMT15790
19D4	4230 19FC	1575	BNZ TELAST			CMT15800
19D8	C860 3A3A	1576	LHI R6,RBUFF			CMT15810
19DC	4060 3632	1577	STH R6,RADDRS+2			CMT15820
19E0	4300 1A10	1578	B TESTAA1			CMT15830
19E4	4860 3630	1579	TESTAB LH R6,PADDRS			CMT15840
19E8	4890 362C	1580	LH R9,IADDRS			CMT15850
19EC	0866	1581	LHR R6,R6			CMT15860
19EE	2137	1582	BNZS TELAST			CMT15870
19F0	C860 3A3A	1583	LHI R6,RBUFF			CMT15880
19F4	4060 3630	1584	STH R6,PADDRS			CMT15890
19F8	4300 1A2E	1585	B TELAST0			CMT15900
19FC	C560 3A3A	1586	TELAST CLHI R6,RBUFF			CMT15910
1A00	2335	1587	BES CHEKI2			CMT15920
1A02	4560 3638	1588	CLH R6,LAST			CMT15930
1A06	4280 1AB2	1589	BTC 9,MESSAG			CMT15940
1A0A	C8FF	1590	CHEKI2 LHR R15,R15			CMT15950
1A0C	4330 1A2E	1591	BZ TELAST0			CMT15960
1A10	D390 17D9	1592	TESTAA1 LB R9,MWRITE+11			CMT15970
1A14	0899	1593	LHR R9,R9			CMT15980
1A16	4230 1A6E	1594	BNZ TESTAA			CMT15990
1A1A	4890 362E	1595	LH R9,IADDRS+2			CMT16000
1A1E	0899	1596	LHR R9,R9			CMT16010
1A20	4230 1A5A	1597	BNZ TELAST1			CMT16020
1A24	C890 363A	1598	LHI P9,RBUFF			CMT16030

## SUBROUTINE INIT

1A28	4090 362E	1599		STH	R9,WADDRS+2		CMT16040
1A2C	230A	1600		BS	RINI		CMT16050
1A2E	C590 363A	1601	TELASTO	CLHI	R9,WBUFF		CMT16060
1A32	2337	1602		BES	RINI		CMT16070
1A34	0899	1603		LHR	R9,R9		CMT16080
1A36	2137	1604		BNZS	TESTAB1		CMT16090
1A38	C890 363A	1605		LHI	R9,WBUFF		CMT16100
1A3C	4090 362C	1606		STH	R9,WADDRS		CMT16110
1A40	4300 1AC8	1607	RINI	B	INI		CMT16120
1A44	0569	1608	TESTAB1	CLHR	R6,R9		CMT16130
1A46	2384	1609		BNLS	CONT1		CMT16140
1A48	0879	1610		LHP	R7,R9		CMT16150
1A4A	0B76	1611		SHR	R7,R6		CMT16160
1A4C	2303	1612		BS	COMP1		CMT16170
1A4E	0876	1613	CONT1	LHR	R7,R6		CMT16180
1A50	0B79	1614		SHR	R7,R9		CMT16190
1A52	C570 0402	1615	COMP1	CLHI	R7,X'402'		CMT16200
1A56	4280 1AA8	1616		BTC	8,MESSAGE		CMT16210
1A5A	C590 363A	1617	TELAST1	CLHI	R9,WBUFF		CMT16220
1A5E	223F	1618		BES	RINI		CMT16230
1A60	4590 3638	1619		CLH	R9,LAST		CMT16240
1A64	4280 1ABC	1620		BTC	9,MESSAGE		CMT16250
1A68	08FF	1621	CHEKI1	LHR	R15,R15		CMT16260
1A6A	4330 1AC8	1622		BZ	INI		CMT16270
1A6E	D360 17CD	1623	TESTAA	LB	R6,MREAD+11		CMT16280
1A72	4870 17C8	1624		LH	R7,MREAD+6		CMT16290
1A76	D380 17D9	1625		LB	R8,MWRITE+11		CMT16300
1A7A	4890 17D4	1626		LH	R9,MWRITE+6		CMT16310
1A7E	ED60 0010	1627		SLL	R6,16		CMT16320
1A82	0667	1628		DC	X'0667'		CMT16330
1A84	ED80 0010	1629		SLL	R8,16		CMT16340
1A88	0689	1630		DC	X'0689'		CMT16350
1A8A	0568	1631		DC	X'0568'		CMT16360
1A8C	2187	1632		BLS	CONEIT		CMT16370
1A8E	C870 0402	1633	CONSIK	LHI	R7,X'402'		CMT16380
1A92	0A87	1634		DC	X'0A87'		CMT16390
1A94	0568	1635		DC	X'0568'	CLR R6,R8	CMT16400
1A96	2189	1636		BLS	MESSAGE		CMT16410
1A98	2306	1637		BS	RINI2		CMT16420
1A9A	C870 0402	1638	CONEIT	LHI	R7,X'402'		CMT16430
1A9E	0A67	1639		DC	X'0A67'	AR R6,R7	CMT16440
1AA0	0586	1640		DC	X'0586'	CLR R8,R6	CMT16450
1AA2	2183	1641		BLS	MESSAGE		CMT16460
1AA4	4300 1AC8	1642	RINI2	B	INI		CMT16470
1AA8	C850 35EE	1643	MESSAGE	LHI	R5,LAABEL		CMT16480
1AAC	41F0 112A	1644		BAL	LINK,PRINT		CMT16490
1AB0	230A	1645		BS	ROPTIN		CMT16500
1AB2	C850 35A4	1646	MESSAG	LHI	R5,LABELL		CMT16510
1AB6	41F0 112A	1647		BAL	LINK,PRINT		CMT16520
1ABA	2305	1648		BS	ROPTIN		CMT16530
1ABC	C850 35C8	1649	MESSAGE	LHI	R5,LABELL		CMT16540
1ACO	41F0 112A	1650		BAL	LINK,PRINT		CMT16550
1AC4	4300 0AE6	1651	ROPTIN	B	OPTIN		CMT16560

## SUBROUTINE INIT

1AC8	4850	1840	1652	INI	LH	R5,MODE+6			CMT16570
1ACC	2334		1653		BZS	SELCHK			CMT16580
1ACE	C550	0002	1654		CLHI	R5,2	SELCH MODE?		CMT16590
1AD2	213B		1655		BNES	SETDEV			CMT16600
			1656	*					CMT16610
			1657	*		CHECK FOR SELCH FALSE SYNC			CMT16620
			1658	*					CMT16630
1AD4	4870	181C	1659	SELCHK	LH	SELCH,SELADR+6	LOAD SELCH ADDRESS		CMT16640
1AD8	4070	1370	1660		STH	SELCH,DEVSADR			CMT16650
1ADC	4070	16D0	1661		STH	SELCH,ERRDEV			CMT16660
1AE0	DE70	347E	1662		OC	SELCH,STOP	STOP SELCH		CMT16670
1AE4	4240	1866	1663		BTC	4,FALSYN	INSTRUCTION TIMED OUT		CMT16680
			1664	*					CMT16690
			1665	*		CHECK FOR DEVICE FALSE SYNC.			CMT16700
			1666	*					CMT16710
1AE8	4860	1804	1667	SETDEV	LH	DEV,DEVADR+6	LOAD DEVICE ADDRESS		CMT16720
1AEC	4060	1972	1668		STH	DEV,DEVSADR+2			CMT16730
1AFO	4060	16D0	1669		STH	DEV,ERRDEV			CMT16740
1AF4	DE60	348A	1670		OC	DEV,DISARM	DISARM DEVICE		CMT16750
1AF8	4240	1866	1671		BTC	4,FALSYN	INSTRUCTION TIMED OUT		CMT16760
1AFC	DE60	347F	1672		OC	DEV,CLEAR	CLEAR DEVICE		CMT16770
1B00	41F0	33FC	1673		BAL	RET,REWIND	REWIND TAPE		CMT16780
1B04	4860	1810	1674		LH	DEV,DV2ADR+6	GET SECOND DEVICE ADDRESS		CMT16790
1B08	4060	1974	1675		STH	DEV,DEVSADR+4			CMT16800
1B0C	233B		1676		BZS	SETTRK			CMT16810
1B0E	4060	16D0	1677		STH	DEV,ERRDEV			CMT16820
1B12	DE60	348A	1678		OC	DEV,DISARM	DISARM DEVICE		CMT16830
1B16	4240	1866	1679		BTC	4,FALSYN	INSTRUCTION TIMED OUT		CMT16840
1B1A	DE60	347F	1680		OC	DEV,CLEAR	CLEAR DEVICE		CMT16850
1B1E	41E0	33FC	1681		BAL	RET,REWIND	REWIND TAPE		CMT16860
			1682	*					CMT16870
			1683	*		SET UP TRACK MASK			CMT16880
			1684	*					CMT16890
1B22	48C0	184C	1685	SETTRK	LH	R12,TRACK+6	LOAD TRACK NUMBER		CMT16900
1B26	C5C0	0007	1686		CLHI	R12,7	SEVEN?		CMT16910
1B2A	2134		1687		BNES	NINE	NO - NINE		CMT16920
1B2C	C8C0	3F3F	1688		LHI	R12,X'3F3F'	7-TRACK MASK		CMT16930
1B30	23C2		1689		BS	SETMSK			CMT16940
1B32	25C1		1690	NINE	LCS	R12,1	9-TRACK MASK		CMT16950
1B34	40C0	3464	1691	SETMSK	STH	R12,MASK			CMT16960
			1692	*					CMT16970
			1693	*		RESET FLAG			CMT16980
			1694	*					CMT16990
1B38	48C0	1828	1695		LH	R12,INTLEV+6		C	CMT17000
1B3C	D2C0	1966	1696		STB	R12,INTLVL			CMT17010
1B40	D2C0	1967	1697		STB	R12,INTLVL+1			CMT17020
1B44	07CC		1698		XHR	R12,R12			CMT17030
1B46	40C0	346A	1699		STH	R12,EOTFLG			CMT17040
1B4A	40C0	3470	1700		STH	R12,RTYCNT			CMT17050
1B4E	40C0	346C	1701		STH	R12,ERRFLG			CMT17060
1B52	40C0	346E	1702		STH	R12,MODFLG			CMT17070
1B56	40C0	3476	1703		STH	R12,WLRS			CMT17080
1B5A	40C0	3474	1704		STH	R12,DEV2			CMT17090

## SUBROUTINE INIT

1B5E	40C0 3468	1705	STH	R12,DE		CMT17100
1B62	4300 0D9A	1706	B	INITRET		CMT17110
		1707	*			CMT17120
		1708	*			CMT17130
		1709	*	ERROR 00 - DEVICE FALSE SYNC.		CMT17140
		1710	*			CMT17150
1B66	9D65	1711	FALSYN	SSR	DEV,STAT	SYNC ERROR
1B68	C800 3030	1712		LHI	RO,C'00'	ERROR 00
1B6C	41F0 0F82	1713		BAL	R15,ERRDS	
1B70	4300 0AEE	1714		B	OPTIN	
		1715	*	-----		CMT17200

TEST 0 BASIC CONFIDENCE TEST

```

1717 * *****
1718 *
1719 *           T E S T 0
1720 *
1721 *   PURPOSE:
1722 *     TO TEST THE WRITE-BACKSPACE-READ ABILITY OF THE DEVICE
1723 *     AND DETECT ERRORS ON DATA TRANSFER
1724 *
1725 *   ASSUMPTIONS:
1726 *     THIS TEST ASSUMES THAT THE MEMORY TEST, THE PROCESSOR
1727 *     TEST AND THE TTY BASIC CONFIDENCE TEST HAD BEEN RUN
1728 *     WITHOUT DETECTING ANY FAILURE
1729 *
1730 *   DESIGN SPECIFICATIONS:
1731 *     THIS TEST USES THE WRITE-BACKSPACE-READ FEATURE TO
1732 *     GENERATE FILES OF VARIOUS TEST PATTERNS. THE TEST
1733 *     PATTERNS ARE STORED IN BLOCKS OF 8 BYTES EACH. EACH
1734 *     BLOCK IS A SERIES OF DATA WHICH WILL SWITCH THE DATA
1735 *     LINES IN WORST CASE CONDITION. AT THE BEGINNING OF
1736 *     THE GENERATION OF A FILE, A BLOCK OF TEST PATTERN IS
1737 *     REPEATLY COPIED INTO THE WRITE BUFFER UNTIL THE
1738 *     BUFFER IS FULL. THE DATA IN THE BUFFER IS THEN
1739 *     WRITTEN ONTO THE TAPE AS A RECORD. THE RECORD IS
1740 *     BACKSPACED AND READ INTO THE READ BUFFER. THE TWO
1741 *     BUFFERS ARE COMPARED FOR PROPER DATA TRANSFER.
1742 *
1743 *   HOW TO RUN THE TEST:
1744 *     MOUNT THE TAPE ON THE DRIVE AND TURN DEVICE ON LINE.
1745 *     ENTER OPTIONS VIA CONSOLE DEVICE AND SELECT TEST 0.
1746 *     (REFER TO PUBLICATION 06-172A15 FOR CONSOLE INPUTS.)
1747 *     THE TEST IS EXECUTED UPON ENTERING RUN, AND CAN BE
1748 *     TERMINATED BY THE USER AT ANY TIME BY DEPRESSING
1749 *     BREAK OR TAKING DEVICE OFF LINE.
1750 *
1751 *   NOTE:
1752 *     THIS TEST IS FORCED TO BE EXECUTED AT LEAST ONCE
1753 *     EACH TIME WHEN A NON-ZERO VALUE IS ENTERED UNDER
1754 *     OPTION DEVADR OR DV2ADR.
1755 *
1756 *   OPTIONS:
1757 *     TEST, LOOP, CONTIN, NOMSG, DEVADR, SELCH, MODE, TRACK,
1758 *     INTLEV, MODE, TRACK, RECFIL
1759 *     WSTART, RSTART
1760 *
1761 *   ERRORS:
1762 *     00, 01, 02, 04, 05, 06, 07, 09, 10, 11, 12, 13, 14,
1763 *     15, 45, 47, 50
1764 *
1765 * *****
1766 *
1767 *   TEST0  LHI  R4,TEST01  STARTING ADDRESS SET UP FOR
1768 *           BAL  R14,TSTSUP  SECOND DEVICE TEST
1769 *   TEST01 BAL  R14,TSTINIT  TEST INITIALIZE

```

1574 C840 1B7C  
1578 41E0 2B7E  
157C 41E0 2B36

CMT17220  
CMT17230  
CMT17240  
CMT17250  
CMT17260  
CMT17270  
CMT17280  
CMT17290  
CMT17300  
CMT17310  
CMT17320  
CMT17330  
CMT17340  
CMT17350  
CMT17360  
CMT17370  
CMT17380  
CMT17390  
CMT17400  
CMT17410  
CMT17420  
CMT17430  
CMT17440  
CMT17450  
CMT17460  
CMT17470  
CMT17480  
CMT17490  
CMT17500  
CMT17510  
CMT17520  
CMT17530  
CMT17540  
CMT17550  
CMT17560  
CMT17570  
CMT17580  
CMT17590  
CMT17600  
CMT17610  
CMT17620  
CMT17630  
CMT17640  
CMT17650  
CMT17660  
CMT17670  
CMT17680  
CMT17690  
CMT17700  
CMT17710  
CMT17720  
CMT17730  
CMT17740

## TEST 0 BASIC CONFIDENCE TEST

1B80	41C0	324C	1770	BAL	R13, WAIT1	WAIT FOR WMTN=1	CMT17750
1B84	41E0	2BB8	1771	BAL	R14, FSTEOF	WRITE & SENSE EOF	CMT17760
1B88	41C0	31BE	1772	BAL	R13, WAIT2		CMT17770
1B9C	DF60	3483	1773	OC	DEV, BKSPAC	CHECK BACKSPACE FUNCTION	CMT17780
1B90	41F0	2FEE	1774	BAL	R14, SENS03	CHECK FOP EOF	CMT17790
1B94	43C0	2B96	1775	B	CHKEND1		CMT17800
1B98	41C0	31BE	1776	REOF01	BAL	R13, WAIT2	CMT17810
1B9C	DE60	3484	1777	OC	DEV, READ	READ OVER EOF	CMT17820
1BA0	41E0	2FE8	1778	BAL	R14, SENS02	EOF SENSED?	CMT17830
1BA4	43C0	1C82	1779	B	EOFER01	NO - READ EOF RETRY	CMT17840
1BA8	0755		1780	XHR	R5, R5		CMT17850
1BA4	4C50	3470	1791	STH	R5, RTYCNT		CMT17860
1BAE	2422		1782	PROC00	LIS	R2, 2	CMT17870
1BB0	2436		1783	LIS	R3, 6		CMT17880
1BB2	2491		1784	LIS	R9, 1		CMT17890
1BB4	48A0	1858	1785	LH	R10, RECFIL+6		CMT17900
1B98	41E0	2F0E	1786	BAL	R14, RESET	SET BUFFER LIMITS	CMT17910
1BBC	07EB		1787	XHR	R11, R11		CMT17920
1BBE	C788		1788	XHR	R8, R8		CMT17930
1BC0	081B		1789	MOVDT1	LHR	R1, R11	GENERATE 256 BYTE RECORD
1BC2	4841	348C	1790	MOVDT2	LH	CHAR, WDATA(R1)	FROM 8 BYTE DATA BLOCKS
1BC6	4440	3464	1791	MOVDT3	NH	CHAR, MASK	BY COPYING THE BLOCK INTO
1BCA	D080	3F88	1792	STM	R8, RSAVE1		CMT17970
1BCE	E1F0	3618	1793	HA1	LM	R15, RLIM	CMT17980
1BD2	0A8F		1794	AHR	R8, R15		CMT17990
1BD4	4C48	0000	1795	STH	CHAR, O(R8)		CMT18000
1BD8	D180	3F88	1796	LM	R8, RSAVE1		CMT18010
1BDC	2305		1797	BS	HY1		CMT18020
1BDE	D180	3F88	1798	HX1	LM	R8, RSAVE1	CMT18030
1BE2	4048	363A	1799	STH	CHAR, WRUFF(R8)		CMT18040
1BE6	0A82		1800	HY1	AHR	R8, R2	CMT18050
1BE8	C110	1BC2	1801	BXLE	R1, MOVDT2		CMT18060
1BEC	4580	3466	1802	CLH	R8, NBYTE		CMT18070
1BF0	4280	1BC0	1803	BL	MOVDT1		CMT18080
1BF4	C840	C3C3	1804	LHI	CHAR, X'C3C3'	DELIMITER CHARACTER	CMT18090
1BF8	D080	3F88	1805	STM	R8, RSAVE1		CMT18100
1BFC	E1F0	3620	1806	HA2	LM	R15, RLIM	CMT18110
1C00	0A8F		1807	AHR	R8, R15		CMT18120
1C02	2681		1808	AIS	R8, 1		CMT18130
1C04	D248	0000	1809	STB	CHAR, O(R8)		CMT18140
1C08	D180	3F88	1810	LM	R8, RSAVE1		CMT18150
1C0C	2305		1811	BS	HY2		CMT18160
1C0E	D180	3F88	1812	HX2	LM	R8, RSAVE1	CMT18170
1C12	D248	3A3B	1813	STB	CHAR, RBUFF+1(R8)		CMT18180
1C16	2481		1814	HY2	LIS	R8, 1	COUNTER FOR NUMBER OF RECORDS
1C18	41C0	2C3A	1815	GENFIL	BAL	R12, WTRFC	WRITE A RECORD
1C1C	43C0	1C8E	1816	B	WRTER0	ERROR RETURN	CMT18210
1C20	0755		1817	XHR	R5, R5		CMT18220
1C22	4050	3470	1818	STH	R5, RTYCNT	RESET RETRY COUNTER	CMT18230
1C26	41E0	2BEC	1819	PROC01	BAL	R14, BSPACE	BACKSPACE & STATUS CHECK
1C2A	41C0	2CF4	1820	RERDR	BAL	R12, RDREC	READ A RECORD
1C2E	4300	1CB0	1821	B	RDER0	ERROR RETURN	CMT18260
1C32	0755		1822	XHR	R5, R5		CMT18270



## TEST 0 BASIC CONFIDENCE TEST

1C34	4050	3470	1823		STH	R5,RTYCNT	RESET PENTRY COUNTER	CMT18280
1C38	41E0	2E34	1824	PROCO3	BAL	R14,COMPAR	COMPARE DATA	CMT18290
1C3C	48E0	1900	1825		LH	R5,SDUMP+6	BUFFER DUMP?	CMT18300
1C40	2333		1826		BZS	NODJMP	NO - NO DUMP	CMT18310
1C42	41E0	2F90	1827		BAL	R14,DUMP	DUMP READ BUFFER	CMT18320
1C46	C180	1C18	1828	NODUMP	BXLE	R8,GENFIL		CMT18330
1C4A	41E0	31BE	1829	WEOF02	BAL	R13,WAIT2	WAIT FOR NMTN = 1	CMT18340
1C4E	9D65		1830		SSR	DEV,STAT		CMT18350
1C50	C3E0	0020	1831		THI	STAT,X'20'	EOT?	CMT18360
1C54	2333		1832		BZS	EOFMRK		CMT18370
1C56	41E0	33FC	1833		BAL	RET,REWIND	REWIND TAPE	CMT18380
1C5A	DE60	348B	1834	EOFMRK	OC	DEV,WEOF		CMT18390
1C5E	41E0	2FE2	1835		BAL	R14,SENS01		CMT18400
1C62	4300	1CC0	1836		B	EOFER02		CMT18410
1C66	0755		1837		XHR	R5,R5		CMT18420
1C68	40E0	3470	1838		STH	R5,RTYCNT		CMT18430
1C6C	0788		1839	PROCO2	XHR	R8,R8	CHECK NEXT DATA BLOCK	CMT18440
1C6E	08B1		1840		LHR	R11,R1		CMT18450
1C70	2638		1841		AIS	R3,8		CMT18460
1C72	4841	348C	1842		LH	CHAR,WDATA(R1)		CMT18470
1C76	4230	1BC6	1843		BNZ	MOVDT3	ZERO?	CMT18480
1C7A	41E0	3026	1844		BAL	R13,TSTMOD	YES - CHECK NEXT MODE	CMT18490
1C7E	4300	1BAE	1845		B	PROCO0		CMT18500
			1846	*				CMT18510
			1847	*				CMT18520
			1848	*				CMT18530
			1849	EOFER01	BAL	R14,RETRY	RETRY READ EOF	CMT18540
1C82	41E0	304A	1850		B	REOF01		CMT18550
1C86	4300	1B98	1851		B	PROCO0		CMT18560
1C8A	4300	1BAE	1852	WRTERO	LH	R14,EOTFLG	WRITE ERROR RETRY	CMT18570
1C8E	48E0	346A	1853		BZS	RCOVR	EOT? - NO - RETRY	CMT18580
1C92	2337		1854		BAL	RET,REWIND	REWIND TAPE	CMT18590
1C94	41E0	33FC	1855		BAL	R14,FSTEOF	WRITE & SENSE EOF	CMT18600
1C98	41E0	28B8	1856		B	GENFIL		CMT18610
1C9C	4300	1C18	1857	RCOVR	BAL	R14,ERRMSG2		CMT18620
1CA0	41E0	300E	1858		BAL	R14,RETRY	RETRY 5 TIMES	CMT18630
1CA4	41E0	304A	1859		B	GENFIL		CMT18640
1CA8	4300	1C18	1860		B	PROCO1		CMT18650
1CAC	4300	1C26	1861	RDERO	BAL	R14,ERRMSG2		CMT18660
1CB0	41E0	300E	1862		BAL	R14,RETRY	READ ERROR - RETRY 5 TIMES	CMT18670
1CB4	41E0	304A	1863		B	PERDR		CMT18680
1CB8	4300	1C2A	1864		B	PROCO3		CMT18690
1CBC	4300	1C38	1865	EOFER02	BAL	R14,RETRY	RETRY WEOF	CMT18700
1CC0	41E0	304A	1866		B	WEOF02		CMT18710
1CC4	4300	1C4A	1867		B	PROCO2		CMT18720
1CC8	4300	1C6C						

## TEST 1 VARIABLE RECORD LENGTH

```

1869 * *****
1870 *
1871 *           T E S T 1
1872 *
1873 *   PURPOSE:
1874 *   TO TEST THE ABILITY OF THE DEVICE TO WRITE AND READ
1875 *   VARIABLE LENGTH RECORDS.
1876 *
1877 *   ASSUMPTIONS:
1878 *   THIS TEST ASSUMES THAT TEST 0 HAD BEEN RUN WITHOUT
1879 *   DETECTING ANY FAILURE.
1880 *
1881 *   DESIGN SPECIFICATIONS:
1882 *   THIS TEST USES THE WRITE-BACKSPACE-READ FEATURE TO
1883 *   GENERATE FILES WITH VARIABLE LENGTH RECORDS. THE
1884 *   RECORDS ARE GENERATED IN THE WRITE BUFFER WITH A
1885 *   MINIMUM OF 2 BYTES. THE RECORDS WRITTEN VARIES FROM
1886 *   00-01 TO 00-FF (OR 00-3F FOR 7 TRACK MAG. TAPE.)
1887 *   THE TOTAL NUMBER OF FILES GENERATED IS DETERMINED
1888 *   BY THE OPTION FILES.
1889 *
1890 *   HOW TO RUN THE TEST:
1891 *   REFER TO TEST 0. SELECT TEST 1 AND ITS APPROPRIATE
1892 *   OPTIONS.
1893 *
1894 *   OPTIONS:
1895 *   TEST, LOOP, CONTIN, NOMSG, DEVADR, DV2ADR, SELCH,
1896 *   INTLEV, MODE, TRACK, RECFIL, FILES DUMP
1897 *   WSTART,RSTART
1898 *
1899 *   ERRORS:
1900 *   00, 01, 02, 04, 05, 08, 10, 11, 12, 13, 14, 15, 46,
1901 *   47, 50
1902 *
1903 * *****
1904 *
1905 TEST1  LHI   R4,TEST11      STARTING ADDRESS SET UP FOR
1906          BAL  R14,TSTSUP    SECOND DEVICE TEST
1907 TEST11  BAL  R14,TSTINIT    TEST INITIALIZE
1908          BAL  R13,WAIT1     WAIT FOR NMTN=1
1909          BAL  R14,FSTEOF    WRITE & SENSE EOF
1910          BAL  R14,BSET
1911          LIS  R9,1
1912          LH   R10,RCFIL+6    RECORD PER FILE DESIRED
1913          LIS  R2,1
1914          LH   R3,FILES+6
1915          NXTMOD1 LIS  R1,1
1916          VARFIL LIS  R8,2
1917          VARREC LBR  R5,R8
1918          NH   R5,MASK
1919          BNZS GENFIL1A
1920          LIS  R5,1
1921          GENFIL1A  STH  R5,NBYTE

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CMT18740
CMT18750
CMT18760
CMT18770
CMT18780
CMT18790
CMT18800
CMT18810
CMT18820
CMT18830
CMT18840
CMT18850
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CMT18890
CMT18900
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CMT18990
CMT19000
CMT19010
CMT19020
CMT19030
CMT19040
CMT19050
CMT19060
CMT19070
CMT19080
CMT19090
CMT19100
CMT19110
CMT19120
CMT19130
CMT19140
CMT19150
CMT19160
CMT19170
CMT19180
CMT19190
CMT19200
CMT19210
CMT19220
CMT19230
CMT19240
CMT19250
CMT19260

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1CCC  C840 1CD4
1CD0  41E0 2B7E
1CD4  41E0 2B36
1CD8  41E0 324C
1CDC  41E0 2BB8
1CE0  41E0 2F38
1CE4  2491
1CE6  48A0 1858
1CEA  2421
1CEC  4830 1870
1CF0  2411
1CF2  2482
1CF4  9358
1CF6  4450 3464
1CFA  2132
1CFC  2451
1CFE  4050 3466

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## TEST 1 VARIABLE RECORD LENGTH

1D02	41E0	2F0E	1922	BAL	R14,RESET	RESET BUFFER LIMITS	CMT19270
1D06	41C0	2C3A	1923	GENFIL1	BAL R12,WRTREC	WRITE A RECORD	CMT19280
1D0A	4300	1D64	1924	B	WRTER1		CMT19290
1D0E	0755		1925	XHR	R5,R5		CMT19300
1D10	4050	3470	1926	STH	R5,RTYCNT		CMT19310
1D14	41E0	2BEC	1927	PROC11	BAL R14,BSPACE	BACKSPACE & STATUS CHECK	CMT19320
1D18	41C0	2CF4	1928	RERDR1	BAL R12,RDREC	READ A RECORD	CMT19330
1D1C	4300	1D86	1929	B	RDER1		CMT19340
1D20	0755		1930	XHR	R5,R5		CMT19350
1D22	4050	3470	1931	STH	R5,RTYCNT		CMT19360
1D26	41E0	2E34	1932	PROC12	BAL R14,COMPAR	COMPARE DATA	CMT19370
1D2A	4350	1900	1933	LH	R5,SDUMP+6	DUMP?	CMT19380
1D2E	2333		1934	BZS	NODMP1		CMT19390
1D30	41E0	2F90	1935	BAL	R14,DUMP	YES - DUMP READ BUFFER	CMT19400
1D34	C180	1CF4	1936	NODMP1	BXLE R8,VARREC		CMT19410
1D38	41D0	31BE	1937	WEOF12	BAL R13,WAIT2		CMT19420
1D3C	C350	0020	1938	THI	STAT,X'20'		CMT19430
1D40	2333		1939	BZS	EOFMRK1		CMT19440
1D42	41E0	33FC	1940	BAL	RET,REWIND	REWIND TAPE	CMT19450
1D46	DE60	348B	1941	EOFMRK1	OC DEV,WEOF	WRITE EOF	CMT19460
1D4A	41E0	2FE2	1942	BAL	R14,SENS01	CHECK FOR EOF WRITTEN	CMT19470
1D4E	4300	1D96	1943	B	EOFER12		CMT19480
1D52	0755		1944	XHR	R5,R5		CMT19490
1D54	4050	3470	1945	STH	R5,RTYCNT		CMT19500
1D58	C110	1CF2	1946	PROC13	BXLE R1,VARFIL		CMT19510
1D5C	41D0	3026	1947	BAL	R13,TSTMOD	NEXT MODE?	CMT19520
1D60	4300	1CF0	1948	B	NXTMOD1		CMT19530
			1949	*			CMT19540
			1950	*	ERROR RECOVERY PROCEDURE		CMT19550
			1951	*			CMT19560
1D64	48E0	346A	1952	WRTER1	LH R14,EOTFLG	WRITE ERROR RECOVERY	CMT19570
1D68	2337		1953	BZS	RCOVR1	EOT? - NO - RETRY	CMT19580
1D6A	41E0	33FC	1954	BAL	RET,REWIND	REWIND TAPE	CMT19590
1D6E	41E0	2BB8	1955	BAL	R14,FSTEOF	WRITE & SENSE EOF	CMT19600
1D72	4300	1D06	1956	B	GENFIL1	REPEAT WRITE PROCESS	CMT19610
1D76	41E0	300E	1957	RCOVR1	BAL R14,ERRMSG2		CMT19620
1D7A	41E0	304A	1958	BAL	R14,RETRY	RETRY 5 TIMES	CMT19630
1D7E	4300	1D06	1959	B	GENFIL1		CMT19640
1D82	4300	1D14	1960	B	PROC11		CMT19650
1D86	41E0	300E	1961	RDER1	BAL R14,ERRMSG2		CMT19660
1D8A	41E0	304A	1962	BAL	R14,RETRY	RETRY 5 TIMES	CMT19670
1D8E	4300	1D18	1963	B	RERDR1		CMT19680
1D92	4300	1D26	1964	B	PROC12		CMT19690
1D96	41E0	304A	1965	EOFER12	BAL R14,RETRY	RETRY 5 TIMES	CMT19700
1D9A	4300	1D38	1966	B	WEOF12		CMT19710
1D9E	4300	1D58	1967	B	PROC13		CMT19720



## TEST 2 REWIND AND SKIP

1DE2	24A1	2022	LIS	R10,1		CMT20270	
1DE4	4830 187C	2023	LH	R3,REPEAT+6	NUMBER OF SKIP FUNCTIONS	CMT20280	
1DE8	0711	2024	XHR	R1,R1		CMT20290	
1DEA	0531	2025	CLHR	R3,R1		CMT20300	
1DEC	2332	2026	BES	REPEATO		CMT20310	
1DEE	2731	2027	SIS	R3,1		CMT20320	
1DF0	41E0 33FC	2028	REPEATO	BAL	RET,REWIND	REWIND TAPE	CMT20330
1DF4	9D65	2029	SSR	DEV,STAT		CMT20340	
1DF6	C3E0 0020	2030	THI	STAT,X'20'	EOT?	CMT20350	
1DFA	2137	2031	BNZS	SKPFWD		CMT20360	
1DFC	C800 3039	2032	LHI	R0,C'09'	NO - ERROR 09	CMT20370	
1E00	41F0 0F82	2033	BAL	R15,ERRDS		CMT20380	
1E04	4300 2B9A	2034	B	CHKEND		CMT20390	
1E08	0788	2035	SKPFWD	XHR	R8,R8	CMT20400	
1E0A	41D0 31BE	2036	SKPFOR	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT20410
1E0E	DE60 3486	2037	OC	DEV,SKIPF	SKIP FILE FORWARD	CMT20420	
1E12	41E0 2FEE	2038	BAL	R14,SENS03	CHECK FOR EOF	CMT20430	
1E16	4300 1E6A	2039	B	RERD2	NO EOF - ABORT TEST	CMT20440	
1E1A	C180 1E0A	2040	BXLE	R8,SKPFOR		CMT20450	
1E1E	0788	2041	XHR	R8,R8		CMT20460	
1E20	41D0 31BE	2042	SKPRVS	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT20470
1E24	DE60 3487	2043	OC	DEV,SKIPR	SKIP FILE REVERSE	CMT20480	
1E28	41E0 2FEE	2044	BAL	R14,SENS03	CHECK FOR EOF	CMT20490	
1E2C	4300 1E6A	2045	B	RERD2	NO EOF - ABORT TEST	CMT20500	
1E30	C180 1E20	2046	BXLE	R8,SKPRVS		CMT20510	
1E34	C110 1E08	2047	BXLE	R1,SKPFWD		CMT20520	
1E38	4830 1858	2048	LH	R3,RECFIL+6	NUMBER OF RECORDS IN FILE	CMT20530	
1E3C	41D0 31BE	2049	REOF21	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT20540
1E40	DE60 3484	2050	OC	DEV,READ	READ PASS EOF	CMT20550	
1E44	41E0 2FE8	2051	BAL	R14,SENS02	CHECK FOR EOF	CMT20560	
1E48	4300 1E90	2052	B	EOFER21		CMT20570	
1E4C	0755	2053	XHR	R5,R5		CMT20580	
1E4E	4050 3470	2054	STH	R5,RTYCNT		CMT20590	
1E52	2411	2055	PROC24	LIS	R1,1	CMT20600	
1E54	41C0 2CF4	2056	RERDR21	BAL	R12,RDREC	READ A RECORD	CMT20610
1E58	4300 1F9C	2057	B	RDER21		CMT20620	
1E5C	0755	2058	XHR	R5,R5		CMT20630	
1E5E	4050 3470	2059	STH	R5,RTYCNT		CMT20640	
1E62	41E0 2E34	2060	PROC22	BAL	R14,COMPAR	COMPARE DATA	CMT20650
1E66	C110 1E54	2061	BXLE	R1,RERDR21		CMT20660	
1E6A	4300 2B96	2062	RERD2	B	CHKEND1		CMT20670
		2063	*			*	CMT20680
		2064	*	ERROR RECOVERY PROCEDURE		*	CMT20690
		2065	*			*	CMT20700
1E6E	48E0 346A	2066	WRFR2	LH	R14,EOTFLG	EOT?	CMT20710
1E72	2337	2067	BZS	RCOVR2			CMT20720
1E74	41D0 31BE	2068	BAL	R13,WAIT2	YES -		CMT20730
1E78	DE60 3483	2069	OC	DEV,BKSPAC	BACKSPACE - END FILE		CMT20740
1E7C	4300 1DD8	2070	B	TAPEND			CMT20750
1E80	41E0 300E	2071	RCOVR2	BAL	R14,ERRMSG2		CMT20760
1E84	41E0 304A	2072	BAL	R14,RETRY	RETRY 5 TIMES		CMT20770
1E88	4300 1DC6	2073	B	GENFIL2			CMT20780
1E8C	4300 1DD4	2074	B	PROC21			CMT20790

## TEST 2 REWIND AND SKIP

1E90	41E0 304A	2075	EOFER21	BAL	R14,RETRY	RETRY 5 TIMES	CMT20800
1E94	4300 1E3C	2076		B	REOF21		CMT20810
1E98	4300 1E52	2077		B	PROC24		CMT20820
1E9C	9D65	2078	RDER21	SSR	DEV,STAT		CMT20830
1E9E	C350 0060	2079		THI	STAT,X'50'	EOT OR EOF?	CMT20840
1EA2	4230 1E6A	2080		BNZ	RERD2	YES - END OF FILE	CMT20850
1EA6	41E0 300E	2081		BAL	R14,ERRMSG2		CMT20860
1EAA	41E0 304A	2082		BAL	R14,RETRY	RETRY 5 TIMES	CMT20870
1EAE	4300 1E54	2083		B	RERDP21		CMT20880
1EB2	43C0 1E62	2084		B	PROC22		CMT20890

## TEST 3 INTERRUPT TEST

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2086 * *****
2087 *
2088 *
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2138 *

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T E S T 3

PURPOSE:

THIS TEST CHECKS ALL DEVICE FUNCTIONS UNDER DEVICE INTERRUPT. IT CHECKS FOR PROPER INTERRUPT RECEPTION, INTERRUPT QUEUING AND INTERRUPT DISARM & DISABLE.

ASSUMPTIONS:

THIS TEST ASSUMES THAT TESTS 0, 1 & 2 HAD BEEN RUN WITHOUT DETECTING ANY FAILURE.

DESIGN SPECIFICATIONS:

THE USER CAN SPECIFY THE PARTICULAR FUNCTIONS HE WISHES TO TEST BY SELECTING THE PROPER OPTIONS (SEE PROGRAM DESCRIPTION 06-172A15, SECTION 6.4). DEFAULT OPTIONS EXECUTED ARE WRITE, BACKSPACE, READ AND SKIP. THE TEST FIRST WILL CHECK IF INTERRUPT CAN BE DISARMED, DISABLED AND QUEUED. IT THEN GENERATES A FILE, ENDS IT WITH AN EOF. BACKSPACE OVER IT AND READ IT. IT REWINDS THE TAPE AND SKIPS FORWARD AND REVERSE OVER THE FILE. ALL FUNCTIONS ARE PERFORMED UNDER INTERRUPTS, IF ONLY WRITE & READ ARE SPECIFIED. THE TEST REWINDS THE TAPE BEFORE PROCEEDING TO READ THE FILE. SETTING WEOF OPTION WILL WRITE EOF'S TO THE END OF TAPE. (SEE APPENDIX 6 OF PUBLICATION 06-172R00A15)

THE RECORD SIZE IN THIS TEST CAN BE VARIED BY THE OPTION BYTES. THE LIMITS ARE FROM 2 TO X'400'. IF THE USER WISHES TO INCREASE THE UPPER LIMIT, HE MAY DO SO BY INCREASING THE CONTENT OF LOCATION LABELLED "X400". IT MUST BE NOTED THAT THE LOWER LIMIT CANNOT BE LESS THAN 2 AND THE HIGHER LIMIT MUST NOT BE CHANGED TO A VALUE HIGHER THAN X'7FFF'.

HOW TO RUN TEST:

REFER TO TEST 0. SELECT THE DESIRED OPTIONS AND TEST 3. IF DU IS SET, THE TEST WILL PRINT THE MESSAGE: "TURN DEVICE OFF-LINE MOMENTARILY." THE DEVICE MUST BE TURN OFF LINE WITHIN 60 SECONDS AFTER THE MESSAGE, BUT MUST NOT STAY OFF-LINE FOR MORE THAN 30 SECONDS.

OPTIONS:

TEST, LOOP, CONTIN, NOMSG, DEVADR, DV2ADR, SELCH, INTLEV, MODE, TRACK, RECFIL, WRITE, READ, BKSPAC, SKIP, DU, WSTART, RSTART

ERRORS:

00, 01, 02, 04, 05, 07, 08, 10, 11, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 37,

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CMT20910
CMT20920
CMT20930
CMT20940
CMT20950
CMT20960
CMT20970
CMT20980
CMT20990
CMT21000
CMT21010
CMT21020
CMT21030
CMT21040
CMT21050
CMT21060
CMT21070
CMT21080
CMT21090
CMT21100
CMT21110
CMT21120
CMT21130
CMT21140
CMT21150
CMT21160
CMT21170
CMT21180
CMT21190
CMT21200
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CMT21280
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CMT21330
CMT21340
CMT21350
CMT21360
CMT21370
CMT21380
CMT21390
CMT21400
CMT21410
CMT21420
CMT21430

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## TEST 3 INTERRUPT TEST

		2139	*	38, 39, 46, 47, 50.		*	CMT21440
		2140	*			*	CMT21450
		2141	*	*****		*	CMT21460
		2142	*			*	CMT21470
1EB6	C940 1EBE	2143	TEST3	LHI R4,TEST31		STARTING ADDRESS SET UP FOR	CMT21480
1EBA	41E0 2B7E	2144		BAL R14,TSTSUP		SECOND DEVICE TEST	CMT21490
1EBE	41E0 2B36	2145	TEST31	BAL R14,TSTINIT		TEST INITIALIZE	CMT21500
1EC2	4060 1972	2146		STH DEV,DEVSADR+2			CMT21510
1EC6	41D0 324C	2147		BAL R13,WAIT1		WAIT FOR NMTN=1	CMT21520
1ECA	4850 1894	2148		LH R5,DUINT+6		DU OPTION?	CMT21530
1ECE	4330 1EF4	2149		BZ NORINT			CMT21540
		2150	*				CMT21550
		2151	*	TEST DU INTERRUPT (0-1)			CMT21560
		2152	*				CMT21570
1ED2	C850 2284	2153		LHI R5,RTNDU1			CMT21590
1ED6	4050 196C	2154		STH R5,DEVINT+2			CMT21590
1EDA	DE60 3489	2155		OC DEV,ENABL		ENABLE DEVICE	CMT21600
1EDE	C850 3580	2156		LHI R5,MSG10			CMT21610
1EE2	41F0 112A	2157		BAL R15,PRINT			CMT21620
1EE6	41F0 126A	2158		BAL R15,TSTBRK		CHECK BREAK KEY	CMT21630
1EEA	C8E0 3332	2159		LHI R11,C'32'		ERROR 32	CMT21640
1EEE	41E0 319A	2160		BAL R14,TIMEOUT			CMT21650
1EF2	1770	2161		DC H'6000'			CMT21660
		2162	*				CMT21670
		2163	*	TEST INTERRUPT DISARM			CMT21680
		2164	*				CMT21690
1EF4	C850 2272	2165	NORINT	LHI R5,RTNDSM		SET UP RETURN ADDRESS FOR	CMT21700
1EF8	4050 196C	2166		STH R5,DEVINT+2		DISARM ERROR	CMT21710
1EFC	DE60 348A	2167		OC DEV,DISARM		DISARM DEVICE	CMT21720
1FO0	41F0 33FC	2168		BAL RET,REWIND		REWIND TAPE	CMT21730
1FO4	4840 0A22	2169		LH R4,PSW			CMT21740
1FO8	9554	2170		EPSR R5,R4		ENABLE PSW INTERRUPT	CMT21750
1FOA	4200 0000	2171		NOP		WAIT FOR ERRONOUS INTERRUPT	CMT21760
1FOE	C840 30F0	2172		LHI R4,X'30F0'		DISABLE PSW INTERRUPT	CMT21770
1F12	9554	2173		EPSR R5,R4			CMT21780
1F14	4850 1864	2174		LH R5,NBYTE+6		SET UP RECORD LENGTH	CMT21790
1F18	2751	2175		SIS R5,1			CMT21800
1F1A	4050 3466	2176		STH R5,NBYTE			CMT21810
1F1E	41E0 2F0E	2177		BAL R14,RESET			CMT21820
1F22	41E0 2F38	2178		BAL R14,BSET		SET UP WRITE BUFFER	CMT21830
1F26	2491	2179		LIS R9,1		RECORD COUNT	CMT21840
1F28	48A0 1858	2180		LH R10,RECFIL+6		NUMBER OF RECORDS PER FILE	CMT21850
1F2C	41D0 324C	2181	NXTMOD3	BAL R13,WAIT1			CMT21860
1F30	4850 18AC	2182		LH R5,OPWRT+6		WRITE OPTION SET?	CMT21870
1F34	2135	2183		BNZS EOFLOP			CMT21880
1F36	4850 18A0	2184		LH R5,OPRD+6		READ OPTION ?	CMT21890
1F3A	4230 21A4	2185		BNZ RDONLY			CMT21900
		2186	*				CMT21910
		2187	*	TEST INTERRUPT DISABLE			CMT21920
		2188	*				CMT21930
1F3E	C850 2278	2189	EOFLOP	LHI R5,RTNDSB		SET UP RETURN ADDRESS FOR	CMT21940
1F42	4050 196C	2190		STH R5,DEVINT+2		DISABLE ERROR	CMT21950
1F46	DE60 348A	2191		OC DEV,DISARM		DISARM DEVICE INTERRUPTS	CMT21960



## TEST 3 INTERRUPT TEST

1F4A	DE60	3488	2192	OC	DEV,DSABL	DISABLE DEVICE	CMT21970
1F4E	41E0	2BB8	2193	BAL	R14,FSTEOF	WRITE & SENSE EOF	CMT21980
1F52	41E0	31BE	2194	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT21990
1F56	4840	0A22	2195	LH	R4,PSW		CMT22000
1F5A	9554		2196	EPSR	R5,R4	ENABLE PSW INTERRUPT	CMT22010
1F5C	4200	0000	2197	NOP		WAIT FOR ERRONOUS INTERRUPT	CMT22020
1F60	C840	30F0	2198	LHI	R4,X'30F0'	DISABLE PSW INTERRUPT	CMT22030
1F64	9554		2199	EPSR	R5,R4		CMT22040
			2200	*			CMT22050
			2201	*	TEST INTERRUPT QUEUING		CMT22060
			2202	*			CMT22070
1F66	C850	1F7C	2203	LHI	R5,RTN01	SET UP RETURN ADDRESS 01	CMT22080
1F6A	4050	196C	2204	STH	R5,DEVINT+2		CMT22090
1F6E	DE60	3489	2205	OC	DEV,ENABL	ENABL DEVICE	CMT22100
1F72	C8B0	3337	2206	LHI	R11,C'37'	ERROR 37	CMT22110
1F76	41E0	319A	2207	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT22120
1F7A	0002		2208	DC	H'2'		CMT22130
			2209	*			CMT22140
			2210	*	TEST INTERRUPT AFTER REWIND		CMT22150
			2211	*			CMT22160
1F7C	C850	1FA2	2212	RTN01	LHI R5,RTN02	SET UP RETURN ADDRESS 02	CMT22170
1F90	4050	196C	2213	STH	R5,DEVINT+2		CMT22180
1F84	DE60	348A	2214	OC	DEV,DISARM	DISARM INTERRUPTS	CMT22190
1F88	DE60	3489	2215	OC	DEV,ENABL	ENABLE DEVICE INTERRUPT	CMT22200
1F8C	DE60	3482	2216	OC	DEV,REWD	REWIND	CMT22210
1F90	C8B0	3230	2217	LHI	R11,C'20'	ERROR 20	CMT22220
1F94	41E0	319A	2218	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT22230
1F98	03E8		2219	DC	H'1000'		CMT22240
1F9A	41D0	324C	2220	BAL	R13,WAIT1	WAIT FOR NMTN=1	CMT22250
1F9E	4300	1FC6	2221	B	LPEOF		CMT22260
1FA2	D350	16D2	2222	RTN02	LB STAT,INTSTA	GET INTERRUPT STATUS	CMT22270
1FA6	C550	0034	2223	CLHI	STAT,X'34'	X'34'	CMT22280
1FAA	4330	1FC6	2224	BE	LPEOF	YES - GO ON	CMT22290
1FAE	C800	3039	2225	STER02	LHI R0,C'09'	NO - ERROR 09	CMT22300
1FB2	C350	0001	2226	STAERR	THI STAT,1	DU?	CMT22310
1FB6	4230	32A0	2227	BNZ	MTDU		CMT22320
1FBA	41F0	0F82	2228	STERR2	BAL R15,ERRDS		CMT22330
1FBE	DE60	348A	2229	CC	DEV,DISARM		CMT22340
1FC2	4300	2B96	2230	B	CHKEND1		CMT22350
			2231	*			CMT22360
			2232	*	TEST INTERRUPTS AFTER WRITE EOF		CMT22370
			2233	*			CMT22380
1FC6	C850	1FE8	2234	LPEOF	LHI R5,RTN03	SET RETURN ADDRESS 03	CMT22390
1FCA	4050	196C	2235	STH	R5,DEVINT+2		CMT22400
1FCE	DE60	348A	2236	OC	DEV,DISARM	DISARM INTERRUPTS	CMT22410
1FD2	DE60	3489	2237	OC	DEV,ENABL	ENABLE DEVICE INTERRUPT	CMT22420
1FD6	DE60	348B	2238	OC	DEV,WEOF	WRITE EOF	CMT22430
1FDA	C8B0	3231	2239	LHI	R11,C'21'	ERROR 21	CMT22440
1FDE	41E0	319A	2240	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT22450
1FE2	0064		2241	DC	H'100'		CMT22460
1FE4	4300	205C	2242	B	STA05A		CMT22470
1FE8	D350	16D2	2243	RTN03	LB STAT,INTSTA	CHECK STATUS FOR	CMT22480
1FEC	C550	004C	2244	CLHI	STAT,X'4C'	5X INTERRUPT	CMT22490

## TEST 3 INTERRUPT TEST

1FF0	4230	2038	2245	BNE	STAERR1		CMT22500
1FF4	C850	200A	2246	STA03	LHI	R5,RTN04	CMT22510
1FF8	4050	196C	2247		STH	R5,DEVINT+2	CMT22520
1FFC	C8B0	3232	2248		LHI	R11,C'22'	CMT22530
2000	41E0	319A	2249		BAL	R14,TIMEOUT	CMT22540
2004	000A		2250		DC	H'10'	CMT22550
2006	4300	205C	2251		B	STA05A	CMT22560
200A	D350	16D2	2252	RTN04	LB	STAT,INTSTA	CMT22570
200E	C550	0046	2253		CLHI	STAT,X'46'	CMT22580
2012	4230	2038	2254		BNE	STAERR1	CMT22590
2016	C850	202C	2255	STA04	LHI	R5,RTN05	CMT22600
201A	4050	196C	2256		STH	R5,DEVINT+2	CMT22610
201E	C8F0	3233	2257		LHI	R11,C'23'	CMT22620
2022	41E0	319A	2258		BAL	R14,TIMEOUT	CMT22630
2026	000A		2259		DC	H'10'	CMT22640
2028	4300	205C	2260		B	STA05A	CMT22650
202C	D350	16D2	2261	RTN05	LB	STAT,INTSTA	CMT22660
2030	C550	0056	2262		CLHI	STAT,X'56'	CMT22670
2034	4330	2060	2263		BE	STA05	CMT22680
2038	C350	0001	2264	STAERR1	THI	STAT,1	CMT22690
203C	4230	32A0	2265		BNZ	MTDU	CMT22700
2040	C800	3035	2266		LHI	R0,C'05'	CMT22710
2044	C350	0020	2267		THI	STAT,X'20'	CMT22720
2048	4330	1FB2	2268		BZ	STAERR	CMT22730
204C	C850	3506	2269		LHI	R5,M3G04	CMT22740
2050	41F0	112A	2270		BAL	R15,PRINT	CMT22750
2054	DE60	348A	2271		OC	DEV,DISARM	CMT22760
2058	4300	2896	2272		B	CHKEND1	CMT22770
205C	41D0	31BE	2273	STA05A	BAL	R13,WAIT2	CMT22780
2060	4850	18D0	2274	STA05	LH	R5,OPWEOF+6	CMT22790
2064	4230	1FC6	2275		BNZ	LPEOF	CMT22800
			2276	*			CMT22810
			2277	*	TEST WRITE INTERRUPTS		CMT22820
			2278	*			CMT22830
2068	2481		2279		LIS	R8,1	CMT22840
206A	DE60	348A	2280	WREC3	CC	DEV,DISARM	CMT22850
206E	4850	346E	2281		LH	R5,MODFLG	CMT22860
2072	C550	0002	2282		CLHI	R5,2	CMT22870
2076	4330	22FE	2283		BE	SELINW	CMT22880
207A	C850	208A	2284		LHI	R5,RTN06A	CMT22890
207E	4050	196C	2285		STH	R5,DEVINT+2	CMT22900
2082	D0F0	3628	2286		STM	R15,RSV32	CMT22910
2086	D1F0	3618	2287		LM	R15,WLIM	CMT22920
208A	08BF		2288		LHR	R11,R15	CMT22930
208C	D1F0	361C	2289		LM	R15,WLIM+4	CMT22940
2090	08CF		2290		LHR	R12,R15	CMT22950
2092	D1F0	3628	2291		LM	R15,RSV32	CMT22960
2096	41D0	31BE	2292		BAL	R13,WAIT2	CMT22970
209A	DE60	3485	2293		OC	DEV,WRITE	CMT22980
209E	966B		2294		WBR	DEV,R11	CMT22990
20A0	9D65		2295		SSR	DEV,STAT	CMT23000
20A2	2081		2296		BTBS	8,1	CMT23010
20A4	DE60	3489	2297	STA06	OC	DEV,ENABL	CMT23020
						ENABLE DEVICE INTERRUPT	

## TEST 3 INTERRUPT TEST

20A8	C8B0	3236	2298	LHI	R11,C'26'	ERROR 25	CMT23030
20AC	41E0	319A	2299	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT23040
20B0	000A		2300	DC	H'10'		CMT23050
20B2	41D0	320C	2301	BAL	R13,WAIT3	WAIT FOR FOM=1	CMT23060
20B6	9D65		2302	SSR	DEV,STAT		CMT23070
20B8	2303		2303	BS	RTN06A+4		CMT23080
20BA	D350	16D2	2304	RTN06A LB	STAT,INTSTA	GET INTERRUPT STATUS	CMT23090
20BE	C350	0001	2305	THI	STAT,1	DJ?	CMT23100
20C2	4230	32A0	2306	BNZ	MTDU		CMT23110
20C6	C350	0020	2307	THI	STAT,X'20'	EOT?	CMT23120
20CA	2336		2308	BZS	WRTON3	NO - BRANCH	CMT23130
20CC	41F0	2BEC	2309	BAL	R14,BSPACE		CMT23140
20D0	DE60	347F	2310	OC	DEV,CLEAR		CMT23150
20D4	230D		2311	BS	WRTEND		CMT23160
20D6	C350	0004	2312	WRTON3 THI	STAT,X'04'	EX?	CMT23170
20DA	2134		2313	BNZS	STERO6A	YES - STATUS ERROR	CMT23180
20DC	C350	0002	2314	THI	STAT,X'02'	EOM INTERRUPT?	CMT23190
20E0	2135		2315	BNZS	STA06A	YES - GO ON	CMT23200
20E2	C800	3130	2316	STERO6A LHI	R0,C'10'	NO - ERROR 10	CMT23210
20E6	41E0	300E	2317	BAL	R14,ERRMSG2		CMT23220
20EA	C180	206A	2318	STA06A BXLE	R8,WREC3		CMT23230
20EE	41D0	31BE	2319	WRTEND BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT23240
20F2	DE60	348B	2320	OC	DEV,WEOF	WRITE EOF	CMT23250
20F6	4850	18B8	2321	LH	R5,OPBSP+6	BACKSPACE OPTION SET ?	CMT23260
20FA	4330	22C4	2322	BZ	NOBSP		CMT23270
			2323	*			CMT23280
			2324	*	TEST BACKSPACE EOF INTERRUPT		CMT23290
			2325	*			CMT23300
20FE	C850	2124	2326	LHI	R5,RTN07	SET UP RETURN ADDRESS 07	CMT23310
2102	4050	196C	2327	STH	R5,DEVINT+2		CMT23320
2106	41D0	31BE	2328	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT23330
210A	DE60	348A	2329	OC	DEV,DISARM	DISARM QUEUED INTERRUPTS	CMT23340
210E	DE60	3489	2330	OC	DEV,ENABL	ENABLE DEVICE INTERRUPT	CMT23350
2112	DE60	3483	2331	OC	DEV,BKSPAC	BACKSPACE OVER EOF	CMT23360
2116	C8B0	3234	2332	LHI	R11,C'24'	ERROR 24	CMT23370
211A	41E0	319A	2333	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT23380
211E	0032		2334	DC	H'50'		CMT23390
2120	4300	29AC	2335	B	BSFIL		CMT23400
2124	D350	16D2	2336	RTN07 LB	STAT,INTSTA	GET INTERRUPT STATUS	CMT23410
2128	C550	0046	2337	CLHI	STAT,X'46'		CMT23420
212C	2138		2338	BNES	STA07		CMT23430
212E	C550	004C	2339	CLHI	STAT,X'4C'		CMT23440
2132	2135		2340	BNES	STA07		CMT23450
2134	C800	3037	2341	LHI	R0,C'07'	YES - ERROR 07	CMT23460
2138	4300	1FB2	2342	B	STAERR		CMT23470
			2343	*			CMT23480
			2344	*	TEST BACKSPACE RECORD INTERRUPT		CMT23490
			2345	*			CMT23500
213C	C8E0	2168	2346	STA07 LHI	R5,RTN08	SET UP RETURN ADDRESS 08	CMT23510
2140	4050	196C	2347	STH	R5,DEVINT+2		CMT23520
2144	2481		2348	LIS	R8,1		CMT23530
2146	41D0	31BE	2349	BSPFIL BAL	13,WAIT2	WAIT FOR NMTN=1	CMT23540
214A	DE60	348A	2350	OC	DEV,DISARM	DISARM QUEUED INTERRUPTS	CMT23550

## TEST 3 INTERRUPT TEST

214E	DE60 3489	2351	OC	DEV,ENABL	ENABLE DEVICE INTERRUPT	CMT23560
2152	DE60 3483	2352	OC	DEV,BKSPAC	BACKSPACE OVER A RECORD	CMT23570
2156	C8B0 3235	2353	LHI	R11,C'25'	ERROR 25	CMT23580
215A	41E0 319A	2354	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT23590
215E	0032	2355	DC	H'50'		CMT23600
2160	41D0 320C	2356	BAL	R13,WAIT3	WAIT FOR EOM=1	CMT23610
2164	4300 2190	2357	B	STAO8		CMT23620
2168	D350 16D2	2358	RTN08	LB STAT,INTSTA	GET INTERRUPT STATUS	CMT23630
216C	C350 0001	2359	THI	STAT,1	DJ?	CMT23640
2170	4230 32A0	2360	BNZ	MTDJ		CMT23650
2174	C350 0040	2361	THI	STAT,X'40'	EOF?	CMT23660
2178	4230 219C	2362	BNZ	TRYRD		CMT23670
217C	C350 0004	2363	THI	STAT,X'04'	EX?	CMT23680
2180	2134	2364	BNZS	STER08	YES - STATUS ERROR	CMT23690
2182	C350 0002	2365	THI	STAT,X'02'	EOM?	CMT23700
2186	2135	2366	BNZS	STAO8	YES - GO ON	CMT23710
2188	C800 3038	2367	STEP08	LHI R0,C'08'	NO - ERROR 08	CMT23720
218C	41F0 0F82	2368	BAL	R15,ERRDS		CMT23730
2190	C180 2146	2369	STAO8	BXLE R8,BSPFIL		CMT23740
2194	41D0 31BE	2370	BAL	R13,WAIT2		CMT23750
2198	DE60 3483	2371	OC	DEV,BKSPAC		CMT23760
219C	4850 18A0	2372	TRYRD	LH R5,OPRD+6	READ OPTION SET?	CMT23770
21A0	4330 223E	2373	BZ	NOREAD		CMT23780
		2374	*			CMT23790
		2375	*	TEST READ INTERRUPTS		CMT23800
		2376	*			CMT23810
21A4	41D0 31BE	2377	RONLY	BAL R13,WAIT2	WAIT FOR NMTN=1	CMT23820
21A8	DE60 3484	2378	OC	DEV,READ	READ PASS EOF	CMT23830
21AC	2481	2379	LIS	R8,1		CMT23840
21AE	DE60 348A	2380	RREC3	OC DEV,DISARM	DISARM QUEUED INTERRUPTS	CMT23850
21B2	41D0 2F62	2381	BAL	R13,CBBUF	CLEAR READ BUFFER	CMT23860
21B6	4850 346E	2382	LH	R5,MODEFLG		CMT23870
21BA	C550 0002	2383	CLHI	R5,2	SELCH MODE?	CMT23880
21BE	4330 231E	2384	BE	SELINR		CMT23890
21C2	C850 21FE	2385	LHI	R5,RTN09A	SET UP RETURN ADDRESS 09A	CMT23900
21C6	4050 196C	2386	STH	R5,DEVINT+2		CMT23910
21CA	D0F0 3628	2387	STM	R15,RSV32	SAVE R15	CMT23920
21CE	D1F0 3620	2388	LM	R15,RLIM	READ BUFFER ADDRESS	CMT23930
21D2	08BF	2389	LHR	R11,R15		CMT23940
21D4	D1F0 3624	2390	LM	R15,RLIM+4	END ADDRESS	CMT23950
21D8	08CF	2391	LHR	R12,R15		CMT23960
21DA	D1F0 3628	2392	LM	R15,RSV32		CMT23970
21DE	41D0 31BE	2393	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT23980
21E2	DE60 3484	2394	OC	DEV,READ	DEVICE READ	CMT23990
21E6	976B	2395	RBR	DEV,R11	READ BLOCK	CMT24000
21E8	DE60 3489	2396	STAO9	OC DEV,ENABL	ENABLE DEVICE INTERRUPT	CMT24010
21EC	C8E0 3237	2397	LHI	R11,C'27'	ERROR 27	CMT24020
21F0	41E0 319A	2398	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT24030
21F4	0032	2399	DC	H'50'		CMT24040
21F6	41D0 320C	2400	BAL	R13,WAIT3	WAIT FOR EOM=1	CMT24050
21FA	9D65	2401	SSR	DEV,STAT		CMT24060
21FC	2303	2402	BS	RTN09A+4		CMT24070
21FE	D350 16D2	2403	RTN09A	LB STAT,INTSTA	GET INTERRUPT STATUS	CMT24080

## TEST 3 INTERRUPT TEST

2202	C350 0001	2404	THI	STAT,1	DU?	CMT24090	
2206	4230 32A0	2405	BNZ	MTDU		CMT24100	
220A	C350 0060	2406	THI	STAT,X'60'		CMT24110	
220E	4230 223E	2407	BNZ	NOREAD		CMT24120	
2212	C350 0004	2408	THI	STAT,X'04'	EX?	CMT24130	
2216	2134	2409	BNZS	STER09A		CMT24140	
2218	C350 0002	2410	THI	STAT,X'02'	EOM?	CMT24150	
221C	2135	2411	BNZS	RDEND		CMT24160	
221E	C800 3131	2412	STER09A	LHI	RO,C'11'	ERROR 11	CMT24170
2222	41E0 300E	2413	BAL	R14,ERRMSG2		CMT24180	
2226	4850 18DC	2414	RDEND	LH	R5,CMPRE+6	COMPARE OPTION SET ?	CMT24190
222A	2333	2415	BZS	TRYDUM		CMT24200	
222C	41E0 2E34	2416	BAL	R14,COMPAR		CMT24210	
2230	4850 1900	2417	TRYDUM	LH	R5,SDUMP+6	DUMP OPTION SET	CMT24220
2234	2333	2418	BZS	CONT3		CMT24230	
2236	41E0 2F90	2419	BAL	R14,DUMP	DUMP READ BUFFER	CMT24240	
223A	C180 21AE	2420	CONT3	BXLE	R8,RREC3	CMT24250	
223E	41E0 33FC	2421	NOREAD	BAL	RET,REWIND	REWIND TAPE	CMT24260
2242	4850 18C4	2422	LH	R5,OPSKIP+6	SKIP OPTION?	CMT24270	
2246	4330 2262	2423	BZ	ENDTST3		CMT24280	
		2424	*			CMT24290	
		2425	*	TEST SKIP INTERRUPTS		CMT24300	
		2426	*			CMT24310	
224A	D310 3486	2427	LB	R1,SKIPF	LOAD SKIP FORWARD COMMAND	CMT24320	
224E	C8E0 3330	2428	LHI	R11,C'30'	ERROR 30	CMT24330	
2252	41C0 33A0	2429	BAL	R12,SKIPINT		CMT24340	
2256	D310 3487	2430	LB	R1,SKIPR	LOAD SKIP REVERSE COMMAND	CMT24350	
225A	C8E0 3331	2431	LHI	R11,C'31'	ERROR 31	CMT24360	
225E	41C0 33A0	2432	BAL	R12,SKIPINT		CMT24370	
2262	DE60 348A	2433	ENDTST3	OC	DEV,DISARM	CMT24380	
2266	41E0 33FC	2434	BAL	RET,REWIND	REWIND TAPE	CMT24390	
226A	41C0 3026	2435	BAL	R13,TSTM0D		CMT24400	
226E	43C0 1F2C	2436	B	NXTMOD3	NEXT MODE	CMT24410	
		2437	*			CMT24420	
		2438	*	DISARM FAILURE		CMT24430	
		2439	*			CMT24440	
2272	C800 3338	2440	RTNDSM	LHI	RO,C'38'	ERROR 38	CMT24450
2276	2303	2441	BS	INTER31		CMT24460	
		2442	*			CMT24470	
		2443	*	DISABLE FAILURE		CMT24480	
		2444	*			CMT24490	
2278	C800 3339	2445	RTNDSB	LHI	RO,C'39'	ERROR 39	CMT24500
227C	D350 16D2	2446	INTER31	LB	STAT,INTSTA	GET INTERRUPT STATUS	CMT24510
2280	4300 1FBA	2447	B	STERR2		CMT24520	
		2448	*			CMT24530	
		2449	*	DU INTERRUPT		CMT24540	
		2450	*			CMT24550	
2284	D350 16D2	2451	RTNDU1	LB	STAT,INTSTA		CMT24560
2288	C350 0001	2452	THI	STAT,X'01'	DU BIT SET?	CMT24570	
228C	4330 22B8	2453	BZ	DUSTER		CMT24580	
		2454	*			CMT24590	
		2455	*	TEST DU INTERRUPT (1-0)		CMT24600	
		2456	*			CMT24610	

## TEST 3 INTERRUPT TEST

2290	C850	22A6	2457	LHI	R5,RTNDU2		CMT24620	
2294	4050	196C	2458	STH	R5,DEVINT+2		CMT24630	
2298	C8B0	3334	2459	LHI	R11,C'34'	ERROR 34	CMT24640	
229C	41E0	319A	2460	BAL	R14,TIMEOUT		CMT24650	
22A0	0BP8		2461	DC	H'3000'		CMT24660	
22A2	4300	32A0	2462	B	MTDU		CMT24670	
22A6	D350	16D2	2463	RTNDU2	LB	STAT,INTSTA	CMT24680	
22AA	C350	0001	2464	THI	STAT,X'01'	DU BIT SET ?	CMT24690	
22AE	4330	1EF4	2465	BZ	NORINT		CMT24700	
22B2	C800	3335	2466	LHI	R0,C'35'	ERROR 35	CMT24710	
22B6	2303		2467	BS	DUSTER+4		CMT24720	
22B8	C800	3333	2468	DUSTER	LHI	R0,C'33'	ERROR 33	CMT24730
22BC	41F0	0F82	2469	BAL	R15,ERRDS		CMT24740	
22C0	4300	1EF4	2470	B	NORINT		CMT24750	
22C4	C850	22EE	2471	NOBSP	LHI	R5,RTN10	NO BACKSPACE OPTION:	CMT24760
22C8	4050	196C	2472	STH	R5,DEVINT+2	SET UP INTERRUPT RETURN ADRS 10	CMT24770	
22CC	41D0	31BE	2473	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT24780	
22D0	DE60	348A	2474	OC	DEV,DISARM	DISARM QUEUED INTERRUPTS	CMT24790	
22D4	DE60	3489	2475	OC	DEV,ENABL	ENABLE DEVICE INTERRUPT	CMT24800	
22D8	DE60	3482	2476	OC	DEV,REWD	REWIND	CMT24810	
22DC	C8B0	3230	2477	LHI	R11,C'20'	ERROR 20	CMT24820	
22E0	41E0	319A	2478	BAL	R14,TIMEOUT	WAIT FOR INTERRUPT	CMT24830	
22E4	03E8		2479	DC	H'1000'		CMT24840	
22E6	41D0	324C	2480	BAL	R13,WAIT1	WAIT FOR NMTN=1	CMT24850	
22EA	4300	219C	2481	B	TRYRD		CMT24860	
22EE	D350	16D2	2482	RTN10	LB	STAT,INTSTA	GET INTERRUPT STATUS	CMT24870
22F2	C550	0034	2483	CLHI	STAT,X'34'	ET, NMTN AND EX=1?	CMT24880	
22F6	4230	1FAE	2484	BNE	STER02	NO - STATUS ERROR	CMT24890	
22FA	4300	219C	2485	B	TRYRD		CMT24900	
			2486	*			CMT24910	
			2487	*	TEST SELCH INTERRUPTS:		CMT24920	
			2488	*			CMT24930	
22FE	D310	3485	2489	SELINW	LB	R1,WRITE	DEVICE COMMAND	CMT24940
2302	D320	3480	2490		LB	R2,GOWRT	SELCH GO & COMMAND	CMT24950
2306	C830	3618	2491		LHI	R3,WLIM	SELCH WRITE LIMITS	CMT24960
230A	C840	20BA	2492		LHI	R4,RTN06A	DEVICE INTERRUPT RETURN ADDRESS	CMT24970
230E	C850	3390	2493		LHI	R5,SELINT1	SELCH INTERRUPT RETURN ADDRESS	CMT24980
2312	C8B0	3238	2494		LHI	R11,C'28'	ERROR 28	CMT24990
2316	41C0	3350	2495		BAL	R12,SELINT		CMT25000
231A	4300	20A4	2496		B	STA06		CMT25010
231E	D310	3484	2497	SELINR	LB	R1,READ	DEVICE COMMAND	CMT25020
2322	D320	3481	2498		LB	R2,GORD	SELCH GO & COMMAND	CMT25030
2326	C830	3620	2499		LHI	R3,RLIM	SELCH READ LIMITS	CMT25040
232A	C840	21FE	2500		LHI	R4,RTN09A	DEVICE INTERRUPT RETURN ADDRESS	CMT25050
232E	C850	3390	2501		LHI	R5,SELINT1	SELCH INTERRUPT RETURN ADDRESS	CMT25060
2332	C8B0	3239	2502		LHI	R11,C'29'	ERROR 29	CMT25070
2336	41C0	3350	2503		BAL	R12,SELINT		CMT25080
233A	4300	21E8	2504		B	STA09		CMT25090

## TEST 4 WRITE LONG/READ SHORT

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2506 * *****
2507 *
2508 *           T E S T 4
2509 *
2510 *   PURPOSE:
2511 *   TO TEST THE PROPER FUNCTIONING OF THE OVERFLOW
2512 *   CIRCUITRY, AND THE DETECTION OF ABNORMAL I/O
2513 *   CONDITIONS.
2514 *
2515 *   ASSUMPTIONS:
2516 *   THIS TEST ASSUMES THAT TEST 0 HAD BEEN RUN WITHOUT
2517 *   DETECTING ANY FAILURE.
2518 *
2519 *   DESIGN SPECIFICATION:
2520 *   A RECORD IS GENERATED AND THE SAME RECORD IS READ
2521 *   PLUS 32 BYTES. THE PROGRAM TESTS FOR DETECTION OF
2522 *   ABNORMAL TERMINATION OF THE READ OPERATION.
2523 *   CONVERSELY, OVERFLOW IS CHECKED BY READING A RECORD
2524 *   SHORTER THAN THE ONE WRITTEN.
2525 *
2526 *   HOW TO RUN THE TEST:
2527 *   SELECT TEST 4 AND APPROPRIATE OPTIONS, AND ENTER RUN.
2528 *   REFER TO TEST 0.
2529 *
2530 *   OPTIONS:
2531 *   TEST, LOOP, CONTIN, NOMSG, DEVADR, DV2ADR, SELCH,
2532 *   INTLEN, MODE, TRACK, RECFIL, DUMP
2533 *   WSTART, RSTART
2534 *
2535 *   ERRORS:
2536 *   00, 01, 02, 04, 05, 08, 10, 11, 12, 13, 14, 15, 16,
2537 *   17, 18, 46, 47, 50.
2538 *
2539 * *****
2540 *
233E C840 2346 2541 TEST4 LHI R4,TEST41          STARTING ADDRESS SET UP FOR
2342 41E0 237E 2542 BAL R14,TSTSUP          SECOND DEVICE TEST
2346 41E0 233E 2543 TEST41 BAL R14,TSTINIT        TEST INITIALIZE
234A 41E0 33FC 2544 NXTMOD4 BAL RET,REWIND      REWIND TAPE
234E 41E0 2988 2545 BAL R14,FSTEOF          WRITE & SENSE EOF
2352 0755 2546 XHR R5,R5              CLEAR WRITE-LONG/READ-SHORT FLAG
2354 4050 3476 2547 STH R5,WLRS
2358 41E0 270E 2548 BAL R14,RESET          SET BUFFER LIMITS
235C 41E0 2F38 2549 BAL R14,BSET          SET WRITE BUFFER
2360 D0F0 3628 2550 STM R15,RSAV32
2364 D1F0 351C 2551 LM R15,WLIM+4
2368 CBFO 0020 2552 SHI R15,32
236C D0F0 361C 2553 STM R15,WLIM+4
2370 D1F0 3628 2554 LM R15,RSAV32
2374 48A0 1858 2555 LH R10,RCFIL+6        NUMBER OF RECORDS
2378 2491 2556 LIS R9,1
237A 2481 2557 GENFIL4 LIS R8,1
237C 41C0 2C3A 2558 GFIL41 BAL R12,WRTREC      WRITE A RECORD
CNT25110
CNT25120
CNT25130
CNT25140
CNT25150
CNT25160
CNT25170
CNT25180
CNT25190
CNT25200
CNT25210
CNT25220
CNT25230
CNT25240
CNT25250
CNT25260
CNT25270
CNT25280
CNT25290
CNT25300
CNT25310
CNT25320
CNT25330
CNT25340
CNT25350
CNT25360
CNT25370
CNT25380
CNT25390
CNT25400
CNT25410
CNT25420
CNT25430
CNT25440
CNT25450
CNT25460
CNT25470
CNT25480
CNT25490
CNT25500
CNT25510
CNT25520
CNT25530
CNT25540
CNT25550
CNT25560
CNT25570
CNT25580
CNT25590
CNT25600
CNT25610
CNT25620
CNT25630

```

## TEST 4 WRITE LONG/READ SHORT

2380	4300	23F8	2559	B	WRTER4		CNT25640
2384	07E5		2560	XHR	R5,R5		CNT25650
2386	4050	3470	2561	STH	R5,RTYCMT		CNT25660
238A	41E0	2BEC	2562	PROC41	BAL R14,BSPACE	BACKSPACE A RECORD	CNT25670
238E	41C0	2CF4	2563	RERDR4	BAL R12,RDREC	READ A RECORD	CNT25680
2392	4300	2410	2564	B	RDER4	ERROR RETURN - CHECK STATUS	CNT25690
2396	C800	3136	2565	LHI	RO,C'16'	NORMAL RETURN - ERROR 16	CNT25700
239A	41E0	300E	2566	BAL	R14,ERRMSG2		CNT25710
239E	41E0	304A	2567	BAL	R14,RETRY	RETRY 5 TIMES	CNT25720
23A2	4300	238E	2568	B	RERDR4		CNT25730
23A6	4850	1900	2569	PROC42	LH R5,SDUMP+6	DUMP OPTION?	CNT25740
23AA	2333		2570	BZS	PROC43		CNT25750
23AC	41E0	2F90	2571	BAL	R14,DUMP	YES - DUMP READ BUFFER	CNT25760
23B0	C180	237C	2572	PROC43	BXLE R8,GFIL41	CONTINUE	CNT25770
23B4	41D0	31BE	2573	BAL	R13,WAIT2	WAIT FOR NMTN=1	CNT25780
23B8	DE60	348B	2574	OC	DEV,WEOF		CNT25790
23BC	4850	3476	2575	TAPEND4	LH R5,WLRS	WRITE-LONG/READ-SHORT?	CNT25800
23C0	2337		2576	BZS	CONT4		CNT25810
23C2	41E0	33FC	2577	BAL	RET,REWIND	REWIND TAPE	CNT25820
23C6	41D0	3026	2578	BAL	R13,TSTMOD	YES - CHECK MORE MODE	CNT25830
23CA	4300	234A	2579	B	NXTMOD4		CNT25840
23CE	245F		2580	CONT4	LIS R5,15	NO - SET WRITE-LONG/READ-SHORT FLAG	CNT25850
23D0	4050	3476	2581	STH	R5,WLRS		CNT25860
23D4	41E0	33FC	2582	BAL	RET,REWIND	REWIND TAPE	CNT25870
23D8	41E0	2888	2583	BAL	R14,FSTEOF	WRITE & SENSE EOF	CNT25880
23DC	41E0	2F0E	2584	BAL	R14,RESET	SET BUFFER LIMITS	CNT25890
23E0	D0F0	3628	2585	STM	R15,RSV32		CNT25900
23E4	D1E0	3624	2586	LH	R15,RLIN+4		CNT25910
23E8	CBF0	0020	2587	SHI	R15,32		CNT25920
23EC	D0F0	3624	2588	STM	R15,RLIN+4		CNT25930
23F0	D1F0	3628	2589	LH	R15,RSV32		CNT25940
23F4	4300	237A	2590	B	GENFIL4	GO TO NEXT STEP	CNT25950
			2591	*			CNT25960
			2592	*	ERROR PROCEDURE		CNT25970
			2593	*			CNT25980
23F8	48E0	346A	2594	WRTER4	LH R14,EOTFLG	EOT?	CNT25990
23FC	4230	23BC	2595	BNZ	TAPEND4	YES - END OF STEP	CNT26000
2400	41E0	300E	2596	BAL	R14,ERRMSG2		CNT26010
2404	41E0	304A	2597	BAL	R14,RETRY	RETRY 5 TIMES	CNT26020
2408	4300	237C	2598	B	GFIL41		CNT26030
240C	4300	238A	2599	B	PROC41		CNT26040
2410	9D65		2600	RDER4	SSR DEV,STAT	WRITE-LONG/READ-SHORT?	CNT26050
2412	4800	3476	2601	LH	RO,WLRS		CNT26060
2416	4330	2436	2602	BZ	WSRL		CNT26070
241A	C350	0080	2603	THI	STAT,X'80'	YES - ERR SET?	CNT26080
241E	4230	2442	2604	BNZ	NORMAL	YES - CONTINUE	CNT26090
2422	C8G0	3137	2605	LHI	RO,C'17'	NO - ERROR 17	CNT26100
2426	41E0	300E	2606	WERLS	BAL R14,ERRMSG2		CNT26110
242A	41E0	304A	2607	BAL	R14,RETRY	RETRY 5 TIMES	CNT26120
242E	4300	238E	2608	B	RERDR4		CNT26130
2432	4300	23A6	2609	B	PROC42		CNT26140
2436	C350	0080	2610	WSRL	THI STAT,X'80'	ERR SET?	CNT26150
243A	2334		2611	BZS	NORMAL	NO - CONTINUE	CNT26160



TEST 4 WRITE LONG/READ SHORT

243C	C800 3138	2612	LHI	RO,C*18*	YES - ERROR 18	CMT26170
2440	220D	2613	BS	WERLS		CMT26180
2442	0755	2614	NORMAL	XHR		CMT26190
2444	4050 3470	2615	SIH	R5,R5		CMT26200
2448	4300 23A6	2616	B	R5,RTYCNT		CMT26210
				PROC42		

## TEST 5 INTER-RECORD GAP TEST

```

2618 * *****
2619 *
2620 *           T E S T 5
2621 *
2622 *   PURPOSE:
2623 *   TO TEST THE PROPER GENERATION OF INTER-RECORD-GAPS.
2624 *   AND DETECTION OF GAP DATA.
2625 *   NOTE: PROLONGED REPETITION OF THIS TEST MAY WEAR THE
2626 *   FRONT PORTION OF THE TAPE.
2627 *
2628 *   ASSUMPTIONS:
2629 *   THIS TEST ASSUMES THAT TESTS 0 AND 4 HAD BEEN RUN
2630 *   WITHOUT DETECTING ANY FAILURE.
2631 *
2632 *   DESIGN SPECIFICATIONS:
2633 *   THIS TEST GENERATES LONG (512 BYTES) RECORDS OF
2634 *   ALL ONES (FF) ON THE TAPE. IT THEN REWINDS AND
2635 *   WRITE A SHORT RECORD OF VARIOUS DATA (00-FF) OVER
2636 *   THE SAME PORTION OF THE TAPE FOR 100 TIMES. SINCE
2637 *   BACKSPACE DOES NOT ALWAYS STOP AT THE SAME SPOT,
2638 *   ALL THE RECORDS ARE NOT WRITTEN DIRECTLY OVER EACH
2639 *   OTHER. THE LAST RECORD IS WRITTEN REVERSED. THE
2640 *   TAPE IS REWOUND AND THE RECORD READ. THE READ IS
2641 *   REPEATED FOR THE NUMBER OF TIMES AS SPECIFIED BY
2642 *   OPTION IRG. THIS ENSURES THE PICKING UP OF ANY
2643 *   DATA LEFT BY THE PREVIOUS RECORDS WRITTEN.
2644 *
2645 *   OPTIONS:
2646 *   TEST, LOOP, CONTIN, NOMSG, DEVADR, DV2ADR, SELCH,
2647 *   INTLEV, MODE, TRACK, IRG
2648 *   WSTART,RSTART
2649 *
2650 *   ERRORS:
2651 *   00, 01, 02, 04, 05, 07, 08, 10, 11, 12, 13, 14, 15,
2652 *   19, 46, 47, 50
2653 *
2654 * *****
2655 *
244C  C840 2454 2656 TEST5 LHI R4,TEST51          STARTING ADDRESS SET UP FOR
2450  41E0 2B7E 2657          BAL R14,TSTSUP          SECOND DEVICE TEST
2454  41E0 2B36 2658 TEST51  BAL R14,TSTINIT        TEST INITIALIZE
2458  41E0 33FC 2659          BAL RET,REWIND          REWIND TAPE
245C  41E0 2BB8 2660          BAL R14,FSTEOF          WRITE & SENSE EOF
2460  2492 2661          LIS R9,2
2462  C8A0 01FF 2662          LHI R10,511          SET UP FOR 512 BYTE RECORD
2466  40A0 3466 2663          STH R10,NBYTE
246A  41E0 2F0E 2664          BAL R14,RESET          SET BUFFER LIMITS
246E  0788 2665          XHR R8,R8
2470  4840 3464 2666          LH CHAR,MASK          DATA OF RECORD IS
2474  D080 3F88 2667 JUNK1  STM R8,RSAVE1
2478  D1F0 3618 2668 HB2   LM R15,WLIM
247C  0AF8 2669          AHR R15,R8
247E  404F 0000 2670          STH CHAR,0(R15)

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CMT26230
CMT26240
CMT26250
CMT26260
CMT26270
CMT26280
CMT26290
CMT26300
CMT26310
CMT26320
CMT26330
CMT26340
CMT26350
CMT26360
CMT26370
CMT26380
CMT26390
CMT26400
CMT26410
CMT26420
CMT26430
CMT26440
CMT26450
CMT26460
CMT26470
CMT26480
CMT26490
CMT26500
CMT26510
CMT26520
CMT26530
CMT26540
CMT26550
CMT26560
CMT26570
CMT26580
CMT26590
CMT26600
CMT26610
CMT26620
CMT26630
CMT26640
CMT26650
CMT26660
CMT26670
CMT26680
CMT26690
CMT26700
CMT26710
CMT26720
CMT26730
CMT26740
CMT26750

```

## TEST 5 INTER-RECORD GAP TEST

2482	D180	3F88	2671	LM	R8,RSAVE1			
2486	2305		2672	BS	HY3			CMT26760
2488	D180	3F88	2673	HX3	LM	R8,RSAVE1		CMT26770
248C	4048	363A	2674		STH	CHAR,WBUFF(R8)		CMT26780
2490	C180	2474	2675	HY3	BXLE	R8,JUNK1		CMT26790
2494	D350	3465	2676		LB	R5,MASK+1		CMT26800
2498	4050	3466	2677		STH	R5,NBYTE		CMT26810
249C	2491		2678		LIS	R9,1		CMT26820
249E	24A4		2679		LIS	R10,4	SET UP FOR 4 RECORDS	CMT26830
24A0	0788		2680		XHR	R8,R8		CMT26840
24A2	41C0	2C3A	2681	JUNK2	BAL	R12,WRTREC	WRITE 4 LONG RECORDS	CMT26850
24A6	41F0	0F82	2682		BAL	R15,ERRDS		CMT26860
24AA	C180	24A2	2683		BXLE	R8,JUNK2		CMT26870
24AE	41C0	31BE	2684		BAL	R13,WAIT2		CMT26880
24B2	DE60	348B	2685		OC	DEV,WEOF	WRITE EOF MARK	CMT26890
24B6	41E0	33FC	2686		BAL	RET,REWIND	REWIND TAPE	CMT26900
24BA	41E0	2F0E	2687		BAL	R14,RESET	SET BUFFER LIMITS	CMT26910
24BE	41E0	2F38	2688		BAL	R14,BSET	GENERATE WRITE BUFFER	CMT26920
24C2	41E0	2BB8	2689		BAL	R14,FSTEOF	WRITE & SENSE EOF	CMT26930
24C6	41D0	31BE	2690		BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT26940
24CA	DE60	3483	2691		OC	DEV,BKSPAC		CMT26950
24CE	41E0	2FEE	2692		BAL	R14,SENS03	CHECK FOR EOF	CMT26960
24D2	4300	2B96	2693		B	CHKEND1	NO EOF - ABORT T5ST	CMT26970
24D6	C8A0	0064	2694		LHI	R10,100	SET 100 TIMES	CMT26980
24DA	0788		2695		XHR	R8,R8		CMT26990
24DC	41C0	2C3A	2696	CIGCHK	BAL	R12,WRTREC	WRITE A RECORD	CMT27000
24E0	4300	252E	2697		B	WRTER51		CMT27010
24E4	41E0	2BEC	2698	PROC51	BAL	R14,BSPACE	BACKSPACE A RECORD	CMT27020
24E8	C180	24DC	2699		BXLE	R8,CIGCHK		CMT27030
24EC	41E0	2C0A	2700		BAL	R14,SWAP	REVERSE WRITE BUFFER	CMT27040
24F0	41C0	2C3A	2701		BAL	R12,WRTREC	WRITE A RECORD	CMT27050
24F4	4300	254A	2702		B	WRTER52		CMT27060
24F8	41D0	31BE	2703	PROC52	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT27070
24FC	DE60	348B	2704		OC	DEV,WEOF	WRITE EOF	CMT27080
2500	41E0	33FC	2705		BAL	RET,REWIND	REWIND TAPE	CMT27090
2504	41C0	2CF4	2706		BAL	R12,RDREC	READ A RECORD	CMT27100
2508	41F0	0F82	2707		BAL	R15,ERRDS		CMT27110
250C	41E0	2E34	2708		BAL	R14,COMPAR	COMPARE DATA OF LAST RECORD	CMT27120
2510	48A0	1888	2709		LH	R10,IRGDAT+6		CMT27130
2514	0788		2710		XHR	R8,R8		CMT27140
2516	41E0	2BEC	2711	GAPDAT	BAL	R14,BSPACE	BACKSPACE	CMT27150
251A	41C0	2CF4	2712		BAL	R12,RDREC	READ A RECORD	CMT27160
251E	41F0	0F82	2713		BAL	R15,ERRDS		CMT27170
2522	41E0	2E34	2714		BAL	R14,COMPAR		CMT27180
2526	C180	2516	2715		BXLE	R8,GAPDAT	REPEAT OVER SAME RECORD	CMT27190
252A	4300	2B96	2716		B	CHKEND1		CMT27200
			2717	*				CMT27210
			2718	*	ERROR	PROCEDURE		CMT27220
			2719	*				CMT27230
252E	48F0	346A	2720	WRTER51	LH	R15,EOTFLG	EOT?	CMT27240
2532	2338		2721		BZS	WER51		CMT27250
2534	C800	3139	2722	MTNERR	LHI	R0,C'19'	YES - TAPE MOTION ERROR - 19	CMT27260
2538	085F		2723		LHR	R5,R15		CMT27270

## TEST 5 INTER-RECORD GAP TEST

253A	41FO	0F82	2724	BAL	R15,ERRDS		CMT27280
253E	4300	2B9A	2725	B	CHKEND		CMT27290
2542	41FO	0F82	2726	WER51	BAL	R15,ERRDS	CMT27300
2546	4300	24E4	2727	B	PROC51	GO ON	CMT27310
254A	48FO	346A	2728	WRTER52	LH	R15,EOTFLG	CMT27320
254E	4230	2534	2729	BNZ	MTNERR	EOT?	CMT27330
2552	41FO	0F82	2730	BAL	R15,ERRDS		CMT27340
2556	4300	24F8	2731	B	PROC52	GO ON	CMT27350

TEST 6 CYCLIC REDUNDANCY CHECK

```

2733 * *****
2734 *
2735 *           T E S T 6
2736 *
2737 *   PURPOSE:
2738 *   TO CHECK THE CYCLIC REDUNDANCY CHECK (CRC) CHARACTERS
2739 *   GENERATED AT THE END OF EACH RECORD WRITTEN.
2740 *
2741 *   ASSUMPTIONS:
2742 *   TEST 0 HAD BEEN RUN WITHOUT DETECTING ANY FAILURE
2743 *
2744 *   DESIGN SPECIFICATION:
2745 *   IT WAS PRE-CALCULATED THAT THE CRC FOR A RECORD OF
2746 *   00-FF IS X'2929' AND FOR A RECORD OF FF-00 IS X'6A6A'.
2747 *   ALTERNATE RECORDS OF THE ABOVE RECORDS ARE WRITTEN.
2748 *   HARDWARE ADJUSTMENTS SHOULD BE MADE TO ENABLE THE CRC
2749 *   BEING READ. THE RECORDS ARE READ AND THE CRC CHECKED.
2750 *
2751 *   HOW TO RUN THE TEST
2752 *   MAKE SURE THAT THE DEVICE IS A 9 TRACK, 900 BPI
2753 *   MAGNETIC TAPE SYSTEM, WITH THE INTERFACE BOARD ON
2754 *   EXTENSION BOARD. SELECT TEST 6 AND SET CRC OPTICN.
2755 *   WHEN THE FILE IS GENERATED, THE MESSAGE:
2756 *   ADD CRC CAPACITOR AND EXECUTE.
2757 *   WILL BE PRINTED, AND THE PROCESSOR HALTED. REFER
2758 *   TO SECTION 6.2.4 OF PUBLICATION 06-172A15, AND MAKE
2759 *   THE HARDWARE ADJUSTMENT. THE TEST WILL RESUME BY
2760 *   DEPRESSING EXE BUTTON. THE ADDED CAPACITOR MUST BE
2761 *   REMOVED AFTER THE TEST.
2762 *
2763 *   IF OPTION CRC IS NOT SET OR TRACK IS NOT 9 OR DEVICE
2764 *   IS NOT 1 THE TEST WILL ONLY PRINT
2765 *   TEST 06
2766 *   AND RETURN TO INPUT COMMAND MODE WITHOUT FURTHER
2767 *   ACTION
2768 *
2769 *   OPTIONS:
2770 *   TEST, LOOP, CONTIN, NOMSG, DEVADR, DV2ADR, SELCH,
2771 *   INTLEV, MODE, TRACK, RECFIL, DEVICE, CRC, RDCRC
2772 *   WSTART,RSTART
2773 *
2774 *   EPRORS:
2775 *   00, 01, 02, 04, 05, 06 ,10, 11, 12, 13, 14, 15, 48,
2776 *   50.
2777 *
2778 * *****
2779 *
255A 4850 1834 2780 TEST6 LH R5,DEVICE+6          900 BPI MAG. TAPE?
255E 2139      2781      BNZS NOTEST              NO - ABORT TEST
2560 4850 184C 2782      LH R5,TRACK+6
2564 C550 0009 2783      CLHI R5,9              9 TRACK TAPE ?
2568 2134      2784      BNES NOTEST              NO - ABORT TEST
256A 4850 18E8 2785      LH R5,SCRC+6          CRC OPTION SET ?

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## TEST 6 CYCLIC REDUNDUNCY CHECK

256E	2136	2786		BNZS	CRCTST	NO - ABORT TEST	CMT27900
2570	245F	2787	NOTEST	LIS	R5,15		CMT27910
2572	4050 1700	2788		STH	R5,NOERR		CMT27920
2576	4300 0E60	2789		B	TSTEND		CMT27930
257A	C840 2656	2790	CRCTST	LHI	R4,TEST63	STARTING ADDRESS SET UP FOR	CMT27940
257E	41F0 2B7E	2791		BAL	R14,TSTSUP	SECOND DEVICE TEST	CMT27950
2582	C850 0005	2792	TEST61	LHI	R5,X'0005'	CHANGE MASK FOR CRCC=0 TEST	CMT27960
2586	4050 3464	2793		STH	R5,MASK		CMT27970
258A	41E0 2B36	2794		BAL	R14,TSTINIT	TEST INITIALIZE	CMT27980
258E	41E0 2F0E	2795		BAL	R14,RESET	SET BUFFER LIMITS	CMT27990
2592	D0F0 3628	2796		STM	R15,RSV32		CMT28000
2596	D1F0 3624	2797		LM	R15,RLIM+4		CMT28010
259A	26F2	2798		AIS	R15,2		CMT28020
259C	D0F0 3624	2799		STM	R15,RLIM+4		CMT28030
25A0	D1F0 3628	2800		LM	R15,RSV32		CMT28040
25A4	41E0 33FC	2801		BAL	RET,REWIND	REWIND TAPE	CMT28050
25A8	41E0 2BB8	2802		BAL	R14,FSTEOF	WRITE & CHECK EOF	CMT28060
25AC	D010 3F88	2803		STM	R1,RSV32		CMT28070
25B0	2480	2804		LIS	R8,0		CMT28080
25B2	2491	2805		LIS	R9,1		CMT28090
25B4	24A4	2806		LIS	R10,4	5 BYTES PER RECORD	CMT28100
25B6	C850 00D7	2807		LHI	R5,X'00D7'	RECORD WITH ODD NUM BYTES OF	CMT28110
25BA	D1F0 3618	2808	FILLWBUF	LM	R15,WLIM	X'D7' HAS A CRCC = 0	CMT28120
25BE	0AF8	2809		AHR	R15,R8		CMT28130
25C0	D25F 0000	2810		STB	R5,0(R15)		CMT28140
25C4	41F0 126A	2811		BAL	R15,TSTRK	CHECK BREAK KEY	CMT28150
25C8	C180 25BA	2812		BXLE	R8,FILLWBUF		CMT28160
25CC	D110 3F88	2813		LM	R1,RSV32		CMT28170
25D0	C8A0 0001	2814		LHI	R10,1	NUM RECORDS = 1	CMT28180
25D4	2491	2815		LIS	R9,1		CMT28190
25D6	2481	2816		LIS	R8,1		CMT28200
25D8	41C0 2C3A	2817		BAL	R12,WRTREC	WRITE A RECORD	CMT28210
25DC	4300 2776	2818		B	WRTREC		CMT28220
25E0	41D0 31BE	2819		BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT28230
25E4	DE60 348B	2820		OC	DEV,WEOF	WRITE EOF	CMT28240
25E8	41E0 33FC	2821		BAL	RET,REWIND	REWIND TAPE	CMT28250
25EC	DE60 3484	2822		OC	DEV,READ	READ PAST EOF	CMT28260
25F0	41E0 2FE8	2823		BAL	R14,SENS02	EOF?	CMT28270
25F4	4300 2B96	2824		B	CHKEND1	NO - ABORT TEST	CMT28280
25F8	4830 3466	2825		LH	R3,NBYTE	YES	CMT28290
25FC	0788	2826		XHR	R8,R8		CMT28300
25FE	41C0 2CF4	2827		BAL	R12,RDREC	READ A RECORD	CMT28310
2602	4200 0000	2828		NOP			CMT28320
2606	9D65	2829		SSR	DEV,STAT		CMT28330
2608	4210 32A0	2830		BTC	1,MIDU	DU?	CMT28340
260C	C350 0080	2831		THI	STAT,X'80'	ERR BIT SET?	CMT28350
2610	4230 2638	2832		BNZ	CRCZER	YES	CMT28360
2614	41F0 33FC	2833	SECDEV	BAL	RET,REWIND	REWIND TAPE	CMT28370
2618	4850 3474	2834		LH	R5,DEV2	SECOND DEVICE FLAG SET?	CMT28380
261C	4230 2644	2835		BNZ	TEST62	YES - GO TO 2ND PART OF TEST	CMT28390
2620	4060 3478	2836		STH	DEV,DEVONE	SAVE 1ST DEVICE ADDRESS	CMT28400
2624	4860 1810	2837		LH	DEV,DV2ADR+6	GET 2ND DEVICE ADDRESS	CMT28410
2628	4330 2644	2838		BZ	TEST62	ZERO - GOTO 2ND PART OF TEST	CMT28420

## TEST 6 CYCLIC REDUNDANCY CHECK

262C	4060	3474	2839	STH	DEV,DEV2	SET 2ND DEVICE FLAG	CMT28430
2630	4060	16D0	2840	STH	DEV,ERRDEV		CMT28440
2634	4300	2582	2841	B	TEST61	REPEAT CRCC=0 TEST ON 2ND DEV	CMT28450
2638	C800	3531	2842	CRCZER	LHI R0,C'51'	ERROR 51	CMT28460
263C	41F0	0F82	2843	BAL	R15,ERRDS		CMT28470
2640	4300	2614	2844	B	SECDEV		CMT28480
2644	4800	3478	2845	TEST62	LH R0,DEVONE	ARE 2 DEVICES BEING TESTED?	CMT28490
2648	4330	2656	2846	BZ	TEST63	NO	CMT28500
264C	4060	1810	2847	STH	DEV,DV2ADR+6	YES - SAVE 2ND DEV ADDRESS	CMT28510
2650	0860		2848	LHR	DEV,RO	RESTORE 1ST DEV ADDRESS	CMT28520
2652	4060	16D0	2849	STH	DEV,ERRDEV		CMT28530
2656	C850	FFFF	2850	TEST63	LHI R5,X'FFFF'	RESTORE MASK FOR REST OF TEST	CMT28540
265A	4050	3464	2851	STH	R5,MASK		CMT28550
265E	41E0	2B36	2852	BAL	R14,TSTINIT	TEST INITIALIZE	CMT28560
2662	41E0	2F0E	2853	BAL	R14,RESET	SET BUFFER LIMITS	CMT28570
2666	D0F0	3528	2854	STM	R15,RSV32		CMT28580
266A	D1F0	3624	2855	LM	R15,RLIM+4		CMT28590
266E	26F2		2856	AIS	R15,2		CMT28600
2670	D0F0	3624	2857	STM	R15,RLIM+4		CMT28610
2674	D1F0	3628	2858	LM	R15,RSV32		CMT28620
2678	4850	18F4	2859	LH	R5,RDCRC+6	READ CRC ONLY ?	CMT28630
267C	4230	26C2	2860	BNZ	RONLY		CMT28640
2680	41E0	33FC	2861	BAL	RET,REWIND	REWIND TAPE	CMT28650
2684	41E0	2BB8	2862	BAL	R14,FSTEOF	WRITE & CHECK EOF	CMT28660
2688	41E0	2F38	2863	BAL	R14,BSET	SET WRITE BUFFER	CMT28670
268C	48A0	1858	2864	LH	R10,RECFIL+6	SET NUMBER OF RECORDS	CMT28680
2690	2491		2865	LIS	R9,1		CMT28690
2692	2481		2866	LIS	R8,1		CMT28700
2694	41C0	2C3A	2867	GENFIL6	BAL R12,WRTREC	WRITE A RECPD	CMT28710
2698	4300	2776	2868	B	WRTER6		CMT28720
269C	0755		2869	XHR	R5,R5		CMT28730
269E	4050	3470	2870	STH	R5,RTYCNT		CMT28740
26A2	41F0	2COA	2871	PROC61	BAL R14,SWAP	REVERSE WRITE BUFFER	CMT28750
26A6	C180	2694	2872	BXLE	R8,GENFIL6		CMT28760
26AA	41D0	318E	2873	ENDFIL	BAL R13,WAIT2	WAIT FOR NMTN =1	CMT28770
26AE	DE60	348B	2874	OC	DEV,WEOF	WRITE EOF	CMT28780
26B2	C850	3512	2875	LHI	R5,MSG05	PRINT MESSAGE TO ADD	CMT28790
26B6	41F0	112A	2876	BAL	R15,PRINT	CAPACITOR ON CONTROLLER	CMT28800
26BA	C850	080F	2877	LHI	R5,X'080F'		CMT28810
26BE	9154		2878	SLHLS	R5,4		CMT28820
26C0	9505		2879	EPSR	R0,R5	HALT PROCESSOR	CMT28830
26C2	41E0	33FC	2880	RCONLY	BAL RET,REWIND	REWIND TAPE	CMT28840
26C6	DE60	3486	2881	OC	DEV,SKIPP	READ PAST EOF	CMT28850
26CA	41E0	2FE8	2882	BAL	R14,SENS02		CMT28860
26CE	4300	2B96	2883	B	CHKEND1	NO EOF - ABORT TEST	CMT28870
26D2	4830	3466	2884	LH	R3,NBYTE		CMT28880
26D6	0788		2885	XHR	R8,R8		CMT28890
26D8	41C0	2CF4	2886	RDFIL6	BAL R12,RDREC	READ A RECORD	CMT28900
26DC	4300	2798	2887	B	RDER6		CMT28910
26E0	D080	3F88	2888	PROC62	STM R8,RSV1		CMT28920
26E4	D1F0	3620	2889	HB1	LM R15,RLIM		CMT28930
26E8	0AF3		2890	AHR	R15,R3		CMT28940
26EA	26F1		2891	AIS	R15,1		CMT28950





TEST 6 CYCLIC REDUNDUNCY CHECK

279E 4230 2738  
27A2 41F0 0F82  
27A6 4300 25E0

2945  
2946  
2947

BNZ ENDIST6  
BAL R15,ERRDS  
B PROC62

YES - END OF FILE

CMT29490  
CMT29500  
CMT29510

## TEST 7 UTILITY TEST

```

2949 * *****
2950 *
2951 *           T E S T 7
2952 *
2953 *   PURPOSE:
2954 *   A UTILITY TEST TO ALLOW USER TO TEST THE DEVICE
2955 *   IN HIS OWN CHOSEN METHOD. OPTIONS ARE PROVIDED
2956 *   TO SELECT THE INDIVIDUAL FUNCTIONS AS SPECIFIED
2957 *   IN APPENDIX 6 OF PUBLICATION 06-172A15. A SCOPE
2958 *   LOOP OPTION IS ALSO PROVIDED.
2959 *
2960 *   THE RECORD SIZE IN THIS TEST CAN BE VARIED BY THE
2961 *   OPTION BYTES. THE LIMITS ARE FROM 2 TO X'400'. IF
2962 *   THE USER WISHES TO INCREASE THE UPPER LIMIT, HE MAY
2963 *   DO SO BY INCREASING THE CONTENT OF LOCATION LABELLED
2964 *   "X400". IT MUST BE NOTED THAT THE LOWER LIMIT
2965 *   CANNOT BE LESS THAN 2 AND THE HIGHER LIMIT MUST
2966 *   NOT BE CHANGED TO A VALUE HIGHER THAN X'7FFF'.
2967 *
2968 *   ASSUMPTIONS:
2969 *   SAME AS IN TEST 0.
2970 *
2971 *   DESIGN SPECIFICATION:
2972 *   SEVERAL OPTIONS ARE PROVIDED TO THE USER TO SELECT
2973 *   THE DESIRED FUNCTIONS. THE SCOPE LOOP FUNCTIONS
2974 *   SUPERCEDE ALL OTHER FUNCTIONS. IF SCOPE=0, THEN
2975 *   READ ONLY HAS HIGHEST PRIORITY, FOLLOWED BY WRITE
2976 *   EOF CONTINUOUS. SCOPE LOOP IS EXECUTED CONTINUOUSLY
2977 *   WITHOUT ANY ERROR CHECKING. SCOPE 1, 2 & 3 INVOLVES
2978 *   WRITE OPERATION, AND ENSURES PROPER TERMINATION
2979 *   BY WRITING AN EOF. ALL SCOPES CAN BE STOPPED BY
2980 *   BREAK OR DU. SCOPE 5 WILL SKIP FORWARD UNTIL EOT
2981 *   AND THEN SKIP REVERSE TILL BOT. THIS WILL CONTINUE
2982 *   UNTIL STOPPED BY THE USER.
2983 *   WHEN SCOPE=0 THE DEFAULT OPTIONS WILL GENERATE A
2984 *   FILE. BACKSPACE OVER IT AND READ IT. THE BUFFERS
2985 *   ARE COMPARED. IF BACKSPACE IS NOT SPECIFIED, A SKIP
2986 *   FILE REVERSE IS PERFORMED BEFORE READING. MORE THAN
2987 *   ONE FILES CAN BE SPECIFIED BY OPTION FILES.
2988 *   THE WEOF CONTINUOUS OPERATION IS PERFORMED IN THIS
2989 *   TEST WITH NO ERROR CHECKING.
2990 *
2991 *   SEVERAL SIMPLE SUBROUTINES ARE IMPLEMENTED TO
2992 *   PERFORM DIFFERENT TAPE FUNCTIONS. NO ERROR CHECK
2993 *   IS DONE. THIS ALLOWS THE USER TO WRITE SHORT
2994 *   UTILITY PROGRAMS:
2995 *   BAL  R14,EOF           WRITE EOF MARK
2996 *   BAL  R14,RWND         REWIND TAPE
2997 *   BAL  R14,SKFW        SKIP EOF FORWARD
2998 *   BAL  R14,SKRV        SKIP EOF REVERSE
2999 *   BAL  R14,BKSP        BACKSPACE RECORD
3000 *   BAL  R14,WRTBLK      WRITE RECORD BLOCK MODE
3001 *   BAL  R14,RDBLK      READ RECORD BLOCK MODE

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CMT29530
CMT29540
CMT29550
CMT29560
CMT29570
CMT29580
CMT29590
CMT29600
CMT29610
CMT29620
CMT29630
CMT29640
CMT29650
CMT29660
CMT29670
CMT29680
CMT29690
CMT29700
CMT29710
CMT29720
CMT29730
CMT29740
CMT29750
CMT29760
CMT29770
CMT29780
CMT29790
CMT29800
CMT29810
CMT29820
CMT29830
CMT29840
CMT29850
CMT29860
CMT29870
CMT29880
CMT29890
CMT29900
CMT29910
CMT29920
CMT29930
CMT29940
CMT29950
CMT29960
CMT29970
CMT29980
CMT29990
CMT30000
CMT30010
CMT30020
CMT30030
CMT30040
CMT30050

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## TEST 7 UTILITY TEST

		3002	*	BAL	R14,RWSEL	READ OR WRITE REC SELCH MODE	*	CMT30060
		3003	*	NOTE:	ALL READ/WRITE RECORD ROUTINES ASSUME THAT		*	CMT30070
		3004	*		R11 CONTAINS THE STARTING ADDRESS, AND R12		*	CMT30080
		3005	*		CONTAINS THE ENDING ADDRESS OF THE RECORD.		*	CMT30090
		3006	*		ALSO, RWSEL ASSUMES THAT R2 CONTAINS THE		*	CMT30100
		3007	*		DEVICE COMMAND AND R3 CONTAINS THE SELCH		*	CMT30110
		3008	*		GO AND COMMAND.		*	CMT30120
		3009	*				*	CMT30130
		3010	*	HOW TO RUN THE TEST:			*	CMT30140
		3011	*	REFER TO TEST 0. SELECT THE APPROPRIATE OPTION			*	CMT30150
		3012	*	AND RUN TEST 7.			*	CMT30160
		3013	*				*	CMT30170
		3014	*	OPTIONS:			*	CMT30180
		3015	*	TEST, LOOP, CONTIN, NOMSG, DEVADR, DV2ADR, SELCH,			*	CMT30190
		3016	*	INTLEV, MODE, TRACK, RECFIL, FILES, WRITE, READ,			*	CMT30200
		3017	*	BKSPAC, WEOF, BYTES, SCOPE.			*	CMT30210
		3018	*	WSTART,RSTART			*	CMT30220
		3019	*				*	CMT30230
		3020	*	ERRORS:			*	CMT30240
		3021	*	00, 01, 02, 04, 05, 08, 10, 11, 12, 13, 14, 15, 46,			*	CMT30250
		3022	*	47, 50.			*	CMT30260
		3023	*				*	CMT30270
		3024	*	*****			*	CMT30280
		3025	*				*	CMT30290
27AA	C840	27B2		3026	TEST7	LHI	R4,TEST71	STARTING ADDRESS SET UP FOR
27AE	41E0	2B7E		3027		BAL	R14,TSTSUP	SECOND DEVICE TEST
27B2	41E0	2B36		3028	TEST71	BAL	R14,TSTINIT	TEST INITIALIZE
27B6	41C0	324C		3029		BAL	R13,WAIT1	WAIT FOR NMTN=1
27BA	48A0	1864		3030		LH	R10,NBYTE+6	GET NO. BYTES PER RECORD
27BE	27A1			3031		SIS	R10,1	REDUCE BY 1
27C0	40A0	3466		3032		STH	R10,NBYTE	
27C4	2421			3033		LIS	R2,1	
27C6	4830	1858		3034		LH	R3,RECFIL+6	GET RECORD PER FILE
27CA	41E0	2F0E		3035		BAL	R14,RESET	RESET BUFFER LIMITS
27CE	48E0	3468		3036		LH	R14,DE	
27D2	C5E0	000F		3037		CLHI	R14,X'F'	
27D6	2333			3038		BES	NXTMOD7	
27D8	41E0	2F38		3039		BAL	R14,BSET	SET WRITE BUFFER 00-FF
27DC	41E0	33FC		3040	NXTMOD7	PAL	RET,REWIND	REWIND TAPE
27E0	4850	1918		3041		LH	R5,SCOPE+6	SCOPE LOOP?
27E4	4230	28FA		3042		BNZ	SCLJOP	YES - GO TO SCOPE LOOP
27E8	0788			3043		XHR	R8,R8	NO - RESET FILE COUNTER
27EA	4850	18AC		3044		LH	R5,OPWRT+6	WRITE OPTION?
27EE	2135			3045		BNZS	CHKEOF	YES - CHECK WEOF OPTION
27F0	4850	18A0		3046		LH	R5,OPRD+6	NO - READ OPTION?
27F4	4230	284C		3047		BNZ	RONLY7	YES - READ ONLY
27F8	4850	18D0		3048	CHKEOF	LH	R5,OPWEOF+5	WRITE EOF TO SUPERCEDE WRITE?
27FC	4230	2A70		3049		BNZ	CONEOF	YES - WRITE EOF CONTINUOUSLY
2800	41E0	307E		3050		BAL	R14,INDATA	NO - ACQUIRE DATA STRING
2804	41E0	31BE		3051		BAL	R13,WAIT2	WAIT FOR NMTN=1
2808	41E0	2BB8		3052		BAL	R14,FSTEOF	WRITE & CHECK EOF
280C	2411			3053	WRTFIL	LIS	R1,1	
280E	41C0	2C3A		3054	GENFIL7	BAL	R12,WRTREC	WRITE A RECORD

## TEST 7 UTILITY TEST

2812	4300	28BA	3055	B	WRTER71		CMT3059C
2816	C110	280E	3056	WCON7	BXLE	R1,GENFIL7	CMT30600
281A	41D0	31BE	3057		BAL	R13,WAIT2	CMT30610
281E	DE60	348B	3058		OC	DEV,WE0F	CMT30620
2822	4850	1888	3059		LH	R5,JPBSP+6	CMT30630
2826	4330	289E	3060		BZ	NOBSP7	CMT30640
282A	41D0	31BE	3061		BAL	R13,WAIT2	CMT30650
282E	DE60	3483	3062		OC	DEV,BKSPAC	CMT30660
2832	2411		3063		LIS	R1,1	CMT30670
2834	41E0	2BEC	3064	BSFIL7	BAL	R14,BSPACE	CMT30680
2838	C110	2834	3065		BXLE	R1,BSFIL7	CMT30690
283C	4850	19A0	3066		LH	R5,OPRD+6	CMT30700
2840	4330	287E	3067		BZ	ENDFIL7	CMT30710
2844	41D0	31BE	3068		BAL	R13,WAIT2	CMT30720
2848	DE60	3483	3069		OC	DEV,BKSPAC	CMT30730
284C	41D0	31BE	3070	RONLY7	BAL	R13,WAIT2	CMT30740
2850	DE60	3484	3071		OC	DEV,READ	CMT30750
2854	2411		3072	RDFIL7	LIS	R1,1	CMT30760
2856	41C0	2CF4	3073	RERDR7	BAL	R12,RDREC	CMT30770
285A	4300	28DC	3074		B	RDER71	CMT30780
285E	4850	18DC	3075		LH	R5,CMPRE+6	CMT30790
2862	2333		3076		BZS	NOCOM	CMT30800
2864	41E0	2F34	3077		BAL	R14,COMPAR	CMT30810
2868	4850	1900	3078	NOCOM	LH	R5,SDUMP+6	CMT30820
286C	2333		3079		BZS	RDON	CMT30830
286E	41E0	2F90	3080		BAL	R14,DUMP	CMT30840
2872	C110	2856	3081	RDON	BXLE	R1,RERDR7	CMT30850
2876	41D0	31BE	3082		BAL	R13,WAIT2	CMT30860
287A	DF60	3484	3083		OC	DEV,READ	CMT30870
287E	2681		3084	ENDFIL7	AIS	R8,1	CMT30880
2880	4580	1870	3085		CLH	R8,FILES+6	CMT30890
2884	2387		3086		BNLS	END7	CMT30900
2886	4850	18AC	3087		LH	R5,OPWRT+6	CMT30910
288A	4230	280C	3088		BNZ	WRTFIL	CMT30920
288E	4300	2854	3089		B	RDFIL7	CMT30930
2892	41E0	33FC	3090	END7	BAL	RET,REWIND	CMT30940
2896	41D0	3026	3091		BAL	R13,TSTMOD	CMT30950
289A	4300	27DC	3092		B	NXTMOD7	CMT30960
289E	4850	18A0	3093	NOBSP7	LH	R5,OPRD+6	CMT30970
28A2	4330	287E	3094		BZ	ENDFIL7	CMT30980
28A6	41D0	31BE	3095		BAL	R13,WAIT2	CMT30990
28AA	DE60	3487	3096		OC	DEV,SKIPR	CMT31000
28AE	41D0	31BE	3097		BAL	R13,WAIT2	CMT31010
28B2	DF60	3487	3098		OC	DEV,SKIPR	CMT31020
28B6	4300	284C	3099		B	RONLY7	CMT31030
			3100	*			CMT31040
			3101	*	ERROR	PROCEDURE	CMT31050
			3102	*			CMT31060
28BA	48E0	346A	3103	WRTER71	LH	R14,EOTFLG	CMT31070
28BE	2135		3104		BNZS	WEOT7	CMT31080
28C0	41E0	300E	3105		BAL	R14,ERRMSG2	CMT31090
28C4	4300	281E	3106		B	WCON7	CMT31100
28C8	41D0	31BE	3107	WEOT7	BAL	R13,WAIT2	CMT31110

## TEST 7 UTILITY TEST

28CC	DE60 3483	3108	OC	DEV,BKSPAC	BACKSPACE A RECORD	CMT31120
28D0	41E0 318E	3109	BAL	R13,WAIT2		CMT31130
28D4	DE60 348B	3110	OC	DEV,WEOF	WRITE EOF	CMT31140
28D8	4300 2892	3111	B	END7		CMT31150
28DC	9D65	3112	RDER71	SSR	DEV,STAT	CMT31160
28DE	C350 0040	3113	THI	STAT,X'40'		CMT31170
28E2	4230 287E	3114	BNZ	ENDFIL7		CMT31180
28E6	C350 0020	3115	THI	STAT,X'20'	EOT?	CMT31190
28EA	4230 2892	3116	BNZ	END7		CMT31200
28EE	41E0 300E	3117	BAL	R14,ERRMSG2		CMT31210
28F2	4300 2868	3118	B	NOCOM		CMT31220
28F6	4300 1498	3119	SLCHINT	B	RETOPSW	CMT31230
		3120	*			CMT31240
		3121	*	SCOPE LOOPS: NO ERROR CHECK		CMT31250
		3122	*			CMT31260
28FA	C550 0005	3123	SCLOOP	CLHI	R5,5	CMT31270
28FE	4330 2A92	3124		BE	SKPCOM	CMT31280
2902	C550 0004	3125		CLHI	R5,4	CMT31290
2906	4380 29E8	3126		BNL	RDCON	CMT31300
290A	41E0 307E	3127		BAL	R14,INDATA	YES - GET DATA PATTERN
290E	4850 1918	3128		LH	R5,SCOPE+6	CMT31310
2912	2751	3129		SIS	R5,1	CMT31320
2914	0A55	3130		AHR	R5,R5	CMT31330
2916	4800 346E	3131		LH	R0,MODFLG	CMT31340
291A	C500 0001	3132		CLHI	R0,1	BLOCK MODE?
291E	2332	3133		BES	BLKMOD	CMT31360
2920	2651	3134		AIS	R5,1	CMT31370
2922	D3A5 3486	3135	BLKMOD	LB	R10,SQMASK(R5)	CMT31380
2926	C840 2AFC	3136		LHI	R4,LOOPBRK	CMT31390
292A	4040 16FA	3137		STH	R4,KBINT	CMT31400
292E	41F0 132E	3138		BAL	R15,KB RD	SET KEYBOARD INTERRUPT
2932	C850 28F6	3139		LHI	R5,SLCHINT	CMT31420
2936	4050 196A	3140		STH	R5,DEVINT	CMT31430
293A	4840 0A22	3141		LH	R4,PSW	ENABLE PSW INTERRUPT
293E	9554	3142		EPSR	R5,R4	CMT31450
2940	41E0 32C2	3143		BAL	R14,EOF	CMT31460
2944	088A	3144	ADVANCE	LHR	R8,R10	CMT3147C
		3145	*		THIS ROUTINE WRITES A FILE WITH LEADING EOF. IF EOT	CMT31480
		3146	*		IS DETECTED, IT REWINDS TAPE AND WRITES WHOLE FILE	CMT31490
		3147	*		AGAIN. ROUTINE WFILB USES THE WB MODE AND ROUTINE	CMT31500
		3148	*		WFILS USES SELCH MODE	CMT31510
2946	D0F0 3628	3149		STM	R15,RSV32	CMT31520
294A	D1F0 3618	3150		LM	R15,WLIM	CMT31530
294E	08BF	3151		LHR	R11,R15	CMT31540
2950	D1F0 361C	3152		LM	R15,WLIM+4	CMT31550
2954	08CF	3153		LHR	R12,R15	CMT31560
2956	D1F0 3628	3154		LM	R15,RSV32	CMT31570
295A	9081	3155	WFILB	SRLS	R8,1	SHIFT SEQUENCE MASK
295C	4380 2986	3156		BNC	WFILS	NO CARRY - RYPASS
2960	41E0 32E4	3157		BAL	R14,WRTBLK	WRITE A RECORD (BLOCK MODE)
2964	9D65	3158		SSR	DEV,STAT	CMT3161C
2966	2221	3159		BFBS	2,1	CMT31620
2968	C350 0020	3160		THI	STAT,X'20'	EOT?
						CMT31630
						CMT31640

## TEST 7 UTILITY TEST

296C	233D	3161	BZS	WFILS	NO - GO ON	CMT31650
296E	41E0 32FC	3162	EOT7	BAL R14,BKSP	YES - BACKSPACE THE LAST RECORD	CMT31660
2972	41E0 32C2	3163		BAL R14,EOF	WRITE EOF	CMT31670
2976	41E0 32CC	3164	PREOT	BAL R14,RWND	REWIND	CMT31680
297A	C850 3506	3165		LHI R5,MSG04	EXIT TEST	CMT31690
297E	41E0 112A	3166		BAL R15,PRINT		CMT31700
2982	4300 2B9A	3167		B	CHKEND	CMT31710
2986	9081	3168	WFILS	SRLS R8,1	SHIFT SEQUENCE MASK	CMT31720
2988	4380 29AC	3169		BNC BSFIL	NO CARRY - BYPASS	CMT31730
298C	D320 3485	3170		LB R2,WRITE	DEVICE WRITE COMMAND	CMT31740
2990	D330 3480	3171		LB R3,GOWRT	SELCH WRITE COMMAND	CMT31750
2994	4810 0A24	3172		LH R1,PSW2	DISABLE INTERRUPTS AT	CMT31760
2998	9541	3173		EPSR R4,R1	PROCESSOR LEVEL	CMT31770
299A	41E0 3306	3174		BAL R14,RWSEL	WRITE A RECORD (SELCH MODE)	CMT31780
299E	9514	3175		EPSR R1,R4	RESTORE PSW	CMT31790
29A0	9D65	3176		SSR DEV,STAT		CMT31800
29A2	2221	3177		BFBS 2,1		CMT31810
29A4	C350 0020	3178		THI STAT,X'20'	EOT?	CMT31820
29A8	4230 296E	3179		BNZ EOT7		CMT31830
		3180	*	THIS ROUTINE BACKSPACE A FILE BEYOND ITS LEADING		CMT31840
		3181	*	EOF MARK		CMT31850
29AC	9081	3182	BSFIL	SRLS R8,1	SHIFT SEQUENCE MASK	CMT31860
29AE	4380 2944	3183		BNC ADVANCE	NO CARRY - BYPASS	CMT31870
29B2	41E0 32FC	3184		BAL R14,BKSP	BACKSPACE A RECORD	CMT31880
		3185	*	THIS ROUTINE READS A FILE WITH LEADING EOF. IF EOT		CMT31890
		3186	*	IS DETECTED, IT REWINDS AND READS AGAIN		CMT31900
		3187	*	ROUTINE RFILB USES RB MODE AND RFILS USES SELCH MODE		CMT31910
		3188	*			CMT31920
29B6	DOF0 3628	3189	RFILB	STM R15,RSV32		CMT31930
29BA	D1F0 3620	3190		LM R15,RLIM		CMT31940
29BE	08BF	3191		LHR R11,R15		CMT31950
29C0	D1F0 3624	3192		LM R15,RLIM+4		CMT31960
29C4	08CF	3193		LHR R12,R15		CMT31970
29C6	D1F0 3628	3194		LM R15,RSV32		CMT31980
29CA	9081	3195		SRLS R8,1	SHIFT SEQUENCE MASK	CMT31990
29CC	2383	3196		BNCS RFILS	NO CARRY - BYPASS	CMT32000
29CE	41E0 32F0	3197		BAL R14,RDBLK	READ A RECORD (BLOCK MODE)	CMT32010
29D2	9081	3198	RFILS	SRLS R8,1	SHIFT SEQUENCE MASK	CMT32020
29D4	4380 2944	3199		BNC ADVANCE	NO CARRY - RESTART CYCLE	CMT32030
29D8	D320 3484	3200		LB R2,READ	DEVICE READ COMMAND	CMT32040
29DC	D330 3481	3201		LB R3,GORD	SELCH READ COMMAND	CMT32050
29E0	41E0 3306	3202		BAL R14,RWSEL	READ A RECORD (SELCH MODE)	CMT32060
29E4	4300 2944	3203		B	ADVANCE	CMT32070
		3204	*			CMT32080
		3205	*	READ ONLY SCOPE LOOP		CMT32090
		3206	*	THIS ROUTINE READS RECORDS ON THE TAPE UNTIL AN		CMT32100
		3207	*	EOF IS DETECTED. THEN THE TEST WILL PAUSE WITH THE		CMT32110
		3208	*	MESSAGE "EOF". IF CR IS ENTERED ON KEYBOARD, THE		CMT32120
		3209	*	TEST IS ABORTED. IF LF IS ENTERED, THE TEST READS		CMT32130
		3210	*	ON TO THE NEXT EOF. IF EOT IS DETECTED, THE TEST		CMT32140
		3211	*	IS ABORTED.		CMT32150
		3212	*			CMT32160
29E8	DOF0 3628	3213	RDCOM	STM R15,RSV32		CMT32170



## TEST 7 UTILITY TEST

2A96	DE60	3484	3267	OC	DEV,READ	READ PASS FIRST EOF	CMT32710
2A9A	41E0	33DE	3268	SKPCON1	BAL R14,SKFW	SKIP FORWARD	CMT3272C
2A9E	9D65		3269	SSR	DEV,STAT		CMT3273C
2AA0	4210	32A0	3270	BTC	1,MTDU		CMT3274C
2AA4	41F0	126A	3271	BAL	R15,TSTBRK		CMT3275C
2AA8	C350	0022	3272	THI	STAT,X'22'	EOM OR EOT?	CMT3276C
2AAC	2237		3273	BZS	SKPCON1+4		CMT3277C
2AAE	C350	0020	3274	THI	STAT,X'20'	EOT?	CMT3278C
2AB2	223C		3275	BZS	SKPCON1		CMT3279C
2AB4	DE60	347F	3276	OC	DEV,CLEAR	YES - CLEAR DEVICE	CMT3280C
2AB8	41E0	33E8	3277	REVRS	BAL R14,SKRV	SKIP REVERSE	CMT3281C
2ABC	41D0	32D6	3278	BAL	R13,SENMTN	WAIT FOR NMTN=1	CMT3282C
2AC0	9D65		3279	SSR	DEV,STAT		CMT3283C
2AC2	4210	32A0	3280	BTC	1,MTDU		CMT3284C
2AC6	C350	0020	3281	THI	STAT,X'20'	EOT?	CMT3285C
2ACA	2239		3282	BZS	REVRS	NO - SKIP REVERSE AGAIN	CMT3286C
2ACC	DE60	347F	3283	OC	DEV,CLEAR	YES - CLEAR DEVICE	CMT3287C
2AD0	9D65		3284	SSR	DEV,STAT		CMT3288C
2AD2	C350	0020	3285	THI	STAT,X'20'	BOT?	CMT3289C
2AD6	4230	2A92	3286	BNZ	SKPCON	YES - GO SKIP FORWARD	CMT3290C
2ADA	41E0	33E8	3287	REVRS1	BAL R14,SKRV	CONTINUE SKIP REVERSE	CMT3291C
2ADE	9D65		3288	SSR	DEV,STAT		CMT3292C
2AE0	4210	32A0	3289	BTC	1,MTDU		CMT3293C
2AE4	41F0	126A	3290	BAL	R15,TSTBRK		CMT3294C
2AE8	C350	0022	3291	THI	STAT,X'22'	EOM OR BOT?	CMT3295C
2AEC	2237		3292	BZS	REVRS1+4		CMT3296C
2AEE	C350	0020	3293	THI	STAT,X'20'	BOT?	CMT3297C
2AF2	223C		3294	BZS	REVRS1		CMT3298C
2AF4	DE60	347F	3295	OC	DEV,CLEAR		CMT3299C
2AF8	4300	2A92	3296	B	SKPCON	GO SKIP FORWARD	CMT3300C
			3297	*			CMT3301C
			3298	*	THIS SECTION CHECKS IF THE KEYBOARD CHARACTER IS		CMT3302C
			3299	*	BREAK.		CMT3303C
			3300	*			CMT3304C
2AFC	9B24		3301	LOOPBRK	RDR R2,R4	GET THE CHARACTER	CMT3305C
2AFE	C440	007F	3302		WHI R4,X'7F'		CMT3306C
2B02	4230	1498	3303		BNZ RETOPSW	NO - CONTINUE LOOP	CMT3307C
2B06	C840	1492	3304		LHI R4,NOBRK	YES - RESTORE BRK CHECK ROUTINE	CMT3308C
2B0A	4040	16FA	3305		STH R4,KBINT	IN ETPE	CMT3309C
2B0E	C820	00F0	3306		LHI R2,X'F0'	RESTORE REG. SET	CMT3310C
2B12	9512		3307		EPSR R1,R2		CMT3311C
2B14	4850	346E	3308		LH R5,MODFLG		CMT3312C
2B18	C550	0002	3309		CLHI R5,2	MODE 2?	CMT3313C
2B1C	2135		3310		BNES CLRDEV		CMT3314C
2B1E	9D75		3311		SSR SELCH,STAT		CMT3315C
2B20	2081		3312		BTBS 8,1		CMT3316C
2B22	DE70	347E	3313		OC SELCH,STOP	STOP SELCH	CMT3317C
2B26	DE60	347F	3314	CLRDEV	OC DEV,CLEAR	CLEAR DEVICE	CMT3318C
2B2A	41E0	32C2	3315		BAL R14,EOF	WRITE EOF	CMT3319C
2B2E	41E0	32CC	3316		BAL R14,RWND	REWIND	CMT3320C
2B32	4300	0A86	3317		B OPTIN		CMT3321C





## SUBROUTINES

		3372	*						CMT33760
2B95	41F0	33FC		3373	CHKEND1	BAL	RET,REWIND	REWIND TAPE	CMT33770
2B9A	4850	3474		3374	CHKEND	LH	R5,DEV2	SECOND DEVICE FLAG SET?	CMT33780
2B9E	4230	0E60		3375		BNZ	TSTEND	YES - END TEST	CMT33790
2RA2	4860	1810		3376		LH	DEV,DV2ADR+6	GET 2ND DEVICE ADDRESS	CMT33800
2BA6	4330	0E60		3377		BZ	TSTEND	ZERO - END TEST	CMT33810
2BAA	48F0	3472		3378		LH	R15,NXTDEV	TEST AGAIN	CMT33820
29AE	4060	3474		3379		STH	DEV,DEV2	SET 2ND DEVICE FLAG	CMT33830
2BB2	4060	16D0		3380		STH	DEV,ERRDEV		CMT33840
2BB5	030F			3381		BR	R15		CMT33850
				3382	*				CMT33860
				3383	*	*****			CMT33870
				3384	*				CMT33880
				3385	*	SUBROUTINE FSTEOF			CMT33890
				3386	*		THIS ROUTINE WRITES AN EOF AND CHECKS IT. IF NMTN		CMT33900
				3387	*		DOES NOT DROP WITHIN ONE INSTRUCTION TIME AFTER THE		CMT33910
				3388	*		OUTPUT COMMAND, ERROR 50 IS LOGGED AND THE TEST		CMT33920
				3389	*		ABORTED ASSUMING THAT THE TAPE DRIVE IS IN THE WRITE		CMT33930
				3390	*		PROTECT MODE.		CMT33940
				3391	*		IF NO EOF IS DETECTED AFTER A TIMED WAITING PERIOD,		CMT33950
				3392	*		THIS TEST IS ABORTED.		CMT33960
				3393	*		THIS ROUTINE IS USUALLY CALLED AFTER A REWIND, AND		CMT33970
				3394	*		IT RESETS THE EOF FLAG.		CMT33980
				3395	*		CALLING SEQUENCE:		CMT33990
				3396	*	BAL	R14,FSTEOF		CMT34000
				3397	*		ERROR: 50		CMT34010
				3398	*	*****			CMT34020
				3399	*				CMT34030
2BB8	40F0	3E88		3400	FSTEOF	STH	R14,SAVERTN	SAVE RETURN ADDRESS	CMT34040
2BBC	0755			3401		XHR	R5,R5	RESET EOF FLAG	CMT34050
2BBE	4050	346A		3402		STH	R5,EOFELG		CMT34060
2BC2	DE60	348B		3403		OC	DEV,WEOF	WRITE EOF	CMT34070
2BC6	4200	0000		3404		NOP		WAIT FOR NMTN=0	CMT34080
2BCA	9D65			3405		SSR	DEV,STAT		CMT34090
2BCC	C350	0010		3406		THI	STAT,X'10'	NMTN=0?	CMT34100
2BD0	2138			3407		BNZS	WRTPT	NO - WRITE PROTECT ERROR	CMT34110
2BD2	41E0	2FE2		3408		BAL	R14,SENS01	CHECK EOF	CMT34120
2BD6	4300	2896		3409		B	CHKEND1	NO EOF - ABORT TEST	CMT34130
2BDA	48E0	3E88		3410		LH	R14,SAVERTN		CMT34140
2BDE	030E			3411		BR	R14		CMT34150
2BE0	C800	3530		3412	WRTPT	LHI	RO,C'50'	ERROR 50	CMT34160
2BE4	41F0	0F82		3413		BAL	R15,ERRDS		CMT34170
2BE8	4300	0AE6		3414		B	OPTIN		CMT34180
				3415	*				CMT34190
				3416	*	*****			CMT34200
				3417	*	SUBROUTINE BSPACE			CMT34210
				3418	*		THIS ROUTINE BACKSPACES A RECORD. IF ERROR STATUS		CMT34220
				3419	*		IS SENSED, AN ERROR MESSAGE IS PRINTED.		CMT34230
				3420	*		RETURNS ON R14		CMT34240
				3421	*		ERROR: 08		CMT34250
				3422	*	*****			CMT34260
				3423	*				CMT34270
2BEC	41D0	31BE		3424	BSPACE	BAL	R13,WAIT2	WAIT FOR NMTN=1	CMT34280

## SUBROUTINES

2BF0	DE60 3483	3425	OC	DEV,BKSPAC	BACKSPACE	CMT34290
2BF4	41D0 320C	3426	BAL	R13,WAIT3	WAIT FOR EOM=1	CMT34300
2BF8	9D65	3427	SSR	DEV,STAT		CMT34310
2BFA	C350 00C0	3428	THI	STAT,X'CO'	ERR OR EOF SET?	CMT34320
2BFE	033E	3429	BZR	R14	NO - RETURN	CMT34330
2C00	C800 3038	3430	LHI	RO,C'08'	STATUS ERROR - 08	CMT34340
2C04	41F0 0F82	3431	BAL	R15,ERRDS		CMT34350
2C08	030E	3432	BR	R14		CMT34360
		3433	*	*****		CMT34370
		3434	*	SUBROUTINE SWAP		CMT34380
		3435	*	THIS ROUTINE REVERSES THE WRITE BUFFER		CMT34390
		3436	*	CALLING SEQUENCE:		CMT34400
		3437	*	BAL R14,SWAP		CMT34410
		3438	*	*****		CMT34420
		3439	*			CMT34430
2C0A	07BB	3440	SWAP	XHR R11,R11		CMT34440
2C0C	48C0 3466	3441	LH	R12,NBYTE		CMT34450
2C10	D0F0 3628	3442	STM	R15,RSV32		CMT34460
2C14	D1F0 3618	3443	LM	R15,WLIM		CMT34470
2C18	0ABF	3444	AHR	R11,R15		CMT34480
2C1A	0ACF	3445	AHR	R12,R15		CMT34490
2C1C	D1F0 3628	3446	LM	R15,RSV32		CMT34500
2C20	D34B 0000	3447	SWP1	LB CHAR,0(R11)		CMT34510
2C24	D35C 0000	3448	LB	STAT,0(R12)		CMT34520
2C28	D24C 0000	3449	STB	CHAR,0(R12)		CMT34530
2C2C	D25B 0000	3450	STB	STAT,0(R11)		CMT34540
2C30	2681	3451	AIS	R11,1	INCREASE LOWER END POINTER	CMT34550
2C32	27C1	3452	SIS	R12,1	DECREASE UPPER END POINTER	CMT34560
2C34	05EC	3453	CLHR	R11,R12	POINTERS MEET OR CROSS?	CMT34570
2C36	208B	3454	BLS	SWP1	NO - CONTINUE	CMT34580
2C38	030E	3455	BR	R14	YES - EXIT	CMT34590
		3456	*			CMT34600
		3457	*	*****		CMT34610
		3458	*	SUBROUTINE WRTREC		CMT34620
		3459	*	THIS ROUTINE WRITES A RECORD ONTO THE MAG. TAPE		CMT34630
		3460	*	IT OPERATES EITHER ON SELCH MODE OR PR/WB MODE.		CMT34640
		3461	*	THE STARTING ADDRESS OF RECORD TO BE WRITTEN IS		CMT34650
		3462	*	STORED AT LOCATION WLIM, AND THE ENDING ADDRESS		CMT34660
		3463	*	AT LOCATION WLIM+2. IF NO ERROR OCCURS DURING THE		CMT34670
		3464	*	TRANSFER. IT WILL RETURN ON 4(R12). ERROR RETURN		CMT34680
		3465	*	IS AT 0(R12)		CMT34690
		3466	*	CALLING SEQUENCE:		CMT34700
		3467	*	BAL R12,WRTREC		CMT34710
		3468	*	B ERROR	RETURN HERE ON ERROR	CMT34720
		3469	*	NEXT INSTRUCTION	RETURN HERE ON NORMAL COMPLETION	CMT34730
		3470	*	*****		CMT34740
		3471	*			CMT34750
2C3A	41E0 319E	3472	WRTPEC	BAL R13,WAIT2	WAIT FOR NMTN=1	CMT34760
2C3E	9D65	3473	SSR	DEV,STAT		CMT34770
2C40	C350 0020	3474	THI	STAT,X'20'	EOF?	CMT34780
2C44	4230 2C78	3475	BNZ	WEOT	YES - SET EOFFLAG	CMT34790
2C48	4850 346E	3476	LH	R5,MODFLG	WHICH MODE?	CMT34800
2C4C	C550 0001	3477	CLHI	R5,1		CMT34810



## SUBROUTINES

2CF2	030C	3531	RWREC3	BR	R12	ERROR RETURN	CMT35330
		3532	*			*	CMT35340
		3533	*	*****			CMT35350
		3534	*	SUBROUTINE RDREC			CMT35360
		3535	*	THIS ROUTINE READS A RECORD FROM THE MAG. TAPE			CMT35370
		3536	*	IT OPERATES EITHER ON SELCH MODE OR RB/WB MODE.			CMT35380
		3537	*	THE STARTING ADDRESS OF THE READ BUFFER IS STORED			CMT35390
		3538	*	AT LOCATION RLIM, AND THE ENDING ADDRESS AT			CMT35400
		3539	*	LOCATION RLIM+2. IF NO ERROR OCCURS DURING THE			CMT35410
		3540	*	TRANSFER, IT WILL RETURN ON 4(R12). ERROR RETURN			CMT35420
		3541	*	IS AT 0(R12)			CMT35430
		3542	*	CALLING SEQUENCE:			CMT35440
		3543	*	BAL	R12,RDREC	*	CMT35450
		3544	*	B	ERROR	*	CMT35460
		3545	*	NEXT INSTRUCTION			CMT35470
		3546	*	*****			CMT35480
		3547	*			*	CMT35490
2CF4	41D0 2F62	3548	RDREC	BAL	R13,CRBUF		CMT35500
2CF8	41D0 31BE	3549		BAL	R13,WAIT2	WAIT FOR MMTN=1	CMT35510
2CFC	4850 346E	3550		LH	R5,MODFLG		CMT35520
2D00	C550 0001	3551		CLHI	R5,1	RB/WB MODE?	CMT35530
2D04	4330 2D2C	3552		BE	RDBMD		CMT35540
		3553	*				CMT35550
		3554	*	SELCH MODE			CMT35560
		3555	*				CMT35570
2D08	D010 3F08	3556		STM	R1,RSAVE		CMT35580
2D0C	C810 3620	3557		LHI	R1,RLIM	SELCH READ LIMITS	CMT35590
2D10	D320 3484	3558		LB	R2,READ	DEVICE READ COMMAND	CMT35600
2D14	D330 3481	3559		LB	R3,GORD	SELCH GO & READ	CMT35610
2D18	C800 3135	3560		LHI	R0,C'15'	ERROR 15	CMT35620
2D1C	C840 3131	3561		LHI	R4,C'11'	ERROR 11	CMT35630
2D20	41F0 2D62	3562		BAL	R11,RWREC	READ A RECORD	CMT35640
2D24	43C0 2C7E	3563		B	ERROUT		CMT35650
2D28	4300 2C8C	3564		B	NORMRET		CMT35660
		3565	*				CMT35670
		3566	*	BLOCK MODE			CMT35680
		3567	*				CMT35690
2D2C	D010 3F08	3568	RDBMD	STM	R1,RSAVE		CMT35700
2D30	DCF0 3628	3569		STM	R15,RSV32		CMT35710
2C34	D1F0 3620	3570		LM	R15,RLIM		CMT35720
2D38	08BF	3571		LHR	R11,R15		CMT35730
2D3A	D1F0 3524	3572		LM	R15,RLIM+4		CMT35740
2D3E	08CF	3573		LHR	R12,R15		CMT35750
2D40	D1F0 3628	3574		LM	R15,RSV32		CMT35760
2D44	C800 3131	3575		LHI	R0,C'11'	ERROR 11	CMT35770
2D48	DE60 3484	3576		OC	DEV,READ		CMT35780
2D4C	976B	3577		RBR	DEV,R11		CMT35790
2D4E	43F0 2CDC	3578		BFC	R15,RWTRM1	CONDITION ZERO? - NORMAL RETURN	CMT35800
2D52	D110 3F08	3579	RABEND	LM	R1,RSAVE		CMT35810
2D56	9D65	3580		SSR	DEV,STAT		CMT35820
2D58	4210 32A0	3581		BTC	1,MTDU	DU?	CMT35830
2D5C	C800 3133	3582		LHI	R0,C'13'	ERROR 13	CMT35840
2D60	030C	3583		BR	R12	ERROR RETURN	CMT35850



## SUBROUTINES

2DD8	4230	2DFA	3637	BNE	MISMATCH	NO - MISMATCH	CMT36390
2DDC	9R75		3638	RDR	SELCH,R5		CMT3640C
2DDE	D451	0007	3639	CLB	R5,7(R1)		CMT3641G
2DE2	213C		3640	BNES	MISMATCH	NO - MISMATCH	CMT36420
2DE4	41D0	320C	3641	RWTRM	BAL R13,WAIT3	WAIT FOR EOM=1	CMT3643C
2DE8	9D65		3642	SSR	DEV,STAT		CMT36440
2DEA	C350	0080	3643	THI	STAT,X'80'	ERR SET?	CMT36450
2DEE	4230	2DF6	3644	BNZ	RWREC2	YES - BRANCH	CMT3646G
2DF2	430B	0004	3645	B	4(R11)	NORMAL RETURN - TO 2ND INSTRUCTION	CMT36470
			3646	*		AFTER CALL TO RWREC.	CMT36480
2DF6	0804		3647	RWREC2	LHR RO,R4	PUT ERROR NUM IN RO	CMT3649C
2DF8	030B		3648	BR	R11	ERROR RETURN	CMT36500
2DFA	9D65		3649	MISMATCH	SSR DEV,STAT	END ADDRESS MISMATCH	CMT36510
2DFC	4210	32A0	3650	BTC	1,MTDU	DU?	CMT3652C
2E00	C520	0022	3651	CLHI	R2,X'22'	IS DEVICE COMMAND 'WRITE'	CMT36530
2E04	023B		3652	BMZR	R11	NO - ERROR NUM IS ALREADY IN RO	CMT36540
2E06	C350	0020	3653	THI	STAT,X'20'	YES - EOT?	CMT3655C
2E0A	033B		3654	BZR	R11	NO - ERROR NUM IS ALREADY IN RO	CMT36560
2E0C	4050	346A	3655	STH	STAT,EOTFLG	YES - SET EOT FLAG	CMT36570
2E10	030B		3656	BR	R11	ERROR RETURN	CMT36580
			3657	*			CMT3659G
			3658	*	SUBROUTINE FSYNC		CMT3660C
			3659	*	THIS ROUTINE IS CALLED WHEN FALSE SYNC IS DETECTED		CMT36610
			3660	*	AFTER AN OUTPUT COMMAND. IT CALLS ERPALL, AND THEN		CMT36620
			3661	*	BRANCHES TO OPTIN TO ABORT THE TEST.		CMT3663C
			3662	*	CALLED ON R15		CMT36640
			3663	*	*****		CMT3665G
			3664	*			CMT3666G
2E12	9500		3665	FSYNC	EPSR RO,RO	GET CURRENT PSW	CMT36670
2E14	4000	16CA	3666	STH	RO,OPSW	SAVE PSW	CMT36680
2E18	40F0	16CE	3667	STH	R15,OLOC	SAVE LOCATION	CMT3669C
2E1C	4060	16D0	3668	STH	DEV,ERRDEV	SAVE DEVICE ADDRESS	CMT3670C
2E20	DD60	16D2	3669	SS	DEV,ERRSTA	SAVE STATUS BYTE	CMT3671C
2E24	C800	3030	3670	LHI	RO,C'00'	ERROR 00	CMT3672C
2E28	4000	173E	3671	STH	RO,ERRNO	SAVE ERROR NUMBER	CMT3673C
2E2C	41F0	0F9A	3672	BAL	R15,ERRALL		CMT3674C
2E30	4300	0AE6	3673	B	OPTIN	ABORT TEST	CMT3675C
			3674	*			CMT3676C
			3675	*	*****		CMT3677C
			3676	*	SUBROUTINE COMPAR		CMT3678C
			3677	*	THIS ROUTINE COMPARES THE DATA IN THE READ BUFFER		CMT3679C
			3678	*	WITH THAT IN THE WRITE BUFFER. IF MISMATCH IS		CMT3680C
			3679	*	DETECTED, THE BYTE FROM BOTH BUFFERS ARE PRINTED.		CMT3681C
			3680	*	CALLING SEQUENCE:		CMT3682C
			3681	*	BAL R14,COMPAR		CMT3683C
			3682	*	POSSIBLE ERROR: 46, 47		CMT3684C
			3683	*	*****		CMT3685G
			3684	*			CMT3686C
2E34	D010	3F88	3685	COMPAR	STM R1,RSAVE1		CMT3687C
2E38	2491		3686	LIS	R9,1		CMT3688C
2E3A	48A0	3466	3687	LH	R10,NBYTE		CMT3689C
2E3E	0788		3688	XHR	R8,R8		CMT3690C
2E40	41F0	126A	3689	COMBYT	BAL R15,TSTBRK	CHECK BREAK KEY	CMT36910

## SUBROUTINES

2E44	DOFO	3628	3690	STM	R15,RSV32		CMT36920	
2E48	D1FO	3620	3691	LM	R15,RLIM		CMT36930	
2E4C	OAF8		3692	AHR	R15,R8	BYTE NUMBER	CMT36940	
2E4E	D34F	0000	3693	LB	CHAR,0(R15)		CMT36950	
2E52	D1FO	3618	3694	LM	R15,WLIM	WRITE BUFFER ADDRESS	CMT36960	
2E56	OAF8		3695	AHR	R15,R8	BYTE NUMBER	CMT36970	
2E58	D35F	0000	3696	LB	R5,0(R15)	BYTE OF WRITE BUFFER	CMT36980	
2E5C	D1FO	3628	3697	LM	R15,RSV32	RESTORE R15	CMT36990	
2E60	0545		3698	CLHR	CHAR,R5	COMPARE	CMT37000	
2E62	4230	2EAO	3699	BNE	COMERR		CMT37010	
2E66	C180	2E40	3700	BXLE	R8,COMBYT	CONTINUE	CMT37020	
2E6A	DOFO	3628	3701	CHKDEL	STM	R15,RSV32	CMT37030	
2E6E	D1FO	3620	3702	LM	R15,RLIM		CMT37040	
2E72	OAF8		3703	AHR	R15,R10		CMT37050	
2E74	26F2		3704	AIS	R15,2		CMT37060	
2E76	D34F	0000	3705	LB	CHAR,0(R15)		CMT37070	
2E7A	D1FO	3628	3706	LM	R15,RSV32		CMT37080	
2E7E	C540	00C3	3707	CLHI	CHAR,X'C3'	COMPARE - X'C3'	CMT37090	
2E82	2339		3708	BES	ENDCOMP		CMT37100	
2E84	C800	3437	3709	LHI	R0,C'47'	ERROR 47	CMT37110	
2E88	41FO	0F6A	3710	BAL	R15,ERRD		CMT37120	
2E8C	C850	356A	3711	LHI	R5,MSG08		CMT37130	
2E90	41D0	3186	3712	BAL	R13,MSGPRT		CMT37140	
2E94	0711		3713	ENDCOMP	XHR	R1,R1	CMT37150	
2E96	4010	346C	3714	STH	R1,ERRFLG	RESET ERROR FLAG	CMT37160	
2E9A	D110	3F88	3715	LM	R1,RSV32		CMT37170	
2E9E	030E		3716	BR	R14	RETURN	CMT37180	
2EA0	4810	346C	3717	COMERR	LH	R1,ERRFLG	DATA NOT EQUAL - CHECK ERROR FLAG	CMT37190
2EA4	4230	2EDA	3718	BNZ	PRIND		CMT37200	
2EA8	C800	3436	3719	LHI	R0,C'46'	ERROR 46	CMT37210	
2EAC	4000	346C	3720	STH	R0,ERRFLG	SET ERROR FLAG	CMT37220	
2EB0	41FO	0F6A	3721	BAL	R15,ERRD		CMT37230	
2EB4	4050	3E3C	3722	STH	R5,TEMP		CMT37240	
2EB8	C850	356A	3723	LHI	R5,MSG08		CMT37250	
2EBC	41D0	3186	3724	BAL	R13,MSGPRT		CMT37260	
2EC0	C850	34BC	3725	LHI	R5,MSG01A		CMT37270	
2EC4	41D0	3186	3726	BAL	R13,MSGPRT	PRINT MESSAGE	CMT37280	
2EC8	C850	34CC	3727	LHI	R5,MSG01B		CMT37290	
2ECC	41D0	3186	3728	BAL	R13,MSGPRT	PRINT MESSAGE	CMT37300	
2ED0	4850	3E3C	3729	LH	R5,TEMP		CMT37310	
2ED4	0711		3730	XHR	R1,R1		CMT37320	
2ED6	4010	16FE	3731	STH	R1,ISITERR		CMT37330	
2EDA	2402		3732	PRIND	LJS	R0,2	CMT37340	
2EDC	41FO	10DA	3733	BAL	R15,R5HEX	PRINT DATA BYTE	CMT37350	
2EE0	0854		3734	LHR	R5,CHAR		CMT37360	
2EE2	C840	0020	3735	LHI	R4,X'20'	SPACE	CMT37370	
2EE6	0722		3736	XHR	R2,R2		CMT37380	
2EE8	D000	3F08	3737	SPACE8	STM	R0,RSV32	CMT37390	
2EEC	41FO	11B0	3738	BAL	R15,OUTCHR		CMT37400	
2EF0	D100	3F08	3739	LM	R0,RSV32		CMT37410	
2EF4	2621		3740	AIS	R2,1		CMT37420	
2EF6	C520	0008	3741	CLHI	R2,8		CMT37430	
2EFA	2089		3742	BLS	SPACE8		CMT37440	



## SUBROUTINES

2EFC	2402	3743	LIS	R0,2		CMT37430
2EFE	41F0 10DA	3744	BAL	R15,R5HEX	PRINT DATA BYTE	CMT37440
2F02	41F0 11A2	3745	BAL	R15,CRLF		CMT37450
2F06	C180 2E40	3746	BXLE	R8,COMBYT	CONTINUE	CMT37460
2FOA	4300 2E6A	3747	B	CHKDEL		CMT37470
		3748	*			CMT37480
		3749	*	*****		CMT37490
		3750	*	SUBROUTINE RESET		CMT37500
		3751	*	THIS ROUTINE SETS UP THE READ AND WRITE BUFFER		CMT37510
		3752	*	LIMITS.		CMT37520
		3753	*	CALLING SEQUENCE:		CMT37530
		3754	*	BAL R14,RESET		CMT37540
		3755	*	*****		CMT37550
		3756	*			CMT37560
2FOE	4800 3466	3757	RESET	LH R0,NBYTE		CMT37570
2F12	D0F0 3628	3758	STM	R15,RSV32		CMT37580
2F16	D1F0 3618	3759	LM	R15,WLIM		CMT37590
2F1A	085F	3760	LHR	R5,R15		CMT37600
2F1C	0A50	3761	AHR	R5,R0		CMT37610
2F1E	08F5	3762	LHR	R15,R5		CMT37620
2F20	D0F0 361C	3763	STM	R15,WLIM+4		CMT37630
2F24	D1F0 3620	3764	LM	R15,RLIM		CMT37640
2F28	085F	3765	LHP	R5,R15		CMT37650
2F2A	0A50	3766	AHR	R5,R0		CMT37660
2F2C	08F5	3767	LHR	R15,R5		CMT37670
2F2E	D0F0 3624	3768	STM	R15,RLIM+4		CMT37680
2F32	D1F0 3628	3769	LM	R15,RSV32		CMT37690
2F36	030E	3770	BR	R14		CMT37700
		3771	*			CMT37710
		3772	*	*****		CMT37720
		3773	*	SUBROUTINE BSET		CMT37730
		3774	*	THIS ROUTINE SETS UP THE WRITE BUFFER. IT FILLS		CMT37740
		3775	*	THE BUFFER WITH DATA OF 00-FF, AND SETS THE DELIMITER		CMT37750
		3776	*	AT THE END OF THE READ BUFFER.		CMT37760
		3777	*	CALLING SEQUENCE:		CMT37770
		3778	*	BAL R14,BSET		CMT37780
		3779	*	*****		CMT37790
		3780	*			CMT37800
2F38	D010 3F88	3781	BSET	STM R1,RSV1		CMT37810
2F3C	2491	3782	LIS	R9,1		CMT37820
2F3E	48A0 3466	3783	LH	R10,NBYTE		CMT37830
2F42	0788	3784	XHR	R8,R8		CMT37840
2F44	0858	3785	SETWBUF	LHR R5,R8		CMT37850
2F46	4450 3464	3786	NH	R5,MASK	MASK FOR 7 TRACK	CMT37860
2F4A	D1F0 3618	3787	LM	R15,WLIM		CMT37870
2F4E	0AF8	3788	AHR	R15,R8		CMT37880
2F50	D25F 0000	3789	STB	R5,0(R15)		CMT37890
2F54	41F0 126A	3790	BAL	R15,TSTBRK	CHECK BREAK KEY	CMT37900
2F58	C180 2F44	3791	BXLE	R8,SETWBUF		CMT37910
2F5C	D110 3F88	3792	LM	R1,RSV1		CMT37920
2F60	030E	3793	BR	R14		CMT37930
		3794	*			CMT37940
		3795	*	*****		CMT37950

## SUBROUTINES

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3796 * SUBROUTINE CRBUF * CMT37960
3797 * THIS ROUTING CLEARS THE READ BUFFER AND SETS THE * CMT37970
3798 * DELIMITER (X'C3C3') AT THE END OF THE BUFFER * CMT37980
3799 * CALLING SEQUENCE: * CMT37990
3800 * BAL R13,CRBUF * CMT38000
3801 * ***** * CMT38010
3802 * * CMT38020
2F62 D010 3F88 3803 CRBUF STM R1,RSAVE1 CMT38030
2F66 D1F0 3624 3804 LM R15,RLIM+4 CMT38040
2F6A 08AF 3805 LHR R10,R15 CMT38050
2F6C 2492 3806 LIS R9,2 CMT38060
2F6E 0755 3807 XHR R5,R5 CMT38070
2F70 D1F0 3620 3808 LM R15,RLIM CMT38080
2F74 088F 3809 LHR R8,R15 CMT38090
2F76 4058 0000 3810 CRBUF1 STM R5,0(R8) CMT38100
2F7A 41F0 126A 3811 BAL R15,TSTBRK CHECK BREAK KEY CMT38110
2F7E C180 2F76 3812 BXLE R8,CRBUF1 CMT38120
2F82 C850 C3C3 3813 LHI R5,X'C3C3' CMT38130
2F86 D25A 0002 3814 STB R5,2(R10) CMT38140
2F8A D110 3F88 3815 LM R1,RSAVE1 CMT38150
2F8E 030D 3816 BR R13 CMT38160
3817 * * CMT38170
3818 * ***** * CMT38180
3819 * SUBROUTINE DUMP * CMT38190
3820 * THIS ROUTINE DUMPS THE READ BUFFER ONE BYTE AT A * CMT38200
3821 * TIME AND 16 BYTES IN A LINE. * CMT38210
3822 * CALLING SEQUENCE: * CMT38220
3823 * BAL R14,DUMP * CMT38230
3824 * ***** * CMT38240
3825 * * CMT38250
2F90 D010 3F88 3826 DUMP STM R1,RSAVE1 CMT38260
2F94 2491 3827 LIS R9,1 CMT38270
2F96 24AF 3828 LIS R10,15 16 BYTES PER LINE CMT38280
2F98 0722 3829 XHR R2,R2 CMT38290
2F9A C840 0020 3830 LHI R4,X'20' SPACE CMT38300
2F9E 0788 3831 OUTDMP XHR R8,R8 CMT38310
2FA0 D0F0 3628 3832 DMPLIN STM R15,RSV32 SAVE R15 CMT38320
2FA4 D1F0 3620 3833 LM R15,RLIM READ BUFFER ADDRESS CMT38330
2FA8 0AF2 3834 AHR R15,R2 CMT38340
2FAA D35F 0000 3835 LB R5,0(R15) LOAD BYTE FROM READ BUFFER CMT38350
2FAE D1F0 3628 3836 LM R15,RSV32 RESTORE R15 CMT38360
2FB2 2402 3837 LIS R0,2 CMT38370
2FB4 41F0 10DA 3838 BAL R15,R5HEX PRINT BYTE CMT38380
2FB8 41F0 11B0 3839 BAL R15,OUTCHR PRINT SPACE CMT38390
2FBC 41F0 126A 3840 BAL R15,TSTBRK BREAK? CMT38400
2FC0 4520 3466 3841 CLH R2,NBYTE FULL BUFFER PRINTED? CMT38410
2FC4 2388 3842 BNLS DUBLIN CMT38420
2FC6 2621 3843 AIS R2,1 NO - CONTINUE CMT38430
2FC8 C180 2FA0 3844 BXLE R8,DMPLIN 16 BYTES? CMT38440
2FCC 41F0 11A2 3845 BAL R15,CRLF YES - CR,LF CMT38450
2FDO 4300 2F9E 3846 B OUTDMP CMT38460
2FD4 41F0 11A2 3847 DUBLIN BAL R15,CRLF DOUBLE LINE FEED CMT38470
2FD8 41F0 11A2 3848 BAL R15,CRLF CMT38480

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## SUBROUTINES

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2FDC  D110 3F88      3849      LM    R1,RSAVE1      CMT3849C
2FE0  030E          3850      BR    R14              RETURN      CMT3850C
3851  *              3851  *              *              CMT3851C
3852  * *****              *              CMT3852C
3853  * SUBROUTINE SENS01, SENS02 & SENS03              *              CMT3853C
3854  * THIS ROUTINE DETERMINES WHETHER AN EOF HAS BEEN *              CMT3854C
3855  * DETECTED. IF NOT, AN ERROR MESSAGE WILL BE PRINTED *              CMT3855C
3856  * AND RETURN ON ERROR. IF NO ERROR IS DETECTED, IT *              CMT3856C
3857  * WILL RETURN TO LOCATION 4(R14)                    *              CMT3857C
3858  * THREE ENTRY POINTS ARE PROVIDED:                  *              CMT3858C
3859  * SENS01      FOR SENSING EOF AFTER WEOF              *              CMT3859C
3860  * SENS02      FOR SENSING EOF AFTER READ              *              CMT3860C
3861  * SENS03      FOR SENSING EOF AFTER SKIP & BACKSPACE *              CMT3861C
3862  * CALLING SEQUENCE:                                  *              CMT3862C
3863  * BAL  R14,SENS01      (EXAMPLE)                      *              CMT3863C
3864  * B    ERROR          ERROR RETURN HERE              *              CMT3864C
3865  * NEXT INSTRUCTION  NORMAL RETURN HERE              *              CMT3865C
3866  * *****              *              CMT3866C
3867  *              *              CMT3867C
2FE2  C800 3035      3868  SENS01  LHI  R0,C'05'      ERROR 05 (WEOF)      CMT3868C
2FE6  2306          3869          BS  SENE0F              CMT3869C
2FE8  C800 3036      3870  SENS02  LHI  R0,C'06'      ERROR 06 (READ EOF)  CMT3870C
2FEC  2303          3871          BS  SENE0F              CMT3871C
2FEE  C800 3037      3872  SENS03  LHI  R0,C'07'      ERROR 07 (SKIP & BACKSPACE EOF)  CMT3872C
2FF2  41E0 320C      3873  SENE0F  BAL  R13,WAIT3      WAIT FOR EOM=1      CMT3873C
2FF6  9D65          3874          SSR  DEV,STAT              CMT3874C
2FF8  2348          3875          BFFS 4,E0FER      EX BIT SET?          CMT3875C
2FFA  C350 0080      3876          THI  STAT,X'80'      ERR BIT SET?          CMT3876C
2FFE  2135          3877          BNZS E0FER              CMT3877C
3000  C350 0040      3878          THI  STAT,X'40'      EOF DETECTED?        CMT3878C
3004  423E 0004      3879          BNZ  4(R14)              CMT3879C
3008  41F0 0F82      3880  E0FER  BAL  R15,ERRDS              CMT3880C
300C  030E          3881      BR    R14              CMT3881C
3882  *              *              CMT3882C
3883  * *****              *              CMT3883C
3884  * SUBROUTINE ERRMSG2                                  *              CMT3884C
3885  * THIS SUBROUTINE PRINTS THE ERROR MESSAGES WITH THE *              CMT3885C
3886  * MODE MESSAGE                                          *              CMT3886C
3887  * THE MESSAGE PRINTED IS:                              *              CMT3887C
3888  * ERROR  XYYY          XX=TEST #, YY=ERROR #          *              CMT3888C
3889  * DEV DD STA SS      DD=DEVICE #, SS=STATUS          *              CMT3889C
3890  * MODE N              N=MODE NUMBER                  *              CMT3890C
3891  * RETURN ON R14                                        *              CMT3891C
3892  * *****              *              CMT3892C
300E  41E0 0F82      3893  ERRMSG2  BAL  R15,ERRDS      PRINT ERROR MESSAGE  CMT3893C
3012  C850 356A      3894          LHI  R5,MSG08              CMT3894C
3016  41D0 3186      3895          BAL  R13,MSGPRT              CMT3895C
301A  030E          3896          BR    R14              CMT3896C
3897  * *****              *              CMT3897C
3898  * SUBROUTINE SETMOD & TSTMOD                          *              CMT3898C
3899  * THESE ROUTINES SET THE PROPER MODE THE DEVICE IS TO *              CMT3899C
3900  * BE TESTED UNDER.                                     *              CMT3900C
3901  * ROUTINE SETMOD SETS THE INITIAL TEST MODE ACCORDING *              CMT3901C

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## SUBROUTINES

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3902 *          TO THE OPTION MODE. IF ZERO, IT WILL SET MODE 2          *          CMT39020
3903 *          ROUTINE TSTM0D TESTS IF ANY MORE TEST IS TO BE          *          CMT39030
3904 *          PERFORMED UNDER A DIFFERENT MODE. IF MODE OPTION          *          CMT39040
3905 *          IS ZERO, IT WILL DECREMENT MODE. IF MODE OPTION IS          *          CMT39050
3906 *          NON-ZERO OR DECREMENTED MODE IS ZERO. IT WILL BRANCH          *          CMT39060
3907 *          TO TEST END.                                              *          CMT39070
3908 *          CALLING SEQUENCE:                                          *          CMT39080
3909 *          BAL  R13,SETMOD      OR          *          CMT39090
3910 *          BAL  R13,TSTM0D          *          CMT39100
3911 *          *****                                                  *          CMT39110
3912 *          *          CMT39120
301C 4850 1840 3913 SETMOD  LH  R5,MODE+6          GET MODE OPTION          CMT39130
3020 213C      3914      BNZS  MSET          CMT39140
3022 2452      3915      LIS  R5,2          MODE 0 - START WITH MODE 2          CMT39150
3024 230A      3916      BS  MSET          CMT39160
3026 4850 1840 3917 TSTM0D  LH  R5,MODE+6          MODE 0?          CMT39170
302A 4230 2B9A 3918      BNZ  CHKEND          NO - END TEST          CMT39180
302E 4850 346E 3919      LH  R5,MODFLG          YES -          CMT39190
3032 2751      3920      SIS  R5,1          DECREMENT MODE FLAG          CMT39200
3034 4330 2B9A 3921      BZ  CHKEND          ZERO? - END TEST          CMT39210
3038 4050 346E 3922 MSET  STH  R5,MODFLG          STORE          CMT39220
303C CA50 0030 3923      AHI  R5,X'30'          CMT39230
3040 D250 356F 3924      STB  R5,MSG08+5          SET MODE MESSAGE          CMT39240
3044 41F0 126A 3925      BAL  R15,TSTBRK          CHECK BREAK KEY          CMT39250
3048 030D      3926      BR  R13          CMT39260
3927 *          *****                                                  *          CMT39270
3928 *          SUBROUTINE RETRY                                          *          CMT39280
3929 *          THIS ROUTINE KEEPS A RETRY COUNT. IF THE COUNT IS          *          CMT39290
3930 *          LESS THAN 5, THE ROUTINE WILL BACKSPACE AND RETURN          *          CMT39300
3931 *          AT LOCATION 0(R14). OTHERWISE, IT RETURNS AT 4(R14).          *          CMT39310
3932 *          CALLING SEQUENCE:                                          *          CMT39320
3933 *          BAL  R14,RETRY          *          CMT39330
3934 *          B    TRY AGAIN          GO TRY AGAIN          *          CMT39340
3935 *          B    PROCEED          PROCEED          *          CMT39350
3936 *          *****                                                  *          CMT39360
3937 *          *          CMT39370
304A 4850 3470 3938 RETRY  LH  R5,RTYCNT          LOAD RETRY COUNTER          CMT39380
304E C550 0005 3939      CLHI R5,5          5 TIMES?          CMT39390
3052 238B      3940      BNLS RTYFAIL          CMT39400
3054 2651      3941      AIS  R5,1          INCREMENT COUNTER          CMT39410
3056 4050 3470 3942      STH  R5,RTYCNT          CMT39420
305A 41D0 31BE 3943      BAL  R13,WAIT2          WAIT FOR NMTN=1          CMT39430
305E DE60 3483 3944      OC  DEV,BKSPAC          BACKSPACE          CMT39440
3062 41F0 126A 3945      BAL  R15,TSTBRK          CHECK BREAK KEY          CMT39450
3066 030E      3946      BR  R14          CMT39460
3068 0755      3947 RTYFAIL XHR  R5,R5          5 TIMES FAILED          CMT39470
306A 4050 3470 3948      STH  R5,RTYCNT          CMT39480
306E C850 34DC 3949      LHI  R5,MSG02          CMT39490
3072 41D0 3186 3950      BAL  R13,MSGPRT          PRINT MESSAGE          CMT39500
3076 41F0 126A 3951      BAL  R15,TSTBRK          CHECK BREAK KEY          CMT39510
307A 430E 0004 3952      B    4(R14)          CMT39520
3953 *          *****                                                  *          CMT39530
3954 *          *          CMT39540

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## SUBROUTINES

		3955	*	SUBROUTINE INDATA		*	CMT39550
		3956	*	THIS ROUTINE ACCEPTS A DATA STRING OF UP TO 64 BYTES		*	CMT39560
		3957	*	FROM THE TTY. THE INPUT CHARACTER MUST BE A VALID		*	CMT39570
		3958	*	HEX CHARACTER. AND THE PROGRAM WILL STORE THE		*	CMT39580
		3959	*	CORRESPONDING HEX VALUE INTO THE WRITE BUFFER. UPON		*	CMT39590
		3960	*	RECEPTION OF CR, THE ROUTING WILL GENERATE THE WHOLE		*	CMT39600
		3961	*	WRITE BUFFER BY REPEATING THE INPUTED STRING		*	CMT39610
		3962	*			*	CMT39620
		3963	*	IF THE TEST IS REPEATED BY MODE=0, CONTIN=1 OR LOOP,		*	CMT39630
		3964	*	THIS ROUTINE WILL BE BY-PASSED AFTER THE FIRST PASS.		*	CMT39640
		3965	*	NO DATA IS REQUESTED ON SUBSEQUENT PASSES. THIS		*	CMT39650
		3966	*	ROUTINE WILL NEVER BE EXECUTED IF OPTION DATA IS		*	CMT39660
		3967	*	RESET.		*	CMT39670
		3968	*	CALLING SEQUENCE		*	CMT39680
		3969	*	BAL R14,INDATA		*	CMT39690
		3970	*			*	CMT39700
		3971	*	*****		*	CMT39710
		3972	*			*	CMT39720
307E	4840	190C	3973	INDATA LH R4,DATA+6	DATA OPTION SET?		CMT39730
3082	033E		3974	BZR R14	NO - EXIT		CMT39740
3084	4840	3468	3975	LH R4,DE	DATA FLAG SET?		CMT39750
3088	023E		3976	BNZRF R14	YES - EXIT		CMT39760
308A	244F		3977	LIS R4,15	NO - SET DATA FLAG		CMT39770
308C	4040	3468	3978	STH R4,DE	AND		CMT39780
3090	D010	3F88	3979	STM R1,RSAVE1	GET DATA PATTERN		CMT39790
3094	C850	3572	3980	LHI R5,MSG09	PRINT MESSAGE		CMT39800
3098	41C0	3186	3981	BAL R13,MSGPRT			CMT39810
309C	41F0	126A	3982	BAL R15,TSTBRK	CHECK BREAK KEY		CMT39820
30A0	2491		3983	LIS R9,1			CMT39830
30A2	0788		3984	XHR R8,R8			CMT39840
30A4	0722		3985	XHR R2,R2			CMT39850
30A6	D080	3F08	3986	GETDATA STM R8,RSAVE			CMT39860
30AA	41F0	121C	3987	BAL R15,GETCHR	GET A CHARACTER		CMT39870
30AE	D180	3F08	3988	LM R8,RSAVE			CMT39880
30B2	C540	000D	3989	CLHI CHAR,X'OD'	CR?		CMT39890
30B6	4330	314C	3990	BE INEND	YES - INPUT END		CMT39900
30BA	41D0	3158	3991	BAL R13,HEXCHK	CHECK FOR HEX CHAR		CMT39910
30BE	220C		3992	BS GETDATA	INVALID DATA, GET ANOTHER		CMT39920
30C0	0854		3993	LHR R5,CHAR			CMT39930
30C2	9154		3994	SLLS R5,4	SHIFT FIRST HEX DIGIT LEFT		CMT39940
30C4	D080	3F08	3995	STM R8,RSAVE			CMT39950
30C8	41F0	121C	3996	GTDAT2 BAL R15,GETCHR	GET SECOND CHARACTER		CMT39960
30CC	D180	3F08	3997	LM R8,RSAVE			CMT39970
30D0	C540	000D	3998	CLHI CHAR,X'OD'	CR?		CMT39980
30D4	4330	310E	3999	BE INEND1	YES - INPUT END		CMT39990
30D8	41D0	3158	4000	BAL R13,HEXCHK	CHECK HEX CHAR		CMT40000
30DC	220A		4001	BS GTDAT2	INVALID DATA, GET ANOTHER		CMT40010
30DE	0654		4002	OHP R5,CHAR	APPEND SECOND HEX DIGIT		CMT40020
30E0	4450	3464	4003	NH R5,MASK			CMT40030
30E4	D0F0	3628	4004	STM R15,RSV32			CMT40040
30E8	D1F0	3618	4005	LM R15,WLIM			CMT40050
30EC	0AF8		4006	AHR R15,R8			CMT40060
30EE	D25F	0000	4007	STB R5,0(R15)			CMT40070

## SUBROUTINES

30F2	D1F0	3628	4008	LM	R15,RSV32		CMT40080
30F6	2622		4009	AIS	R2,2		CMT40090
30F9	C520	0040	4010	CLHI	R2,64	64 CHARACTERS (32 HEX)?	CMT40100
30FC	4380	3120	4011	BNL	INEND2		CMT40110
3100	C180	30A6	4012	EXLE	R8,GETDATA	BUFFER LENGTH EXCEED?	CMT40120
3104	41F0	11A2	4013	DATFIL	BAL	R15,CRLF	CMT40130
3108	D110	3F88	4014	LM	R1,RSV1		CMT40140
310C	030E		4015	BR	R14		CMT40150
310E	D0F0	3628	4016	INEND1	STM	R15,RSV32	CMT40160
3112	D1F0	3618	4017	LM	R15,WLIM		CMT40170
3116	0AF8		4018	AHR	R15,R8		CMT40180
3118	D25F	0000	4019	STB	R5,0(R15)		CMT40190
311C	D1F0	3628	4020	LM	R15,RSV32		CMT40200
3120	0722		4021	INEND2	XHR	R2,R2	CMT40210
3122	C080	3104	4022	MOVDATA	BXH	R8,DATFIL	CMT40220
3126	D0F0	3628	4023	MOVDAT1	STM	R15,RSV32	CMT40230
312A	D1F0	3618	4024	LM	R15,WLIM		CMT40240
312E	0AF2		4025	AHR	R15,R2		CMT40250
3130	D34F	0000	4026	LB	CHAR,0(R15)		CMT40260
3134	D1F0	3618	4027	LM	R15,WLIM		CMT40270
3138	0AF8		4028	AHR	R15,R8		CMT40280
313A	D25F	0000	4029	STB	R5,0(R15)		CMT40290
313E	D1F0	3628	4030	LM	R15,RSV32		CMT40300
3142	41F0	126A	4031	BAL	R15,TSTBRK	CHECK BREAK KEY	CMT40310
3146	2621		4032	AIS	R2,1		CMT40320
3148	4300	3122	4033	B	MOVDATA		CMT40330
314C	0822		4034	INEND	LHR	R2,R2	CMT40340
314E	4330	3104	4035	BZ	DATFIL		CMT40350
3152	0722		4036	XHR	R2,R2		CMT40360
3154	4300	3126	4037	B	MOVDAT1		CMT40370
			4038	*	*****		CMT40380
			4039	*	SUBROUTINE HEXCHK		CMT40390
			4040	*	THIS ROUTINE CHECKS IF THE CONTENT OF R4 (CHAR) IS		CMT40400
			4041	*	A VALID HEX CHARACTER. IT THEN CONVERTS IT INTO A		CMT40410
			4042	*	HEX DIGIT, AND RETURNS AT 4(R13). IF THE CHARACTER		CMT40420
			4043	*	IS NOT A VALID HEX CHARACTER, IT OUTPUTS A '?',		CMT40430
			4044	*	AND RETURNS AT 0(R13)		CMT40440
			4045	*	CALLING SEQUENCE:		CMT40450
			4046	*	BAL R13,HEXCHK		CMT40460
			4047	*	B ERROR	ERROR RETURN	CMT40470
			4048	*	NEXT INSTRUCTION	NORMAL RETURN	CMT40480
			4049	*	*****		CMT40490
			4050	*			CMT40500
			4051	*			CMT40510
3158	C540	0030	4052	HEXCHK	CLHI	CHAR,C'0'	LESS THAN 0?
315C	4280	317C	4053		BL	NOHEX	YES - INVALID
3160	C540	003A	4054		CLHI	CHAR,X'3A'	NO - LESS THAN X'3A'?
3164	2188		4055		BLS	GDHEX	YES - VALID
3166	C540	0041	4056		CLHI	CHAR,C'A'	NO - LESS THAN A?
316A	2189		4057		BLS	NOHEX	YES - INVALID
316C	C540	0047	4058		CLHI	CHAR,C'G'	NO - GREATER THAN F?
3170	2386		4059		BLS	NOHEX	YES - INVALID
3172	2649		4060		AIS	CHAR,9	NO - CONVERT TO HEX DIGIT

## SUBROUTINES

3174	C440 000F	4061	GDHEX	NHI	CHAR,X'0F'				
3178	43CE 0002	4062		B	2(R13)			CMT40610	
317C	C840 003F	4063	NOHEX	LHI	CHAR,C'2'	INVALID CHAR -		CMT40620	
3180	41FC 11B0	4064		BAL	R15,OUTCHR	PRINT '?'		CMT40630	
3184	03CE	4065		BR	R13			CMT40640	
		4066	*	*****					CMT40650
		4067	*	SUBROUTINE MSGPRT					CMT40660
		4068	*	THIS ROUTINE SETS UP THE CALLING SEQUENCE TO PRINT					CMT40670
		4069	*	A MESSAGE. THE STARTING ADDRESS OF THE MESSAGE					CMT40680
		4070	*	SHOULD BE STORED IN R5.					CMT40690
		4071	*	CALLING SEQUENCE:					CMT40700
		4072	*	BAL	R13,MSGPRT			CMT40710	
		4073	*	*****					CMT40720
		4074	*						CMT40730
3186	4050 16FE	4075	MSGPRT	STH	R5,ISITERR			CMT40740	
318A	41F0 112A	4076		BAL	R15,PRINT			CMT40750	
318E	0755	4077		XHR	R5,R5			CMT40760	
3190	4050 16FE	4078		STH	R5,ISITERR			CMT40770	
3194	41F0 126A	4079		BAL	R15,TSTBRK	CHECK BREAK KEY		CMT40780	
3198	03CD	4080		BR	R13			CMT40790	
		4081	*						CMT40800
		4082	*	*****					CMT40810
		4083	*	SUBROUTINE TIMEOUT					CMT40820
		4084	*	THIS ROUTINE WAITS FOR INTERRUPT WITH INTERRUPT					CMT40830
		4085	*	ENABLED AT PROCESSOR LEVEL. A TIMER IS SET UP TO					CMT40840
		4086	*	TIME OUT THE INTERRUPT WAITING PERIOD AND THE					CMT40850
		4087	*	CALLING PROGRAM CAN SPECIFY THE TIME-OUT IN UNITS					CMT40860
		4088	*	OF IOMS EACH BY SPECIFY THE NUMBER OF UNITS DESIRED					CMT40870
		4089	*	AT THE HALFWORD FOLLOWING THE CALLING INSTRUCTION.					CMT40880
		4090	*	IF INTERRUPT IS RECEIVED, EXIT IS MADE TO AN					CMT40890
		4091	*	INTERRUPT HANDLER IN THE PROGRAM EXECUTIVE. WHICH					CMT40900
		4092	*	WILL IN TURN BRANCH TO LOCATION SET UP BY THE					CMT40910
		4093	*	PROGRAM BEFORE ENTERING TIMEOUT ROUTINE.					CMT40920
		4094	*	IF THE ROUTINE TIMES OUT, IT WILL PICK UP THE ERROR					CMT40930
		4095	*	NUMBER FROM R11, PRINT THE ERROR MESSAGE, AND EXIT AT					CMT40940
		4096	*	LOCATION 4(R14).					CMT40950
		4097	*	CALLING SEQUENCE:					CMT40960
		4098	*	BAL	R14,TIMEOUT			CMT40970	
		4099	*	DC	N	NUMBER OF IOMS UNITS FOR I.O.		CMT40980	
		4100	*	*****					CMT40990
		4101	*						CMT41000
319A	48C0 0A22	4102	TIMEOUT	LH	R0,PSW	ENABLE INTERRUPT AT		CMT41010	
319E	9550	4103		EPSR	R5,R0	PROCESSOR LEVEL		CMT41020	
31A0	41F0 126A	4104		BAL	R15,TSTBRK	CHECK BREAK KEY		CMT41030	
31A4	48CE 0000	4105		LH	R0,(R14)	PICK UP DESIRED TIME PERIOD		CMT41040	
31A8	41FC 10C0	4106		BAL	R15,TIMER	DELAY TIMER (BASIC IOMS)		CMT41050	
31AC	C8C0 30F0	4107		LHI	R0,X'30F0'	DISABLE INTERRUPTS AT		CMT41060	
31B0	9550	4108		EPSR	R5,R0	PROCESSOR LEVEL		CMT41070	
31B2	08C0	4109		LHR	R0,R11	PICK UP ERROR NUMBER		CMT41080	
31B4	9DE5	4110		SSR	DEV,STAT			CMT41090	
31B6	41F0 0F82	4111		BAL	R15,ERRDS			CMT41100	
31BA	43CE 0002	4112		B	2(R14)			CMT41110	
		4113	*	*****					CMT41120
									CMT41130

## SUBROUTINES

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4114 * SUBROUTINE WAIT2 * CMT41140
4115 * THIS ROUTINE WAITS FOR NMTN=1 UNDER TIMED CONDITION * CMT41150
4116 * IF ROUTINE TIMES OUT OR DETECTS END OF TAPE (EOT), * CMT41160
4117 * THE DEVICE IS RESET, ERROR MESSAGE IS PRINTED AND * CMT41170
4118 * THE CURRENT TEST IS ABORTED. * CMT41180
4119 * RETURN ON R13 * CMT41190
4120 * ERROR: 01 * CMT41200
4121 * ***** CMT41210
4122 WAIT2 SSR DEV,STAT * CMT41220
4123 BTC 1,MTDU DEVICE UNAVAILABLE CMT41230
4124 TH1 STAT,X'10' NMTN = 1? CMT41240
4125 BNZR R13 YES - EXIT CMT41250
4126 STM R1,RSAVE1 CMT41260
4127 LIS R2,1 CMT41270
4128 LH R3,TIME 10MS TIMING LOOP CMT41280
4129 LHR R9,R2 CMT41290
4130 LIS R10,10 CMT41300
4131 XHR R8,R8 CMT41310
4132 WX21 XHR R1,R1 TIME OUT LOOP CMT41320
4133 WX22 SSR DEV,STAT CMT41330
4134 BTC 1,MTDU DU? CMT41340
4135 TH1 STAT,X'10' NMTN = 1? CMT41350
4136 BNZ W2EXIT YES EXIT CMT41360
4137 BAL R15,TSTBRK CHECK BREAK KEY CMT41370
4138 BXLE R1,WX22 CMT41380
4139 BXLE R8,WX21 CMT41390
4140 OC DEV,CLEAR TIMED OUT ON NMTN CMT41400
4141 LHI R0,C'01' ERROR 01 CMT41410
4142 BAL R15,ERRDS CMT41420
4143 B OPTIN CMT41430
4144 W2EXIT LM R1,RSAVE1 CMT41440
4145 BR R13 CMT41450
4146 * ***** CMT41460
4147 * SUBROUTINE WAIT3 * CMT41470
4148 * THIS ROUTINE WAITS FOR EOM UNDER TIMED CONDITION. * CMT41480
4149 * IT IS CALLED AFTER EVERY READ, WRITE, BACKSPACE, * CMT41490
4150 * WEOF OR SKIP OPERATION. IF EOM IS NOT SET AFTER * CMT41500
4151 * TIME OUT, THE ROUTINE RETURNS WITH AN ERROR MESSAGE * CMT41510
4152 * CALLING SEQUENCE: * CMT41520
4153 * BAL R13,WAIT3 * CMT41530
4154 * CMT41540
4155 * ***** CMT41550
320C 9D65 4156 WAIT3 SSR DEV,STAT CMT41560
320E 4210 32A0 4157 BTC 1,MTDU DU? CMT41570
3212 022D 4158 BTCR 2,R13 EOM - EXIT CMT41580
3214 D010 3F88 4159 STM R1,RSAVE1 CMT41590
3218 2421 4160 LIS R2,1 SET UP TIME OUT COUNTER CMT41600
321A 4830 0A1E 4161 LH R3,TIME CMT41610
321E 0892 4162 LHR R9,R2 CMT41620
3220 C8A0 0064 4163 LHI R10,100 CMT41630
3224 0788 4164 XHR R8,R8 CMT41640
3226 0711 4165 WX31 XHR R1,R1 CMT41650
3228 9D65 4166 WX32 SSR DEV,STAT CMT41660

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## SUBROUTINES

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322A 4210 32A0          4167          BTC 1,MTDU          DU?          CMT41670
322E 4220 3246          4168          BTC 2,W3EXIT        EOM - EXIT      CMT41680
3232 41F0 126A          4169          BAL R15,TSTBRK     CHECK BREAK KEY CMT41690
3236 C110 3228          4170          BXLE R1,WX32       CMT41700
323A C180 3226          4171          BXLE R8,WX31       CMT41710
323E C800 3034          4172          LHI R0,C'04'       TIMED OUT - ERROR 04 CMT41720
3242 41F0 0F82          4173          BAL R15,ERRDS      CMT41730
3246 D110 3F88          4174          W3EXIT LM R1,RSAVE1 CMT41740
324A 030D              4175          BR R13             ERROR RETURN     CMT41750
4176 *
4177 * *****
4178 * SUBROUTINE WAIT1
4179 * THIS ROUTINE WAITS FOR NMTN=1 UNDER TIMED CONDITION.
4180 * THE TIMEOUT PERIOD IS DESIGNED TO ACCOMODATE THE
4181 * TIME NECESSARY TO REWIND THE LONGEST TAPE. IF THE
4182 * ROUTINE TIMED OUT, THE TEST IS ABORTED WITH AN ERROR
4183 * MESSAGE .
4184 * RETURN ON R13
4185 * ERROR: 02.
4186 * *****
324C D010 3F88          4187          WAIT1 STM R1,RSAVE1
3250 0755              4188          XHR R5,R5
3252 4050 346A          4189          STH P5,EOTFLG
3256 2421              4190          LIS R2,1           SET UP LOOP COUNTER
3258 C830 7FF0          4191          LHI R3,X'7FF0'
325C 0892              4192          LHR R9,P2
325E C8A0 00FF          4193          LHI R10,X'FF'
3262 0788              4194          XHR R8,R8
3264 0711              4195          WX11 XHR R1,R1        TIME OUT LOOP
3266 9D65              4196          WX12 SSR DEV,STAT
3268 4210 32A0          4197          BTC 1,MTDU          DU?
326C C350 0010          4198          THI STAT,X'10'     NMTN = 1 ?
3270 4230 329A          4199          BNZ W1EXIT         YES EXIT
3274 C350 0020          4200          THI STAT,X'20'     EOT?
3278 2335              4201          RZS WX13
327A DE60 347F          4202          OC DEV,CLEAR       EOT - CLEAR DEVICE
327E 41F0 126A          4203          BAL R15,TSTBRK     CHECK BREAK KEY
3282 C110 3266          4204          WX13 BXLE R1,WX12
3286 C180 3264          4205          BXLE R8,WX11
328A DE60 347F          4206          OC DEV,CLEAR       TIME OUT ON NMTN
328E C800 3032          4207          LHI R0,C'02'       ERROR 02
3292 41F0 0F82          4208          BAL R15,ERRDS
3296 4300 0AE6          4209          B OPTN
329A D110 3F88          4210          W1EXIT LM R1,RSAVE1
329E 030D              4211          BR R13
4212 *
4213 * *****
4214 * DEVICE UNAVAILABLE:
4215 * RETURN TO INPUT COMMAND MODE
4216 * *****
4217 *
32A0 DE70 347E          4218          MTDU OC SELCH,STOP
32A4 D250 16D2          4219          STB STAT,ERRSTA

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## SUBROUTINES

32A8	C850 34F4	4220	LHI	R5,MSG03	MAGNETIC TAPE DEVICE UNAVAILABLE	CMT42200	
32AC	4050 16FE	4221	STH	R5,ISITERR		CMT42210	
32B0	41F0 112A	4222	BAL	R15,PRINT	PRINT MESSAGE	CMT42220	
32B4	41E0 101E	4223	BAL	RET,ERRDS1	PRINT DEVICE # AND STATUS	CMT42230	
32B8	0755	4224	XHR	R5,R5		CMT42240	
32BA	4050 16FE	4225	STH	R5,ISITERR		CMT42250	
32BE	4300 0AE6	4226	B	OPTIN		CMT42260	
		4227	* *****				CMT42270
		4228	* SUBROUTINE EOF				CMT42280
		4229	* THIS ROUTINE WRITES AN EOF				CMT42290
		4230	* CALLING SEQUENCE				CMT42300
		4231	* BAL R14,EOF				CMT42310
		4232	* *****				CMT42320
		4233	*				CMT42330
32C2	41D0 32D6	4234	EOF	BAL R13,SENMTN	CHECK FOR NMTN=1	CMT42340	
32C6	DE60 348B	4235	OC	DEV,WEOF	WRITE AN EOF	CMT42350	
32CA	030E	4236	BR	R14	NO EOF - EXIT	CMT42360	
		4237	* *****				CMT42370
		4238	* SUBROUTINE RWND				CMT42380
		4239	* THIS ROUTINE REWINDS THE TAPE				CMT42390
		4240	* CALLING SEQUENCE:				CMT42400
		4241	* BAL R14,RWND				CMT42410
		4242	* *****				CMT42420
32CC	41D0 32D6	4243	RWND	BAL R13,SENMTN	CHECK FOR NMTN=1	CMT42430	
32D0	DE60 3482	4244	OC	DEV,REWD	REWIND	CMT42440	
32D4	030E	4245	BR	R14	RETURN	CMT42450	
		4246	* *****				CMT42460
		4247	* SUBROUTINE SENMTN				CMT42470
		4248	* THIS ROUTINE WAITS FOR NMTN=1.				CMT42480
		4249	* RETURNS ON R13				CMT42490
		4250	* *****				CMT42500
32D6	9D65	4251	SENMTN	SSR DEV,STAT		CMT42510	
32D8	C350 0010	4252	THI	STAT,X'10'	NMTN=1?	CMT42520	
32DC	023D	4253	BNZR	R13	YES - RETURN	CMT42530	
32DE	41F0 126A	4254	BAL	R15,TSTBRK	CHECK BREAK KEY	CMT42540	
32E2	2206	4255	BS	SENMTN	LOOP CHECK	CMT42550	
		4256	* *****				CMT42560
		4257	* SUBROUTINE WRTBLK				CMT42570
		4258	* THIS ROUTINE WAITS FOR NMTN, AND WRITES A RECORD				CMT42580
		4259	* USING WB MODE				CMT42590
		4260	* THE STARTING & ENDING ADDRESSES OF THE RECORD ARE				CMT42600
		4261	* STORED IN R11 & R12 RESPECTIVELY				CMT42610
		4262	* *****				CMT42620
		4263	*				CMT42630
32E4	41D0 32D6	4264	WRTBLK	BAL R13,SENMTN	CHECK FOR NMTN=1	CMT42640	
32E8	DE60 3485	4265	OC	DEV,WRITE	DEVICE WRITE MODE	CMT42650	
32EC	966B	4266	WBR	DEV,R11	WRITE RECORD BLOCK MODE	CMT42660	
32EE	030E	4267	BR	R14	RETURN	CMT42670	
		4268	* *****				CMT42680
		4269	* SUBROUTINE RDBLK				CMT42690
		4270	* THIS ROUTINE READS A RECORD IN THE RB MODE. THE STARTING				CMT42700
		4271	* & ENDING ADDRESSES ARE ASSUMED TO BE IN R11 & R12				CMT42710
		4272	* RESPECTIVELY.				CMT42720



## SUBROUTINES

		4326	*	R2 CONTAINS SELCH COMMAND	*	CMT43260
		4327	*	R3 CONTAINS STARTING ADDRESS OF READ OR WRITE LIMITS	*	CMT43270
		4328	*	R4 CONTAINS DEVICE INTERRUPT RETURN ADDRESS	*	CMT43280
		4329	*	R5 CONTAINS SELCH INTERRUPT RETURN ADDRESS	*	CMT43290
		4330	*	R11 CONTAINS THE TIMEOUT ERROR NUMBER	*	CMT43300
		4331	*	RETURN ON R12	*	CMT43310
		4332	*	*****	*	CMT43320
3350	4050 196A	4333	SELINT	STH R5,DEVINT	STORE RTN ADRS FOR SELCH INTERRUPT	CMT43330
3354	07E5	4334		XHR R5,R5	RESET RETURN ADDRESS	CMT43340
3356	4050 196C	4335		STH R5,DEVINT+2	FOR DEVICE INTERRUPT	CMT43350
335A	41D0 31BE	4336		BAL R13,WAIT2	WAIT FOR NMTN=1	CMT43360
335E	4850 16C4	4337		LH R5,MOD32		CMT43370
3362	2138	4338		BNZS XMOD32		CMT43380
3364	DE70 347E	4339		OC SELCH,STOP	STOP SELCH	CMT43390
3368	D873 0000	4340		WH SELCH,0(R3)	SET UP SELCH TRANSFER LIMITS	CMT43400
336C	D873 0004	4341		WH SELCH,4(R3)		CMT43410
3370	230B	4342		RS XDEV		CMT43420
3372	DE70 3614	4343	XMOD32	OC SELCH,STOP2	STOP WITH EXTENDED ADDRESS	CMT43430
3376	DA73 0001	4344		WD SELCH,1(R3)		CMT43440
337A	D873 0002	4345		WH SELCH,2(R3)		CMT43450
337E	DA73 0005	4346		WD SELCH,5(R3)		CMT43460
3382	D873 0006	4347		WH SELCH,6(R3)		CMT43470
3386	9E61	4348	XDEV	OCR DEV,R1	OUTPUT DEVICE COMMAND	CMT43480
3388	9E72	4349		OCR SELCH,R2	OUTPUT SELCH COMMAND	CMT43490
338A	41E0 319A	4350		BAL R14,TIMEOUT	WAIT FOR SELCH INTERRUPT	CMT43500
338E	01F4	4351		DC H'500'		CMT43510
3390	DE70 347E	4352	SELINT1	OC SELCH,STOP		CMT43520
3394	07E5	4353		XHR R5,R5	RESET RETURN ADDRESS	CMT43530
3396	4050 196A	4354		STH R5,DEVINT	FOR SELCH INTERRUPT	CMT43540
339A	4040 196C	4355		STH R4,DEVINT+2	STORE DEVICE INTERRUPT RETURN ADRS	CMT43550
339E	030C	4356		BR R12	RETURN	CMT43560
		4357	*	*****	*	CMT43570
		4358	*	SUBROUTINE SKIPINT	*	CMT43580
		4359	*	THIS ROUTINE TESTS SKIP INTERRUPTS ON FORWARD OR	*	CMT43590
		4360	*	BACKWARD SKIPS.	*	CMT43600
		4361	*	ASSUMPTIONS:	*	CMT43610
		4362	*	R1 CONTAINS THS SKIP COMMAND	*	CMT43620
		4363	*	R11 CONTAINS THE TIMEOUT ERROR NUMBER	*	CMT43630
		4364	*	RETURN ON R12	*	CMT43640
		4365	*	*****	*	CMT43650
33A0	0788	4366	SKIPINT	XHR R8,R8		CMT43660
33A2	C850 33C0	4367		LHI R5,RTN11	SET UP RETURN ADDRESS	CMT43670
33A6	4050 196C	4368		STH R5,DEVINT+2		CMT43680
33AA	41D0 31BE	4369	SKIPINT1	BAL R13,WAIT2		CMT43690
33AE	DE60 348A	4370		OC DEV,DISARM	DISARM DEVICE	CMT43700
33B2	DE60 3489	4371		OC DEV,ENABL		CMT43710
33B6	9E61	4372		OCR DEV,R1	OUTPUT SKIP COMMAND	CMT43720
33B8	41E0 319A	4373		BAL R14,TIMEOUT		CMT43730
33BC	07D0	4374		DC H'2000'		CMT43740
33BE	230A	4375		BS STA11		CMT43750
33C0	D350 16D2	4376	RTN11	LB STAT,INTSTA	GET INTERRUPT STATUS	CMT43760
33C4	C5E0 004C	4377		CLHI STAT,X'4C'		CMT43770
33C8	2335	4378		BES STA11		CMT43780

## SUBROUTINES

33CA	C800	3037	4379	LHI	R0,C'07'	ERR0R 07	CMT43790
33CE	4300	1FB2	4380	B	STAERR		CMT43800
33D2	2681		4381	STA11	AIS	R8,1	CMT43810
33D4	C500	0002	4382		CLHI	R8,2	CMT43820
33D8	4280	33AA	4383		BL	SKIPINT1	CMT43830
33DC	03CC		4384		BP	R12	CMT43840
			4385	*	*****		CMT43850
			4386	*			CMT43860
			4387	*	SUBROUTINES SKFW & SKRV		CMT43870
			4388	*	THIS ROUTINE SKIPS A FILE PASS AN EOF		CMT43880
			4389	*	*****		CMT43890
			4390	*			CMT43900
33DE	41D0	32D6	4391	SKFW	BAL	R13,SENMTN	CMT43910
33E2	DE60	3486	4392		CC	DEV,SKIPP	CMT43920
33E6	030E		4393		BR	R14	CMT43930
33E8	41D0	32D6	4394	SKRV	BAL	R13,SENMTN	CMT43940
33EC	DE60	3487	4395		CC	DEV,SKIPR	CMT43950
33FO	03CE		4396		BR	R14	CMT43960
			4397	*	*****		CMT43970
			4398	*	SUBROUTINE ERRDSA SAVES THE EPROR NUM (R0) AND THE STATUS		CMT43980
			4399	*	BYTE (STAT) FOR USE BY ERRDS		CMT43990
			4400	*	*****		CMT44000
			4401	*			CMT44010
33F2	4000	173E	4402	ERRDSA	STH	R0,ERRNO	CMT44020
33F6	D250	16D2	4403		STB	STAT,ERRSTA	CMT44030
33FA	030E		4404		BR	RET	CMT44040
			4405	*	*****		CMT44050
			4406	*	SUBROUTINE REWIND WAITS FOR NMTN=1, REWINDS THE TAPE, AND		CMT44060
			4407	*	WAITS FOR NMTN=1 AGAIN.		CMT44070
			4408	*	*****		CMT44080
			4409	*			CMT44090
33FC	41D0	324C	4410	REWIND	BAL	R13,WAIT1	CMT44100
3400	DF60	3482	4411		CC	DEV,REWD	CMT44110
3404	41D0	324C	4412		BAL	R13,WAIT1	CMT44120
3408	03CE		4413		BR	RET	CMT44130
			4414	*	*****		CMT44140
			4415	*			CMT44150
			4416	*	ROUTINES TO CHECK VALID OPTION VALUES		CMT44160
			4417	*	*****		CMT44170
			4418	*			CMT44180
340A	C360	FFFE	4419	ZERONE	THI	R6,X'FFFE'	CMT44190
340E	033F		4420		BR	R15	CMT44200
3410	030C		4421		BP	R12	CMT44210
3412	C560	0009	4422	TRACKS	CLHI	R6,9	CMT44220
3416	033F		4423		BR	R15	CMT44230
3418	C560	0007	4424		CLHI	R6,7	CMT44240
341C	033F		4425		BR	R15	CMT44250
341E	030C		4426		BP	R12	CMT44260
3420	C560	0003	4427	MODES	CLHI	R6,3	CMT44270
3424	028F		4428		BLR	R15	CMT44280
3426	030C		4429		BP	R12	CMT44290
3428	C560	0100	4430	X256	CLHI	R6,X'100'	CMT44300
342C	028F		4431		BLR	R15	CMT44310

## SUBROUTINES

342E	030C	4432	BR	R12		CMT44320
3430	C560 0002	4433	MIN2	CLHI	R6,2	CMT44330
3434	028C	4434		BLR	R12	CMT44340
3436	2301	4435		BS	X3FF	CMT44350
3439	4560 3612	4436	X3FF	CLH	R6,X400	CMT44360
343C	028F	4437		BLR	R15	CMT4437C
343E	030C	4438		BR	R12	CMT44380
3440	0866	4439	DEVCHN	LHR	R6,R6	CMT44390
3442	2235	4440		BZS	X3FF	CMT44400
3444	0755	4441		XHR	R5,P5	CMT44410
3446	2207	4442		BS	X3FF	CMT44420
3448	C560 0006	4443	SCOP	CLHI	R6,6	CMT44430
344C	028F	4444		BLR	R15	CMT44440
344E	030C	4445		BR	R12	CMT44450
3450	C560 0005	4446	LEVEL	CLHI	R6,5	CMT44460
3454	038C	4447		BNLR	R12	CMT44470
3456	D260 1966	4448		STB	R6,INTLVL	CMT44480
345A	D260 1967	4449		STB	R6,INTLVL+1	CMT44490
345E	D260 1968	4450		STB	R6,INTLVL+2	CMT44500
3462	030F	4451		BR	R15	CMT44510
		4452	*****			CMT44520

		4454	*	*****			CMT44540
		4455	*	CONSTANTS		*	CMT44550
		4456	*	*****		*	CMT44560
		4457	*			*	CMT44570
3454	FFFF	4458	MASK	DC	I'FFFF'		CMT44580
3466	00FF	4459	NBYTE	DC	I'FF'		CMT44590
345B	0000	4460	DE	DC	0		CMT44600
345A	0000	4461	EOTFLG	DC	0		CMT44610
345C	0000	4462	ERRFLG	DC	0		CMT44620
345E	0000	4463	MODFLG	DC	0		CMT44630
3470	0000	4464	RTYCMT	DC	0		CMT44640
3472	0000	4465	NXTDEV	DC	0		CMT44650
3474	0000	4466	DEV2	DC	0		CMT44660
3476	0000	4467	WLR5	DC	0		CMT44670
3473	0000	4468	DEVONE	DC	0		CMT44680
347A	2929	4469	CRCC	DC	I'2929'		CMT44690
347C	6A29	4470	CRCCS	DC	I'6A29'		CMT44700
347E	0820	4471	STOP	DC	I'0820'		CMT44710
	0000 347F	4472	CLEAR	EQU	STOP+1		CMT44720
3480	5070	4473	GOWRT	DC	I'5070'		CMT44730
	0000 3481	4474	GORD	EQU	GOWRT+1		CMT44740
3482	3811	4475	REWD	DC	I'3811'		CMT44750
	0000 3483	4476	BKSPAC	EQU	REWD+1		CMT44760
3484	2122	4477	READ	DC	I'2122'		CMT44770
	0000 3485	4478	WRITE	EQU	READ+1		CMT44780
3486	2313	4479	SKIPF	DC	I'2313'		CMT44790
	0000 3487	4480	SKIPR	EQU	SKIPF+1		CMT44800
3488	8040	4481	DSABL	DC	I'8040'		CMT44810
	0000 3489	4482	ENABL	EQU	DSABL+1		CMT44820
348A	C030	4483	DISARM	DC	I'C030'		CMT44830
	0000 348B	4484	WEOF	EQU	DISARM+1		CMT44840
348C	00FF	4485	WDATA	DC	I'00FF',X'00FF'		CMT44850
348E	00FF						
3490	00FF	4486		DC	I'00FF',X'00FF'		CMT44860
3492	00FF						
3494	0102	4487		DC	I'0102',X'0408'		CMT44870
3496	0408						
3498	1020	4488		DC	I'1020',X'4080'		CMT44880
349A	4080						
349C	7FFB	4489		DC	I'7FFB',X'DFEF'		CMT44890
349E	DFEF						
34A0	F7FB	4490		DC	I'F7FB',X'FDFF'		CMT44900
34A2	FDFF						
34A4	AA55	4491		DC	I'AA55',X'AA55'		CMT44910
34A6	AA55						
34A8	AA55	4492		DC	I'AA55',X'AA55'		CMT44920
34AA	AA55						
34AC	F00F	4493		DC	I'F00F',X'F00F'		CMT44930
34AE	F00F						
34B0	F00F	4494		DC	I'F00F',X'F00F'		CMT44940
34B2	F00F						
34B4	0000	4495		DC	0		CMT44950
34B6	0D16	4496	SQMASK	DC	X'0D16',X'0102',X'0506'		CMT44960
34B8	0102						
34BA	0506						

34BC	4441 5441 2020 2020	4497	MSG01A	DC	C'DATA	DATA',X'D00'	CMT44970
34C4	2020 4441 5441						
34CA	0D00						
34CC	5752 4954 5445 4E20	4498	MSG01B	DC	C'WRITTEN	READ',X'D00'	CMT44980
34D4	2020 5245 4144						
34DA	0D00						
34DC	5245 434F 5645 5259	4499	MSG02	DC	C'RECOVERY UNSUCCESSFUL',X'D00'		CMT44990
34E4	2055 4E53 5543 4345						
34EC	5353 4655 4C20						
34F2	0A0D						
34F4	4445 5649 4345 204F	4500	MSG03	DC	C'DEVICE OFF-LINE',X'D00'		CMT45000
34FC	4646 2D4C 494E 4520						
3504	0D00						
3506	454F 5420	4501	MSG04	DC	C'EOT',X'D00'		CMT45010
350A	0D00						
350C	454F 4620	4502	MSG04A	DC	C'EOF',X'D00'		CMT45020
3510	0D00						
3512	4144 4420 4352 4320	4503	MSG05	DC	C'ADD CRC CAPACITOR AND EXECUTE',X'D00'		CMT45030
351A	4341 5041 4349 544F						
3522	5220 414E 4420 4558						
352A	4543 5554 4520						
3530	0D00						
3532	4352 4320 4348 4152	4504	MSG06	DC	C'CRC CHAR =	',X'D00'	CMT45040
353A	203D 2020 2020 2020						
3542	0D00						
3544	4352 4320 4348 4152	4505	MSG07	DC	C'CRC CHAR EXPT'D =	, READ = ',X'D00'	CMT45050
354C	2045 5850 5427 4420						
3554	3D20 2020 2020 2C20						
355C	5245 4144 203D 2020						
3564	2020 2020						
3568	0D00						
356A	4D4F 4445 2020	4506	MSG08	DC	C'MODE	',X'D00'	CMT45060
3570	0D00						
3572	454E 5445 5220 4441	4507	MSG09	DC	C'ENTER DATA:',X'D00'		CMT45070
357A	5441 3A20						
357E	0D00						
3580	5455 524E 2044 4556	4508	MSG10	DC	C'TURN DEVICE OFF-LINE MOMENTARILY.',X'D00'		CMT45080
3588	4943 4520 4F46 462D						
3590	4C49 4E45 204D 4F4D						
3598	454E 5441 5249 4C59						
35A0	2F20						
35A2	0D00						
35A4	4552 524F 523A 2052	4509	LABEL	DC	C'ERROR: READ BUFFER IN TEST PROGRAM'		CMT45090
35AC	4541 4420 4255 4646						
35B4	4552 2049 4E20 5445						
35BC	5354 2050 524F 4752						
35C4	414D						
35C6	0D00	4510		DC	X'D00'		CMT45100
35C8	4552 524F 523A 2057	4511	LABELL	DC	C'ERROR: WRITE BUFFER IN TEST PROGRAM'		CMT45110
35D0	5249 5445 2042 5546						
35D8	4645 5220 494E 2054						
35E0	4553 5420 5052 4F47						
35E8	5241 4D20						
35EC	0D00	4512		DC	X'D00'		CMT45120
35EE	4552 524F 523A 2052	4513	LAAABEL	DC	C'ERROR: READ BUFFER IN WRITE BUFFER'		CMT45130



35F6	4541 4420 4255 4646							
35FE	4552 2049 4E20 5752							
3606	4954 4520 4255 4646							
360E	4552							
3610	0D00	4514	DC	X'D00'				CMT45140
		4515	*	ALL TEST PROGRAM STORAGE AREA				CMT45150
		4516	*					CMT45160
		4517	*					CMT45170
3612	0401	4518	X400	DC	X'401'			CMT45180
3614	4800	4519	STOP2	DC	X'4800'			CMT45190
3618		4520		ALIGN	8			CMT45200
3618	0000 0000	4521	W LIM	DCY	0			CMT45210
361C	0000 0000	4522		DCY	0			CMT45220
3620	0000 0000	4523	R LIM	DCY	0			CMT45230
3624	0000 0000	4524		DCY	0			CMT45240
3628	0000 0000	4525	RSAY32	DCY	0			CMT45250
362C	0000 0000	4526	WADDRS	DCY	0			CMT45260
3630	0000 0000	4527	RADDRS	DCY	0			CMT45270
3634	0000 0000	4528	MEMTOP	DCY	0			CMT45280
3638	3FFF	4529	LAST	DC	X'3FFF'			CMT45290
	0000 3639	4530	LNZB	EQU	*-1			CMT45300
363A		4531	WBUF	DS	X'400'			CMT45310
3A3A		4532	RBUF	DS	X'402'			CMT45320
3E3C		4533	TEMP	ES	2	TEMPORARY STORAGE LOC		CMT45330
3E40		4534		ALIGN	8			CMT45340
3E40		4535	OPTBUF	DS	6	OPTION INPUT BUFFER		CMT45350
3E46		4536	IOSAVE	DS	2			CMT45360
3E48		4537	INTSAV	DS	64	REGISTERS CN EXT/INT INTERRUPT		CMT45370
3E88		4538	SAVERTN	DS	2			CMT45380
3E8A		4539	ORG	X'3F00'				CMT45390
3F00	0000 0000	4540	PSWSAVE	DCY	0,0			CMT45400
3F04	0000 0000							
3F08		4541	RSAVE	DS	128			CMT45410
3F88		4542	RSAVE1	DS	64			CMT45420
3FC8		4543	ERRSAVE	DS	64			CMT45430
		4544	*					CMT45440

## CHKSUM/M17 PUNCHER

		4546	**CHKSUM				CMT45460
		4547	* START OF CHKSUM FILE				CMT45470
		4548	*				CMT45480
		4549	*				CMT45490
		4550	*				CMT45500
4008	2400	4551	SCHKSUM	LIS	R0,0	PUNCH M17 TAPE WITH CHECKSUM	CMT45510
400A	9510	4552		EPSR	R1,R0	SELECT REG. SET 0	CMT45520
		4553	*				CMT45530
400C	C810 0A00	4554		LDAI	R1,ORIGIN1	START	CMT45540
4010	2421	4555		LIS	R2,1	INCREMENT	CMT45550
4012	C830 3639	4556		LDAI	R3,LNZB	FINAL	CMT45560
4016	2440	4557		LIS	R4,0	CHECKSUM BYTE	CMT45570
4018	D351 0000	4558	SGEN	LB	R5,0(R1)		CMT45580
401C	0745	4559		XAR	R4,R5		CMT45590
401E	C110 4018	4560		BXLE	R1,SGEN		CMT45600
4022	D240 0099	4561		STB	R4,MN+3	CHECKSUM BYTE TO BOOT LOADER	CMT45610
		4562	*				CMT45620
4026	C810 0080	4563	STAFF	LHI	R1,X'0080'		CMT45630
402A	9E21	4564		OCR	R2,R1	DISPLAY : NORMAL MODE	CMT45640
402C	9444	4565		EXBR	R4,R4		CMT45650
402E	9824	4566		WHR	R2,R4	CHECKSUM BYTE TO D1	CMT45660
4030	9411	4567		EXBR	R1,R1		CMT45670
4032	9501	4568		EPSR	R0,R1	HALT PROCESSOR.	CMT45680
4034	D360 007A	4570	SPUNCH	LB	R6,X'7A'	GET BOUTDV (PUNCH) ADDRESS.	CMT45700
4038	DE60 007B	4571		OC	R6,X'7B'	START TAPE PUNCH	CMT45710
403C	9D60	4572		SSR	R6,R0		CMT45720
403E	2081	4573		BTBS	8,1		CMT45730
4040	41F0 4082	4574		BAL	R15,STAPL	PUNCH LEADER	CMT45740
4044	9411	4575		EXBR	R1,R1	(R1) = X'0080'	CMT45750
4046	C830 00CF	4576		LHI	R3,X'CF'		CMT45760
404A	DA61 0000	4577	SPNCH1	WD	R6,0(R1)	PUNCH BOOT LOADER	CMT45770
404E	9D60	4578		SSR	R6,R0		CMT45780
4050	2081	4579		BTBS	8,1		CMT45790
4052	C110 404A	4580		BXLE	R1,SPNCH1		CMT45800
4056	41F0 4088	4581		BAL	R15,STAPL1	PUNCH ONE-FOLD GAP.	CMT45810
		4582	*				CMT45820
405A	D340 0099	4583		LB	R4,MN+3	GET CHECKSUM BYTE	CMT45830
405E	C810 0A00	4584		LDAI	R1,ORIGIN1	(NORMALLY X'A00')	CMT45840
4062	C830 3639	4585		LDAI	R3,LNZB		CMT45850
4066	D351 0000	4586	SPNCH2	LB	R5,0(R1)	PUNCH PROGRAM	CMT45860
406A	0745	4587		XAR	R4,R5		CMT45870
406C	9A65	4588		WDR	R6,R5		CMT45880
406E	9401	4589		EXBR	R0,R1		CMT45890
4070	9820	4590		WHR	R2,R0	DATA ADDRESS TO DISPLAY.	CMT45900
4072	9D60	4591		SSR	R6,R0		CMT45910
4074	2081	4592		BTBS	8,1		CMT45920
4076	C110 4066	4593		BXLE	R1,SPNCH2		CMT45930
407A	41F0 4082	4594		BAL	R15,STAPL	PUNCH TRAILER.	CMT45940
407E	4300 4026	4595		B	STAPE	DISPLAY CHECKSUM, HALT PROCESSOR.	CMT45950

## CHKSUM/M17 PUNCHER

4382	C800 0100	4597	STAPL	LHI	R0,256	TO PUNCH BLANK LEADER	CMT45970
4386	2303	4598		BS	STAPLP		CMT45980
4388	C800 0055	4599	STAPL1	LHI	R0,85	TO PUNCH 1-FOLD GAP	CMT45990
438C	2701	4600	STAPLP	SIS	R0,1		CMT46000
438E	032F	4601		BNPR	R15	RETURN	CMT46010
4390	2430	4602		LIS	R3,0		CMT46020
4392	9A63	4603		WDF	R6,R3	PUNCH BLANK FRAME	CMT46030
4394	9D68	4604		SSE	R6,R8		CMT46040
4396	2081	4605		BTBS	8,1		CMT46050
4398	2206	4606		BS	STAPLP	CONTINUE.	CMT46060
439A		4607	*				CMT46070
		4608		END			CMT46080























## CHKSUM/R17 PUNCHER

		1801	1840	1842	1915	1946	2013	2018	2024	2024	2025	2047	2055	2061
		2427	2430	2489	2497	2803	2813	2904	2924	2927	3053	3056	3063	3065
		3072	3081	3172	3173	3175	3307	3482	3483	3495	3498	3503	3517	3526
		3556	3557	3568	3579	3604	3605	3508	3609	3610	3611	3628	3633	3636
		3639	3685	3713	3713	3714	3715	3717	3730	3730	3731	3731	3732	3803
		3815	3826	3849	3979	4014	4126	4132	4132	4138	4144	4159	4165	4165
		4170	4174	4187	4195	4195	4204	4210	4299	4302	4312	4343	4372	4552
		4554	4558	4560	4563	4564	4567	4567	4568	4575	4575	4577	4580	4584
		4586	4589	4593										
R10	0000 000A	86*	1194	1194	1195	1214	1232	1233	1234	1260	1260	1261	1323	1323
		1335	1336	1785	1912	2022	2180	2555	2662	2663	2679	2694	2709	2806
		2814	2864	3030	3031	3032	3135	3144	3637	3703	3783	3805	3814	3828
		4130	4163	4193										
R11	0000 000B	87*	1787	1787	1789	1840	2159	2206	2217	2239	2248	2257	2288	2294
		2298	2332	2353	2389	2395	2397	2428	2431	2459	2477	2494	2502	3151
		3191	3215	3440	3440	3444	3447	3450	3451	3453	3488	3506	3515	3562
		3571	3577	3645	3648	3652	3654	3656	4109	4266	4277	4304		
R12	0000 000C	88*	253	268	277	287	432	435	445	462	499	753	1685	1686
		1684	1690	1691	1695	1695	1697	1698	1698	1699	1700	1701	1702	1703
		1704	1705	1815	1820	1923	1928	2014	2055	2290	2391	2429	2432	2495
		2503	2558	2563	2681	2696	2701	2706	2712	2817	2827	2867	2886	3054
		3073	3153	3193	3217	3441	3445	3448	3449	3452	3453	3493	3497	3499
		3511	3524	3530	3531	3573	3583	4305	4356	4384	4421	4426	4429	4432
		4434	4438	4445	4447									
R13	0000 000D	89*	1770	1772	1776	1829	1844	1908	1937	1947	2013	2036	2042	2049
		2068	2147	2181	2194	2220	2273	2292	2301	2319	2329	2356	2370	2377
		2381	2393	2400	2435	2473	2480	2573	2578	2684	2690	2703	2819	2873
		2932	2936	3029	3051	3057	3061	3068	3070	3082	3091	3095	3097	3107
		3109	3219	3266	3278	3330	3424	3426	3472	3507	3509	3525	3548	3549
		3641	3712	3724	3726	3728	3815	3873	3895	3926	3943	3950	3981	3991
		4000	4062	4065	4080	4125	4145	4158	4175	4211	4234	4243	4253	4264
		4275	4286	4298	4336	4369	4391	4394	4410	4412				
R14	0000 000E	90*	307	343	433	436	438	460	465	469	775	778	784	1277
		1297	1300	1316	1326	1331	1334	1336	1337	1768	1769	1771	1774	1778
		1786	1819	1824	1827	1835	1849	1852	1855	1857	1858	1861	1862	1865
		1906	1907	1909	1910	1922	1927	1932	1935	1942	1952	1955	1957	1958
		1961	1962	1965	2005	2006	2008	2009	2010	2020	2038	2044	2051	2060
		2066	2071	2072	2075	2081	2082	2144	2145	2160	2177	2178	2193	2207
		2218	2240	2249	2258	2299	2309	2317	2333	2354	2398	2413	2416	2419
		2460	2478	2542	2543	2545	2548	2549	2562	2566	2567	2571	2583	2584
		2594	2596	2597	2606	2607	2657	2658	2660	2664	2687	2688	2689	2692
		2698	2700	2708	2711	2714	2791	2794	2795	2802	2823	2852	2853	2862
		2863	2871	2882	2940	3027	3028	3035	3036	3037	3039	3050	3052	3064
		3077	3080	3103	3105	3117	3127	3143	3157	3162	3163	3164	3174	3184
		3197	3202	3224	3242	3254	3260	3268	3277	3287	3315	3316	3345	3362
		3400	3408	3410	3411	3429	3432	3455	3716	3770	3793	3850	3889	3881
		3896	3946	3952	3974	3975	4015	4105	4112	4235	4245	4267	4278	4288
		4320	4350	4373	4393	4395								
R15	0000 000F	92*	239	259	438	444	485	485	486	752	753	754	756	764
		765	813	889	995	997	1278	1298	1301	1317	1327	1332	1338	1378
		1568	1590	1590	1621	1621	1713	1793	1794	1805	1807	2033	2157	2158
		2228	2270	2286	2287	2288	2289	2290	2291	2368	2387	2388	2389	2390
		2391	2392	2469	2550	2551	2552	2553	2554	2585	2586	2587	2588	2589

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		2668	2669	2670	2682	2707	2713	2720	2723	2724	2726	2728	2730	2796
		2797	2798	2799	2800	2808	2809	2810	2811	2843	2854	2855	2856	2857
		2858	2876	2889	2890	2891	2892	2905	2907	2925	2928	2930	2934	2939
		2946	3138	3149	3150	3151	3152	3153	3154	3166	3189	3190	3191	3192
		3193	3194	3213	3214	3215	3216	3217	3218	3233	3234	3262	3271	3290
		3335	3336	3337	3338	3339	3340	3341	3342	3343	3344	3378	3381	3413
		3431	3442	3443	3444	3445	3446	3504	3505	3506	3508	3511	3512	3569
		3570	3571	3572	3573	3574	3578	3614	3667	3672	3689	3690	3691	3692
		3693	3694	3695	3696	3697	3701	3702	3703	3704	3705	3706	3710	3721
		3733	3739	3744	3745	3758	3759	3760	3762	3763	3764	3765	3767	3768
		3769	3787	3788	3789	3790	3804	3805	3908	3809	3811	3832	3833	3834
		3835	3836	3838	3839	3840	3845	3847	3848	3890	3893	3925	3945	3951
		3982	3987	3996	4004	4005	4006	4007	4008	4013	4016	4017	4018	4019
		4020	4023	4024	4025	4026	4027	4028	4029	4030	4031	4064	4076	4079
		4104	4106	4111	4137	4142	4169	4173	4203	4209	4222	4254	4315	4420
		4423	4425	4428	4431	4437	4444	4451	4574	4581	4594	4601		
R2	0000 0002	78*	102	122	128	177	178	180	181	187	189	198	203	205
		207	208	212	213	214	221	222	223	224	231	231	232	246
		247	304	305	309	309	310	312	313	319	322	344	351	389
		393	395	400	416	417	524	527	528	535	537	538	539	540
		542	544	545	554	555	556	602	603	606	608	609	619	647
		651	652	657	663	668	676	681	682	706	716	726	730	740
		743	793	905	806	808	810	814	829	830	843	969	970	973
		982	986	989	989	1063	1115	1117	1117	1118	1129	1130	1132	1133
		1140	1141	1146	1153	1154	1156	1159	1167	1171	1172	1188	1189	1196
		1210	1211	1213	1219	1272	1283	1284	1292	1293	1295	1303	1311	1312
		1314	1324	1325	1329	1346	1350	1351	1353	1355	1356	1357	1358	1359
		1361	1362	1364	1367	1368	1371	1372	1373	1563	1782	1800	1913	2011
		2490	2498	2902	2922	2926	3033	3170	3200	3240	3301	3306	3307	3484
		3558	3612	3621	3651	3736	3736	3740	3741	3829	3829	3834	3841	3843
		3985	3985	4009	4010	4021	4021	4025	4032	4034	4034	4036	4036	4127
		4129	4160	4162	4190	4192	4313	4349	4555	4564	4566	4590		
R3	0000 0003	79*	107	108	109	199	200	203	219	220	232	285	285	289
		293	295	306	319	344	392	396	466	470	634	637	782	785
		785	794	822	823	824	826	831	855	857	1064	1069	1073	1076
		1077	1080	1081	1082	1083	1093	1094	1107	1108	1114	1118	1124	1130
		1133	1134	1141	1146	1147	1154	1160	1188	1197	1564	1783	1841	1914
		2012	2023	2025	2027	2048	2491	2499	2825	2894	2890	2896	3034	3171
		3201	3241	3485	3559	3615	4129	4161	4191	4316	4340	4341	4344	4345
		4346	4347	4556	4576	4585	4602	4603						
R4	0000 0004	80*	111	112	113	115	123	125	226	227	223	229	229	249
		251	260	262	263	265	272	274	278	310	315	320	321	324
		327	332	334	335	335	337	338	339	340	359	364	374	379
		393	398	407	410	412	419	431	434	449	471	635	754	766
		774	776	809	810	811	812	812	825	826	827	828	828	829
		851	851	852	853	854	865	867	871	873	884	920	930	931
		936	933	939	947	948	1035	1037	1078	1079	1085	1092	1104	1105
		1106	1132	1140	1143	1143	1156	1162	1167	1172	1562	1565	1567	1757
		1905	2004	2143	2169	2170	2172	2173	2195	2195	2198	2199	2492	2500
		2541	2656	2790	3026	3136	3137	3141	3142	3173	3175	3301	3302	3304
		3305	3356	3487	3561	3647	3735	3830	3973	3975	3977	3978	4355	4557
		4559	4561	4565	4565	4566	4583	4587						
R5	0000 0005	81*	113	115	116	116	118	119	120	123	125	131	238	287





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			1592	1593	1593	1595	1596	1596	1598	1599	1601	1603	1603	1605	1606
			1608	1610	1614	1617	1619	1625	1784	1911	2021	2179	2556	2661	2678
			2805	2815	2865	3686	3782	3806	3927	3983	4129	4162	4192	4300	
RABEND	0000	2D52	3579*	3622											
RADDRS	0000	3630	1385	1397	1398	1573	1577	1579	1584	3340	4527*				
RBUFF	0000	3A3A	1576	1583	1586	1813	2896	4532*							
RCONLY	0000	26C2	2860	2880*											
RCOVR	0000	1CA0	1853	1857*											
RCOVR1	0000	1D76	1953	1957*											
RCOVR2	0000	1E80	2067	2071*											
RCOVR6	0000	2788	2935	2939*											
RDBLK	0000	32F0	3197	3224	4275*										
RDBMD	0000	2D2C	3552	3568*											
RDCHAR0	0000	0B26	261	263*											
RDCHR	0000	0B18	259*	271	280										
RDCHR1	0000	0B44	266	272*											
RDCON	0000	29E8	3126	3213*	3238										
RDCONB	0000	2A10	3224*	3231											
RDCONS	0000	2A48	3223	3240*	3249										
RDCRC	0000	18EE	1526*	2859											
RDEND	0000	2226	2411	2414*											
RDER0	0000	1CB0	1821	1861*											
RDER1	0000	1D86	1929	1961*											
RDER21	0000	1E9C	2057	2078*											
RDER4	0000	2410	2564	2600*											
RDER6	0000	2798	2887	2943*											
RDER71	0000	28DC	3074	3112*											
RDFIL6	0000	26D8	2886*	2913											
RDFIL7	0000	2854	3072*	3089											
RDON	0000	2872	3079	3081*											
RDONLY	0000	21A4	2185	2377*											
RDREC	0000	2CF4	1820	1928	2056	2563	2706	2712	2827	2886	3073	3549*			
READ	0000	3484	1777	2050	2378	2394	2497	2822	3071	3083	3200	3220	3240	3267	3558
			3576	4276	4477*	4478									
RECFIL	0000	1852	1513*	1785	1912	2012	2048	2180	2555	2864	3034				
REOF01	0000	1B98	1776*	1850											
REOF21	0000	1E3C	2049*	2076											
REPEAT	0000	1876	1516*	2023											
REPEAT0	0000	1DF0	2026	2028*											
RERD2	0000	1E6A	2039	2045	2062*	2080									
PERDR	0000	1C2A	1820*	1863											
PERDR1	0000	1D18	1928*	1963											
PERDR21	0000	1E54	2056*	2061	2083										
PERDR4	0000	238E	2563*	2568	2608										
PERDR7	0000	2856	3073*	3081											
RESET	0000	2F0E	1786	1922	2009	2177	2548	2584	2664	2687	2795	2853	3035	3757*	
PET	0000	000E	91*	648	658	659	662	664	665	669	670	671	700	710	720
			734	747	1306	1673	1681	1833	1854	1940	1954	2007	2028	2168	2421
			2434	2544	2577	2582	2659	2686	2705	2801	2821	2833	2861	2880	3040
			3090	3373	4223	4404	4413								
RETOPSW	0000	1498	1136	1144	1157	1176*	1455	3119	3303						
RETOPSW1	0000	14A6	1179	1183*											
RETRY	0000	304A	1849	1858	1862	1865	1958	1962	1965	2072	2075	2082	2567	2597	2607









