

Inter-Office Memorandum

To Pilot Design Spec Contributors Date January 10, 1978

From Dave Redell Location Palo Alto

Subject Results of second meeting on the Design Spec. Organization SDD/SD

XEROX

Filed on: <Redell>PilotDS3.memo

Our second meeting on January 5 assessed the progress so far and established new goals for getting the various sections submitted for integration. Some modifications to the Table of Contents were adopted and several authorship assignments were revised:

20 January: Sections submitted for integration into first draft.

31 January: First draft of integrated document distributed.

Some modifications to the Table of Contents were adopted and several authorship assignments were revised.

5.0 Internal Interfaces (6.0 Algorithms and Databases)

- 5.1 **Low-level Mesa support** [Lauer]
To be converted from a subset of the OIS Mesa Functional Spec, in conjunction with Mesa group.
- 5.2 **Input/Output Control System** [Jarvis]
Current draft complete and available for review; comments solicited.
- 5.3 **Input/Output Drivers**
 - Disks: rigid & local floppy** [Jarvis]
Current draft complete and available for review; comments solicited.
 - X-wire & sockets** [Dalal]
Exists as notes plus a prototype implementation; a prose description being written.
 - RS232** [Schwartz]
Current draft complete and available for review; comments solicited.

- Tapes: cassette & 9-track** [Jarvis]
Magnetic cards [Jarvis]
Printer [Jarvis]
Display/keyboard/mouse [Jarvis]
Data Line Scanner [Jarvis]
Serial Interface [Jarvis]
The six items above are all "in limbo", pending clarification of requirements; Pitts will keep an eye on them.
- 5.4 Dynamic Firmware Loading Facility** [Hankins]
Meetings with Peter Deutsch and others have raised questions of generality vs. simplicity; these should be resolved soon, and the resulting compromise documented.
- 5.5 File Page Transfer Facility** [Redell]
Very little has been written here; Steve Purcell and Paul McJones will help with the file sections, and one of them may assume primary responsibility.
- 5.6 Virtual Memory Swapper** [McJones]
Substantial fragments of this section exist; the issues seem clear enough to proceed with a full draft. (NOTE: the Relocated Memory Manager, formerly section 5.1, has been subsumed by the swapper.)
- 5.7 File Manager** [Redell]
Nothing has been written here; Steve Purcell and Paul McJones will help with the file sections, and one of them may assume primary responsibility.
- 5.8 Virtual Memory Manager** [McJones]
Substantial fragments of this section exist; the issues seem clear enough to proceed with a full draft.
- 5.9 Streams** [Lauer]
Most of the design exists as a collection of memos; conversion to design-spec format is under way.
- 5.10 Clocks & timers** [Lauer]
This section is somewhat in limbo due to lack of clear requirements; Hugh will keep an eye on it.
- 5.11 Software/Firmware Bootload and Initialization** [Hankins]
This section is somewhat in limbo due to lack of clear requirements; Hugh will keep an eye on it.

5.12 'Install' Facility

This section is a complete unknown; no one currently has responsibility for it.

[unassigned]

5.13 Hardware Roll Call

The roll-call is essentially part of IOCS initialization; not all details are clear, but Pitts will think about it.

[Jarvis]

**5.14 Error Logging
& Auto-reboot**

This section is somewhat in limbo due to lack of clear requirements; Bill will keep an eye on it for now.

[Lynch?]