

IBM posts SPEC CPU2000 and SPEC CPU2006 scores for new quad-core single-socket system

February 13, 2007 ... IBM® System x™ 3200 combines leading industry-standard technologies, excellent internal data storage capacity, availability, and basic systems management and control features, into an attractively priced entry server. This uniprocessor server provides solid performance to support general-purpose network infrastructure, retail store, or e-mail and messaging applications. (1)

The x3200 used one Quad Core Intel® Xeon® Processor X3210 (2.13GHz, 8MB L2 cache, and 1066 MHz front-side bus—1 processor/4 cores/4 threads) to achieve new scores on the SPEC CPU2000 and SPEC CPU2006 benchmark suites. These results were achieved using Microsoft® Windows® Server 2003 Enterprise x64 Edition with SP1 (64-bit).

The scores in the following tables are the first SPEC CPU2000 and SPEC CPU2006 results published for this processor model.

SPEC CPU2000 Benchmark	x3200 – Quad-Core Intel Xeon Processor X3210 (2.13GHz, 8MB L2 Cache, 1066MHz FSB)
SPECint_base	2,270
SPECint_rate_base2000	89.73
SPECfp_base	2,268
SPECfp_rate_base2000	59.35

SPEC CPU2006 Benchmark	x3200 – Quad-Core Intel Xeon Processor X3210 (2.13GHz, 8MB L2 Cache, 1066MHz FSB)
SPECint_base2006	13.7
SPECint_rate_base2006	40.4
SPECfp_base2006	12.6
SPECfp_rate_base2006	31.6

CPU2000 was released six years ago. Since then, advances in technology and the resulting improvements in hardware and software have made it necessary to ensure that the benchmarks are also improved so that they keep pace. SPEC CPU2006 is designed to measure more technologically advanced systems; hence, these results should not be compared with CPU2000 results.

Results are current as of February 13, 2007. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at www.spec.org.

(1) Planned availability for the x3200 model using the Quad Core Intel Xeon Processor X3210 (2.13GHz, 8MB L2 cache, and 1066 MHz front-side bus) is February 28, 2007.

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