

IBM posts SPEC CPU2006 scores for x3500 M3

x3500 M3 achieves outstanding processor performance for compute-intensive applications

March 16, 2010 ... IBM® today announces SPEC® CPU2006 benchmark scores for the IBM System x® 3500 M3 server using the new Intel® Xeon® 5600 Processor Series.

Configured with the Intel Xeon Processor X5680, the x3500 M3 delivered competitive scores on the SPEC CPU2006 benchmark suite. The x3500 M3 was configured with two Intel Xeon X5680 processors (3.33GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/12 cores/24 threads), 48GB of DDR3 PC3-10600R memory, and SUSE Linux® Enterprise Server 11 x64. (1)

Configured with the Intel Xeon Processor X5677, the x3500 M3 delivered competitive scores on the SPEC CPU2006 benchmark suite. The x3500 M3 was configured with two Intel Xeon X5677 processors (3.46GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/8 cores/16 threads), 48GB of DDR3 PC3-10600R memory, and SUSE Linux Enterprise Server 11 x64. (2)

The scores in the following table are the first SPEC CPU2006 results published for these x3500 M3 processor models.

SPEC CPU2006 Benchmark	IBM System x3500 M3 Six-Core Intel Xeon Processor X5680 – 3.33GHz	IBM System x3500 M3 Quad-Core Intel Xeon Processor X5677 – 3.46GHz
SPECint@2006	41.9	43.3
SPECint_base2006	39.0	40.1
SPECint_rate2006	374	299
SPECint_rate_base2006	349	282
SPECfp@2006	48.1	48.1
SPECfp_base2006	45.3	45.5
SPECfp_rate2006	253	220
SPECfp_rate_base2006	244	211

The System x3500 M3 servers are a compact 5U, two-way, SMP-capable Intel Xeon processor-based platform designed with integrated high-availability features for mainstream network server applications. These servers are highly scalable in configuration, performance, and availability. They contain additional fault tolerance through PCI-Express, and support for PCI-X. They also feature enhanced systems-management control. These flexible tower models deliver the best blend of power, manageability, expandability, and serviceability. They meet the requirements of server applications in the small-to-mainstream businesses that need an affordable general-purpose network server.

Results are current as of March 16, 2010. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at <http://www.spec.org/cpu2006/results/>.

(1) The x3500 M3 model using the Intel Xeon X5680 processor is planned to be generally available March 31, 2010. The x3500 M3 as configured for this benchmark will be available March 31, 2010.

(2) The x3500 M3 model using the Quad-Core Intel Xeon X5677 processor is planned to be generally available March 31, 2010. The x3500 M3 as configured for this benchmark will be available March 31, 2010.

IBM and System x are registered trademarks of IBM Corporation.

Intel and Xeon are registered trademarks of Intel Corporation.

Linux is a registered trademark of Linus Torvalds in the United States, other countries, or both.

SPEC, SPECfp, and SPECint are registered trademarks of the Standard Performance Evaluation Corporation (see <http://www.spec.org/spec/trademarks.html> for all SPEC trademarks and service marks).

All other company/product names and service marks may be trademarks or registered trademarks of their respective companies.