

# **Performance Brief**

# *New xSeries 335 delivers powerful performance for Web-serving applications*

## October 2003

The IBM® @server® xSeries® 335 servers are high-throughput, two-way SMP-capable Intel® Xeon<sup>TM</sup> processor-based servers that meet the challenge of doing more with less – serving more Web pages, handling more secure connections, supporting more e-mail users – while reducing cost and saving space. The x335 is designed to meet the challenge. The x335 incorporates the powerful 3.2GHz<sup>1</sup> Intel Xeon processor with 533MHz frontside bus and a 1MB L3 cache. And, with its 1U design, the x335 delivers maximum xSeries performance density in a flexible stand-alone server.

The SPECweb99\_SSL benchmark was used to measure the x335 server's performance in a configuration that used two 3.2GHz Xeon processors. The SPECweb99\_SSL<sup>2</sup> results and configuration details are summarized below.

SPECweb99_SSL - Conforming Simultaneous Connections
1,440
System Hardware
2 x 3.2GHz Xeon Processor with 1MB L3 Cache
8GB Memory
2 x 36.4GB 15K Ultra320 Disk Drives
Embedded LSI SCSI Controller
Operating System and HTTPS Software
Red Hat Linux 7.3
Zeus V4.2r2
Network Hardware
Intel PRO 1000 MT Dual-Port Server Adapter
Nortel Networks Gigabit Switch

The SPECweb99\_SSL result for the x335 and all other SPECweb99\_SSL results can be viewed at www.spec.org.

THE INFORMATION CONTAINED IN THIS DOCUMENT IS DISTRIBUTED ON AN AS IS BASIS WITHOUT ANY WARRANTY EITHER EXPRESS OR IMPLIED. The use of this information or the implementation of any of these techniques is the customer's responsibility and depends on the customer's ability to evaluate and integrate them into the customer's operational environment. While each item has been reviewed by IBM for accuracy in a specific situation, there is no guarantee that the same or similar results will be obtained elsewhere. Customers attempting to adapt these techniques to their own environment do so at their own risk.

This publication was produced in the United States. IBM may not offer the products, services, or features discussed in this document in other countries, and the information is subject to change without notice. Consult your local IBM representative for information on products and services available in your area.

Published by the IBM xSeries Server Performance Laboratory, IBM Corp.

© Copyright International Business Machines Corporation 2003. All rights reserved.

Permission is granted to reproduce this document in whole or in part, provided the copyright notice as printed above is set forth in full text at the beginning or end of each reproduced document or portion thereof.

Note to U.S. Government Users — Documentation related to restricted rights — Use, duplication or disclosure is subject to restrictions set forth in GSA ADP Schedule Contract with IBM Corp.

### Trademarks

IBM, xSeries and the eServer logo are registered trademarks of International Business Machines Corporation.

Intel and Xeon are trademarks or registered trademarks of Intel Corporation.

SPECweb99 is a trademark of Standard Performance Evaluation Corporation.

Other company, product and service names may be the trademarks or service marks of others.

#### Notes

(1) GHz only measures microprocessor internal clock speed, not application performance. Many factors affect application performance.

(2) SPECweb99\_SSL, a new benchmark released in April 2002, adds Secure Sockets Layer (SSL) Protocol support to SPECweb99, the acknowledged worldwide standard for web server performance evaluation. It tests secure Web server performance using HTTP 1.0/1.1 over the SSL Protocol. It is an extension of, rather than a replacement for, SPECweb99. SPECweb99\_SSL adopts an industry-accepted workload to measure the performance capabilities of a web server with added SSL encryption/decryption. The benchmark's metric represents the number of simultaneous connections that a secure Web server can support while meeting specific throughput and error-rate requirements.