JCOP20 Technical Brief



Overview: This document contains a simple overview about the technical capabilities of the first PK member of the JCOP card OS family. Requests for further information may be directed at <u>javacard@zurich.ibm.com</u>.

1. Basic specifications

JCOP is an IBM BlueZ implementation of the basic specifications [1] and [2] including refinements from Visa International set in the Visa OpenPlatform Card Implementation Guides (<u>http://www.visa.com/nt/suppliers/vendor</u>). All necessary clarifications from ISO7816 and EMV 2000 are also incorporated into the implementation where so required by [1] and [2].

JCOP20 is the public-key member of this family. It conforms to the VOP Card Implementation Guide 2.1.2 Compact from August 2000. It adds public key (RSA) capabilities to the ones listed in JCOP10.

Its successor, JCOP20v2.0 is compliant with the Visa OpenPlatform Card Implementation Requirements Configuration 2 with PK, Version 2.0 from February 2002.



2 Communications

2.1 Supported protocols

ISO7816 T=1 direct convention [default]¹ ISO7816 T=0 direct convention¹ ISO7816 T=1 inverse convention¹ ISO7816 T=0 inverse convention¹

2.2 Supported speeds

At the default clock rate of 3.57 MHz, the following communication speeds can be attained: 9600 bit/sec [default] 19200 bit/sec 38400 bit/sec 57600 bit/sec 115200 bit/sec

¹ The contact protocols of JCOP can be configured to support the clock-stop feature of certain terminals to save power consumption (typically done in mobile phones). This feature is available since JCOP20v2.



3 Memory availability for applications

3.1 EEPROM

3.1.1 Persistent Java heap

Used for allocating persistent objects, applets, and storage of post-issuance loaded applet code (aka packages).

Size: 7kB/15kB/31kB^{*} (without custom ROM applets).

3.1.2 Transaction buffer

Used to save data written transactional, e.g. all persistent byte and short stores, as well as persistent parameters to Util.arrayCopy; see [1].

Size: 512 bytes

3.2 RAM

3.2.1 Transient Java heap

Used for allocating transient objects and arrays of type CLEAR_ON_RESET and CLEAR_ON_DESELECT.

Size: 615 bytes

3.2.2 APDU buffer

Used to hold incoming and outgoing communications data.

Size: 261 bytes

3.2.3 Java stack

Used to hold call parameters, local variables, and stack frames of the VM. Size: 200 bytes

3.3 ROM

24kB/56kB^{*} free for applications





4 Supported optional features

Certain features listed in [1] and [2] are not defined to be mandatory. The ones implemented in JCOP are listed below.

4.1 JavaCard

4.1.1 Garbage Collection

Fully implemented: Deleted objects, applets, and packages are fully reclaimed and the space can be used for other purposes after deletion.

4.1.2 Cryptographic Algorithms

JCOP20 has the ability to generate RSA keys on the card. The following JavaCard API constants (see [1]) are implemented by this version of JCOP:

Ciphers:

ALG_DES_CBC_NOPAD

ALG_DES_CBC_ISO9797_M1

ALG_DES_CBC_ISO9797_M2

ALG_DES_ECB_NOPAD

ALG_DES_ECB_ISO9797_M1

ALG_DES_ECB_ISO9797_M2

ALG_RSA_PKCS1

ALG_RSA_NOPAD

Signatures:

ALG_DES_MAC8_NOPAD

ALG_DES_MAC8_ISO9797_M1

ALG_DES_MAC8_ISO9797_M2

ALG_RSA_SHA_ISO9796

ALG_RSA_SHA_PKCS1

ALG_RSA_MD5_PKCS1

MessageDigest:

ALG_SHA

ALG_MD5



RandomData:

ALG_SECURE_RANDOM

KeyTypes:

LENGTH_DES LENGTH_DES3_2KEY LENGTH_RSA_2048¹ (v2 only) LENGTH_RSA_1024¹ LENGTH_RSA_768¹ LENGTH_RSA_768¹ LENGTH_RSA_512¹ TYPE_DES_TRANSIENT_RESET TYPE_DES_TRANSIENT_DESELECT TYPE_DES TYPE_RSA_PUBLIC TYPE_RSA_PRIVATE² TYPE_RSA_CRT_PRIVATE

KeyPair:

ALG_RSA_CRT

4.2 OpenPlatform

4.2.1 Global PIN

Fully implemented: All described APDU and API interfaces for this feature are present.

² Private Keys must be loaded with key material. On-card key generation is only supported for RSA keys in CRT format



¹ All multiples of 32 (bit) are supported as valid RSA key lengths. Thus, key length values such as 736 (bits) can be passed as parameters to the respective functions.

5 Supported Hardware

The identical JCOP mask can run on these platforms and automatically make use of their resources, i.e., create a bigger persistent Java heap, and make more ROM memory available for ROMed applets^{*}.

5.1 Philips P8WE5017

64 kB ROM → 24kB free for ROM'd applets in Custom Mask Process

16 kB EEPROM

2300 Bytes RAM

Triple-DES coprocessor

FameX RSA coprocessor

5.2 Philips P8WE5033

96 kB ROM \rightarrow 56kB free for ROM'd applets in Custom Mask Process

32 kB EEPROM

2300 Bytes RAM

Triple-DES coprocessor

FameX RSA coprocessor

5.3 Philips P8WE5009

96 kB ROM → 56kB free for ROM'd applets in Custom Mask Process 8 kB EEPROM 2300 Bytes RAM Triple-DES coprocessor FameX RSA coprocessor

Custom applets may be submitted to Philips for inclusion into a ROM mask. The maximum package size is 16kB. The Custom Mask process is only available starting with JCOP20v2.



6 Performance figures

In the absence of standard performance tests, typical applet's operations are timed. The protocol used is T=1 at 9600 bit/sec. The reader clocks the chip at 3.71 MHz. The applets are the Visa approved versions after having been initialized and populated with keys as required for Visa VTF testing. To avoid measuring communications overhead, timing is measured between the last APDU byte sent to the reader and the first byte returned from the card.

Operation	ETUs	msec
SELECT CardManager	145	13.5
INIT UPDATE CardManager	469	45.0
EXTERNAL AUTH CardManager	302	28.1
Install VisaCash	4928	458.3
SELECT VisaCash [*]	154	14.3
Initialize LOAD for VisaCash	769	71.5
Perform LOAD for VisaCash	1453	135.1
Initialize PURCHASE for VisaCash	207	19.3
Perform PURCHASE for VisaCash	1479	137.6
ReadBalance from VisaCash	91	8.5
SELECT VSDC [*]	549	51.1
GenerateAC from VSDC	2282	212.3

The PK operations are largely dominated by the hardware speed of the FameX. Note that on-card key generation is a random-based process; thus the figure given is only an average value. Values are measured at low-level; depending on Java programming skills, application-level code can add some time to the values depicted here.

Operation	msec
Generate 1024bit CRT key	~ 5900
1024 bit CRT public key operation (F4)	50
1024 bit CRT private key operation	250

* Not first SELECT to eliminate potential applet setup effects

A Revision History

- 0.9 Preliminary version (prior to Visa approval)
- 0.91 AIDs or ROMed applets added
- 0.92 References to external specifications added
- 0.93 Incorrect reference to PseudoRandom constant deleted (4.1.2)
- 1.0 Document reformatted
- 1.1 Timing explanations added (6); correction of size information (3.3, 5.1)
- 1.2 Information on supported RSA key lengths added (4.1.2); Information an additional platform support added (3.1, 5.3)
- Specification update to cover JCOP20v2 (1, 4.1.2); correction of RAM heap size (3.2.1); added previously omitted KeyPair constant (4.1.2)
- 2.1 Specification correction (5.3); Custom Mask/Free ROM applet size clarified (5)
- 2.2 Specification amendment stating explicitly that RSA private keys are supported (4.1.2)
- 2.3 Added comment on clock-stop feature (2.1)

B References

- [1] Sun Microsystems: JavaCard 2.1.1 http://java.sun.com/products/javacard
- [2] Global Platform Consortium: OpenPlatform 2.0.1' http://www.globalplatform.org/