

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				2 **** 3 * 4 * CMPSC -- Compression Call instruction test 5 * 6 **** 7 * 8 * PLEASE NOTE that this test only performs the simplest most basic 9 * test of the CMPSC instruction. It does NOT test all aspects of 10 * the instruction. We have a separate much more thorough and much 11 * longer running test program for that. This test only compresses 12 * a small amount of data and then expands it and verifies that what 13 * was expanded matches what it started with. It does NOT check for 14 * a valid condition code or anything else. It's designed to be run 15 * as part of "make test" after a build of Hercules and thus cannot 16 * run for a very long time (and besides, as I said, we already have 17 * a separate offline program that does a much better job of that). 18 * 19 * 20 * -- Sample runtest script -- 21 * 22 * 23 * *Testcase CMPSC (Compression Call) 24 * mainsize 2 25 * numcpu 1 26 * sysclear 27 * archlvl z/Arch 28 * loadcore "\$(testpath)/CMPSC.core" 29 * runtest 1 30 * *Done 31 * 32 * 33 ****

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
		35		PRINT OFF
		3416		PRINT ON
		3417		PRINT DATA
		3419	*****	*****
		3420	*	SATK prolog stuff...
		3421	*****	*****
		3423	ARCHLVL	MNOTE=NO
		3425+\$AL	OPSYN	AL
		3426+\$ALR	OPSYN	ALR
		3427+\$B	OPSYN	B
		3428+\$BAS	OPSYN	BAS
		3429+\$BASR	OPSYN	BASR
		3430+\$BC	OPSYN	BC
		3431+\$BCTR	OPSYN	BCTR
		3432+\$BE	OPSYN	BE
		3433+\$BH	OPSYN	BH
		3434+\$BL	OPSYN	BL
		3435+\$BM	OPSYN	BM
		3436+\$BNE	OPSYN	BNE
		3437+\$BNH	OPSYN	BNH
		3438+\$BNL	OPSYN	BNL
		3439+\$BNM	OPSYN	BNM
		3440+\$BNO	OPSYN	BNO
		3441+\$BNP	OPSYN	BNP
		3442+\$BNZ	OPSYN	BNZ
		3443+\$BO	OPSYN	BO
		3444+\$BP	OPSYN	BP
		3445+\$BXLE	OPSYN	BXLE
		3446+\$BZ	OPSYN	BZ
		3447+\$CH	OPSYN	CH
		3448+\$L	OPSYN	L
		3449+\$LH	OPSYN	LH
		3450+\$LM	OPSYN	LM
		3451+\$LPSW	OPSYN	LPSW
		3452+\$LR	OPSYN	LR
		3453+\$LTR	OPSYN	LTR
		3454+\$NR	OPSYN	NR
		3455+\$SL	OPSYN	SL
		3456+\$SLR	OPSYN	SLR
		3457+\$SR	OPSYN	SR
		3458+\$ST	OPSYN	ST
		3459+\$STM	OPSYN	STM
		3460+\$X	OPSYN	X
		3461+\$AHI	OPSYN	AHI
		3462+\$B	OPSYN	J
		3463+\$BC	OPSYN	BRC
		3464+\$BE	OPSYN	JE
		3465+\$BH	OPSYN	JH
		3466+\$BL	OPSYN	JL
		3467+\$BM	OPSYN	JM

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
		3468+\$BNE	OPSYN	JNE
		3469+\$BNH	OPSYN	JNH
		3470+\$BNL	OPSYN	JNL
		3471+\$BNM	OPSYN	JNM
		3472+\$BNO	OPSYN	JNO
		3473+\$BNP	OPSYN	JNP
		3474+\$BNZ	OPSYN	JNZ
		3475+\$BO	OPSYN	JO
		3476+\$BP	OPSYN	JP
		3477+\$BXLE	OPSYN	JXLE
		3478+\$BZ	OPSYN	JZ
		3479+\$CHI	OPSYN	CHI
		3480+\$AHI	OPSYN	AGHI
		3481+\$AL	OPSYN	ALG
		3482+\$ALR	OPSYN	ALGR
		3483+\$BCTR	OPSYN	BCTGR
		3484+\$BXLE	OPSYN	JXLEG
		3485+\$CH	OPSYN	CGH
		3486+\$CHI	OPSYN	CGHI
		3487+\$L	OPSYN	LG
		3488+\$LH	OPSYN	LGH
		3489+\$LM	OPSYN	LMG
		3490+\$LPSW	OPSYN	LPSWE
		3491+\$LR	OPSYN	LGR
		3492+\$LTR	OPSYN	LTGR
		3493+\$NR	OPSYN	NGR
		3494+\$SL	OPSYN	SLG
		3495+\$SLR	OPSYN	SLGR
		3496+\$SR	OPSYN	SGR
		3497+\$ST	OPSYN	STG
		3498+\$STM	OPSYN	STMG
		3499+\$X	OPSYN	XG

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				3501 ****	*****
				3502 * Initiate the CMPSC CSECT in the CODE region	
				3503 * with the location counter at 0	
				3504 ****	*****
00000000	00020000 00000000	00000000	0003FFFF	3506 CMPSC ASALOAD REGION=CODE	
00000008	00000000 00000008			3507+CMPSC START 0,CODE	
00000010		00000010	00000058	3509+ PSW 0,0,2,0,X'008'	64-bit Restart ISR Trap New PSW
00000058	00020000 00000000			3510+ ORG CMPSC+X'058'	
00000060	00000000 00000018			3512+ PSW 0,0,2,0,X'018'	64-bit External ISR Trap New PSW
00000068	00020000 00000000			3513+ PSW 0,0,2,0,X'020'	64-bit Supervisor Call ISR Trap New PSW
00000070	00000000 00000020			3514+ PSW 0,0,2,0,X'028'	64-bit Program ISR Trap New PSW
00000078	00020000 00000000			3515+ PSW 0,0,2,0,X'030'	64-bit Machine Check Trap New PSW
00000080	00000000 00000028			3516+ PSW 0,0,2,0,X'038'	64-bit Input/Output Trap New PSW
00000088	00020000 00000000				
00000090	00000000 00000030				
00000098	00020000 00000000				
000000A0	00000000 00000038				
000000A8		000000A8	000001A0	3517+ ORG CMPSC+X'1A0'	
000001A0	00020000 00000000			3519+ PSWZ 0,0,2,0,X'120'	Restart ISR Trap New PSW
000001A8	00000000 00000120				
000001B0	00020000 00000000			3520+ PSWZ 0,0,2,0,X'130'	External ISR Trap New PSW
000001B8	00000000 00000130				
000001C0	00020000 00000000			3521+ PSWZ 0,0,2,0,X'140'	Supervisor Call ISR Trap New PSW
000001C8	00000000 00000140				
000001D0	00020000 00000000			3522+ PSWZ 0,0,2,0,X'150'	Program ISR Trap New PSW
000001D8	00000000 00000150				
000001E0	00020000 00000000			3523+ PSWZ 0,0,2,0,X'160'	Machine Check Trap New PSW
000001E8	00000000 00000160				
000001F0	00020000 00000000			3524+ PSWZ 0,0,2,0,X'170'	Input/Output Trap New PSW
000001F8	00000000 00000170				

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3526 **** 3527 * Define the z/Arch RESTART PSW 3528 ****
00000200		00000200	00000001	3530 PREVORG EQU *
		00000200	000001A0	3531 ORG CMPSC+X'1A0' 3532 * PSWZ <sys>,<key>,<mwp>,<prog>,<addr>[,amode] 3533 PSWZ 0,0,0,0,X'200',64
000001A0	00000001 80000000			
000001A8	00000000 00000200			
000001B0		000001B0	00000200	3534 ORG PREVORG
				3536 **** 3537 * Create IPL (restart) PSW 3538 ****
00000200		00000000	0003FFFF	3540 ASA IPL IA-BEGIN 3541+CMPSC CSECT 00000200 00000000 3542+ ORG CMPSC
00000000	00080000 00000200			3543+ PSWE390 0,0,0,0,BEGIN,24
00000008		00000008	00000200	3544+ ORG CMPSC+512 Reset CSECT to end of assigned storage area 00000000 0003FFFF 3545+CMPSC CSECT

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				3547 ****	*****
				3548 *	The actual CMPSC program itself...
				3549 ****	*****
00000200		00000000		3551 USING CMPSC,R0	No base registers needed
00000200				3553 BEGIN DS 0H	
				3554 *	
				3555 ** COMPRESS the data...	
				3556 *	
00000200	E300 02D0 0004		000002D0	3557 LG R0,CMP_R0	R0 <= Compress
00000206	E310 02E0 0004		000002E0	3558 LG R1,CMP_R1	R1 <= Compress
0000020C	E320 02A0 0004		000002A0	3559 LG R2,=AD(CMPADDR)	R2 --> Compression buffer
00000212	E330 02A8 0004		000002A8	3560 LG R3,=AD(1024)	R3 <= Compression buffer size
00000218	E340 02B0 0004		000002B0	3561 LG R4,=AD(INADDR)	R4 --> Input data
0000021E	E350 02B8 0004		000002B8	3562 LG R5,=AD(INSIZE)	R5 <= Input size
00000224	B263 0024			3563 CMPSC R2,R4	Compress data
				3564 *	
				3565 ** Calculate length of compressed data	
				3566 *	
00000228	E360 02A8 0004		000002A8	3567 LG R6,=AD(1024)	R6 <= Original R3 value
0000022E	B909 0063			3568 SGR R6,R3	Subtract ending R3 value
00000232	E360 02C0 0008		000002C0	3569 AG R6,=AD(1)	Plus +1 to get true length
				3570 *	
				3571 ** EXPAND what we compressed...	
				3572 *	
00000238	E300 02D8 0004		000002D8	3573 LG R0,EXP_R0	R0 <= Expand
0000023E	E310 02E8 0004		000002E8	3574 LG R1,EXP_R1	R0 <= Expand
00000244	E320 02C8 0004		000002C8	3575 LG R2,=AD(EXPADDR)	R2 --> Expansion buffer
0000024A	E330 02A8 0004		000002A8	3576 LG R3,=AD(1024)	R3 <= Expansion vuffer size
00000250	E340 02A0 0004		000002A0	3577 LG R4,=AD(CMPADDR)	R4 --> Input data
00000256	B904 0056			3578 LGR R5,R6	R5 <= Input size
0000025A	B263 0024			3579 CMPSC R2,R4	Expand data
				3580 *	
				3581 ** VERIFY it matches original input data...	
				3582 *	
0000025E	E320 02B0 0004		000002B0	3583 LG R2,=AD(INADDR)	R2 --> Original input data
00000264	E330 02B8 0004		000002B8	3584 LG R3,=AD(INSIZE)	R3 <= Original input size
0000026A	E340 02C8 0004		000002C8	3585 LG R4,=AD(EXPADDR)	R4 --> Expanded data
00000270	E350 02B8 0004		000002B8	3586 LG R5,=AD(INSIZE)	R5 <= R3 (same size)
00000276	0F24			3587 CLCL R2,R4	Compare expanded data with original
00000278	4780 0280		00000280	3588 BE GOODEOJ	If it's identical then all is well
0000027C	47F0 0290		00000290	3589 B FAILEOJ	Otherwise something is VERY WRONG!

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				3591 **** 3592 * 3593 ****	PSWs
00000280	92FF 0500	00000500	3595 GOODEOJ	MVI TESTFLAG,X'FF'	Indicate test SUCCESS
00000284	8200 0288	00000288	3596 DWAITEND	LOAD=YES	PSW 00000000... Test SUCCESS
00000288	000A0000 00000000		3598+ LPSW DWAT0009		
			3599+DWAT0009	PSWE390 0,0,2,0,X'000000'	
					10BAD: One of the tests FAILED!
00000290			3601 FAILEOJ	DWAIT LOAD=YES,CODE=BAD	
00000290	8200 0298	00000298	3602+FAILEOJ	DS 0H	
00000298	000A0000 00010BAD		3603+ LPSW DWAT0010		
			3604+DWAT0010	PSWE390 0,0,2,0,X'010BAD'	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
				3606 ****	*****	*****
				3607 *	Working Storage	
				3608 ****	*****	*****
000002A0				3610 LTORG ,	Literals pool	
000002A0	00000000 00002000			3611 =AD(CMPADDR)		
000002A8	00000000 00000400			3612 =AD(1024)		
000002B0	00000000 00001000			3613 =AD(INADDR)		
000002B8	00000000 00000140			3614 =AD(INSIZE)		
000002C0	00000000 00000001			3615 =AD(1)		
000002C8	00000000 00003000			3616 =AD(EXPADDR)		
				3618 FLAGADDR EQU X'500'	Fixed address of test results flag	
				3619		
				3620 INADDR EQU X'1000'	Address of input data	
				3621 CMPADDR EQU X'2000'	Address of compression buffer	
				3622 EXPADDR EQU X'3000'	Address of expansion buffer	
				3623 CDICTADR EQU X'20000'	Address of 64K compression dictionary	
				3624 EDICTADR EQU X'30000'	Address of 64K expansion dictionary	
000002D0				3626 DC 0D'0'	(alignment)	
000002D0	00000000 00005200			3627 CMP_R0 DC XL8'0000000000005200'	R0 Compression options	
000002D8	00000000 00005300			3628 EXP_R0 DC XL8'0000000000005300'	R0 Expansion options	
000002E0	00000000 00020000			3629 CMP_R1 DC AD(CDICTADR)	R1 addr Compression dictionary	
000002E8	00000000 00030000			3630 EXP_R1 DC AD(EDICTADR)	R1 addr Expansion dictionary	
000002F0		000002F0 00000500		3632 ORG CMPSC+FLAGADDR	Fixed address of result flag	
00000500 00				3634 TESTFLAG DC X'00'	Failing test number or X'FF' = good	

7791 EN

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
BEGIN	H	000200	2	3553	3543
CDICTADR	U	020000	1	3623	3659 3629
CDICTSIZ	U	010000	1	5712	
CMPADDR	U	002000	1	3621	3559
CMPDICT	U	020000	1	3661	5712
CMPSC	J	000000	262144	3507	3510 3517 3531 3542 3544 3632 3640 3659 5718 3551
CMP_R0	X	0002D0	8	3627	3557
CMP_R1	A	0002E0	8	3629	3558
CODE	2	000000	262144	3507	
DWAT0009	3	000288	8	3599	3598
DWAT0010	3	000298	8	3604	3603
EDICTADR	U	030000	1	3624	5718 3630
EDICTSIZ	U	010000	1	7771	
EXPADDR	U	003000	1	3622	3575
EXPDICT	U	030000	1	5720	7771
EXP_R0	X	0002D8	8	3628	3573
EXP_R1	A	0002E8	8	3630	3574
FAILEOJ	H	000290	2	3602	3589
FLAGADDR	U	000500	1	3618	3632
GOODEOJ	I	000280	4	3595	3588
IMAGE	1	000000	262144	0	
INADDR	U	001000	1	3620	3640 3561
INFILE	U	001000	1	3642	3653
INSIZE	U	000140	1	3653	3562
PREVORG	U	000200	1	3530	3534
R0	U	000000	1	7774	3551 3557 3573
R1	U	000001	1	7775	3558 3574
R10	U	00000A	1	7784	
R11	U	00000B	1	7785	
R12	U	00000C	1	7786	
R13	U	00000D	1	7787	
R14	U	00000E	1	7788	
R15	U	00000F	1	7789	
R2	U	000002	1	7776	3559 3563 3575 3579 3583 3587
R3	U	000003	1	7777	3560 3568 3576 3584
R4	U	000004	1	7778	3561 3563 3577 3579 3585 3587
R5	U	000005	1	7779	3562 3578 3586
R6	U	000006	1	7780	3567 3568 3569 3578
R7	U	000007	1	7781	
R8	U	000008	1	7782	
R9	U	000009	1	7783	
TESTFLAG	X	000500	1	3634	3595
=AD(1)	A	0002C0	8	3615	3569
=AD(1024)	A	0002A8	8	3612	3560 3567 3576
=AD(CMPADDR)	A	0002A0	8	3611	3559 3577
=AD(EXPADDR)	A	0002C8	8	3616	3575 3585
=AD(INADDR)	A	0002B0	8	3613	3561 3583
=AD(INSIZE)	A	0002B8	8	3614	3562 3584 3586

MACRO	DEFN	REFERENCES
ANTR	101	
APROB	233	
ARCHIND	393	3424
ARCHLVL	534	3423
ASA IPL	660	3540
ASALOAD	740	3506
ASAREA	795	
ASAZAREA	980	
CPUWAIT	1063	
DSECTS	1389	
DWAIT	1592	3597 3601
DWAITEND	1649	3596
ENADEV	1657	
ESA390	1757	
IOCB	1768	
IOC BDS	1944	
IOFMT	1978	
IOINIT	2316	
IOTRFR	2357	
ORB	2405	
POINTER	2594	
PSWFMT	2622	
RAWAIT	2756	
RAWIO	2852	
SIGCPU	3010	
SMMGR	3068	
SMMGRB	3168	
TRAP128	3217	3518
TRAP64	3194	3508 3511
TRAPS	3230	
ZARCH	3304	
ZEROH	3316	
ZEROL	3344	
ZEROLH	3372	
ZEROLL	3395	

DESC	SYMBOL	SIZE	POS	ADDR
------	--------	------	-----	------

Entry: 0

Image	IMAGE	262144	00000-3FFFF	00000-3FFFF
Region	CODE	262144	00000-3FFFF	00000-3FFFF
CSECT	CMPSC	262144	00000-3FFFF	00000-3FFFF

STMT	FILE NAME
1	c:\Users\Fish\Documents\Visual Studio 2008\Projects\MyProjects\ASMA-0\CMSPC\CMSPC.asm
2	C:\Users\Fish\Documents\Visual Studio 2008\Projects\Hercules_Git_Harold\SATK-0\srcasm\satk.mac

** NO ERRORS FOUND **